

PUB4314 (PU4314)

Silicon NPN/PNP planar type

For low-voltage switching

■ Features

- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Satisfactory linearity of forward current transfer ratio h_{FE}
- High-speed switching
- NPN 2 elements + PNP 2 elements

■ Absolute Maximum Ratings $T_C = 25^\circ C$

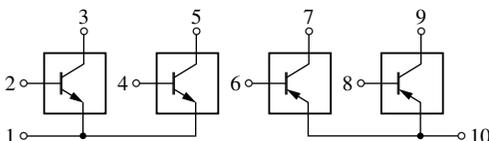
Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	± 40	V
Collector-emitter voltage (Base open)	V_{CEO}	± 20	V
Emitter-base voltage (Collector open)	V_{EBO}	± 5	V
Collector current	I_C	± 7	A
Peak collector current	I_{CP}	± 12	A
Collector power dissipation	P_C	15	W
		$T_a = 25^\circ C$	
Junction temperature	T_j	150	$^\circ C$
Storage temperature	T_{stg}	-55 to +150	$^\circ C$

■ Electrical Characteristics $T_C = 25^\circ C \pm 3^\circ C$

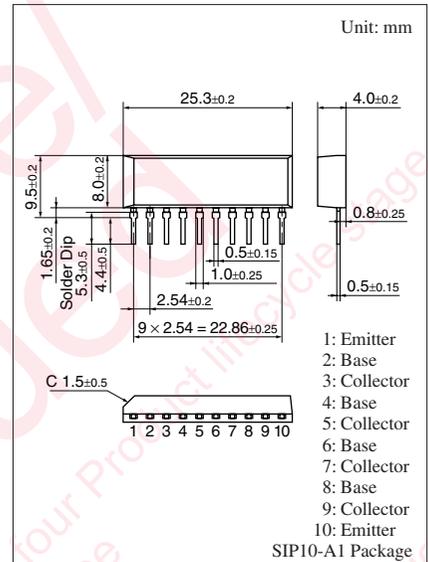
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = \pm 10 \text{ mA}, I_B = 0$	± 20			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = \pm 40 \text{ V}, I_E = 0$			± 50	μA
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = \pm 5 \text{ V}, I_C = 0$			± 50	μA
Forward current transfer ratio	h_{FE1}	$V_{CE} = \pm 2 \text{ V}, I_C = \pm 0.1 \text{ A}$	45			—
	h_{FE2}	$V_{CE} = \pm 2 \text{ V}, I_C = \pm 2 \text{ A}$	60		260	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = \pm 5 \text{ A}, I_B = \pm 0.16 \text{ A}$			± 0.6	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = \pm 5 \text{ A}, I_B = \pm 0.16 \text{ A}$			± 1.5	V
Transition frequency	f_T	$V_{CE} = \pm 10 \text{ V}, I_C = \pm 0.5 \text{ A}, f = 10 \text{ MHz}$		150		MHz
Turn-on time	NPN	t_{on}	$I_C = \pm 2 \text{ A}$ $I_{B1} = \pm 66 \text{ mA}, I_{B2} = \pm 66 \text{ mA}$		0.3	μs
					0.1	
Storage time	NPN	t_{stg}	$V_{CC} = \pm 20 \text{ V}$		0.3	μs
					0.5	
Fall time	NPN	t_f			0.1	μs
					0.1	

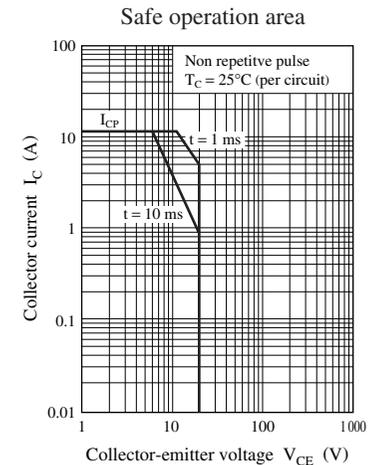
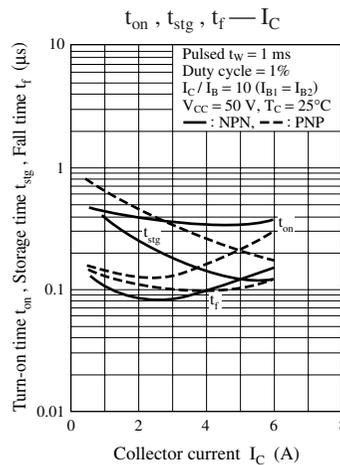
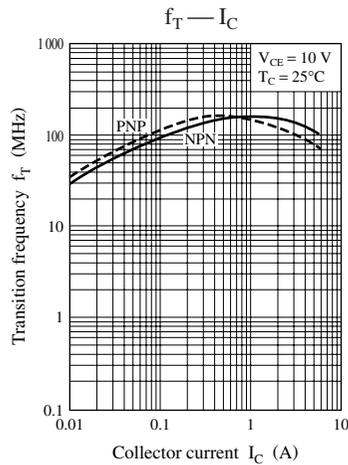
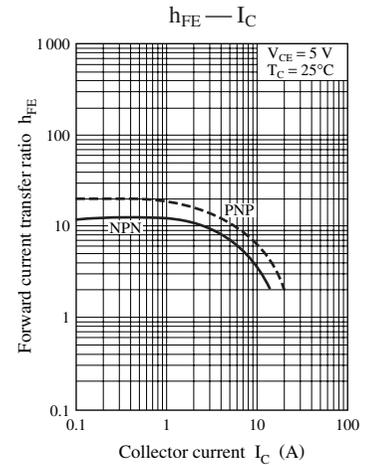
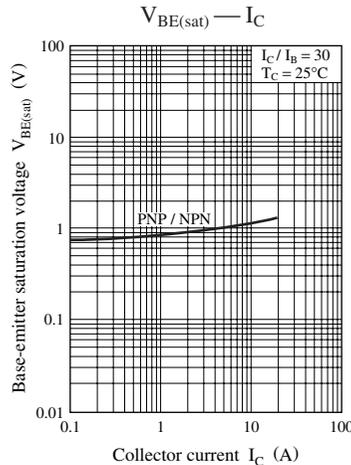
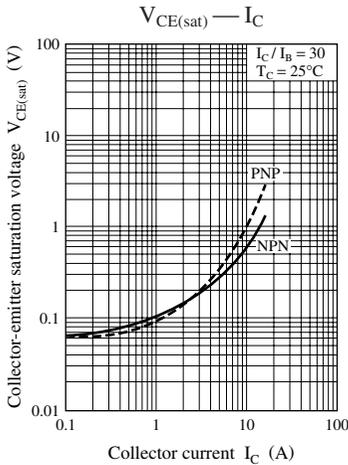
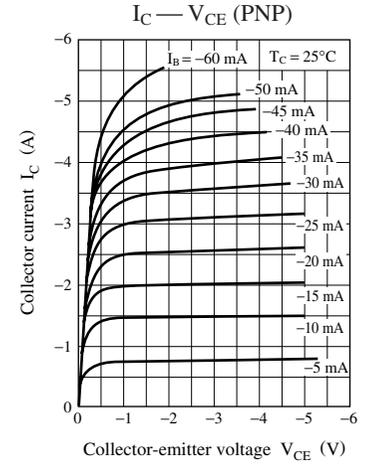
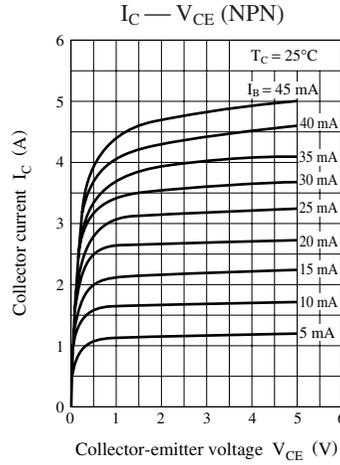
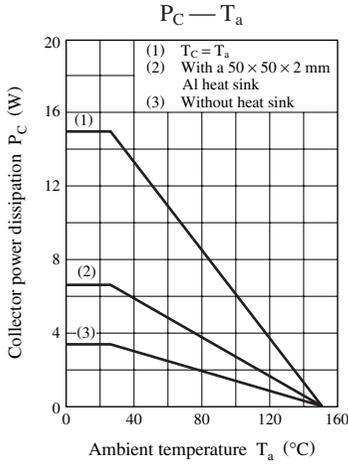
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

■ Internal Connection



Note) The part number in the parenthesis shows conventional part number.





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