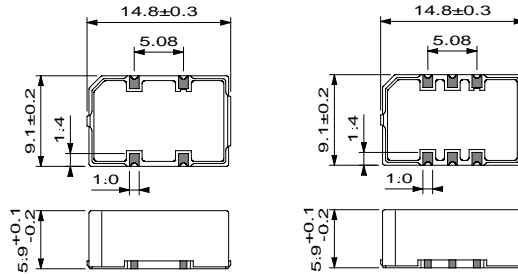
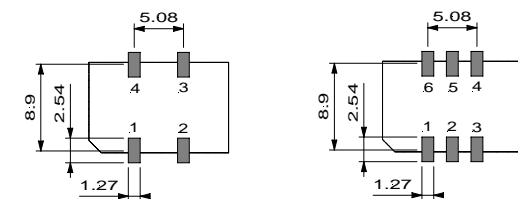


LOW PHASE NOISE SURFACE MOUNT VCXO DFV S1-OLH/OLHZ (3.3 V) & DFV S1-OH/OHZ (5 V)

KEY FEATURES
50 to 125 MHz
Low phase noise
APPLICATIONS
Mobile communications



Function	DFV S1-OH/LH	DFV S1-OHZ/OLHZ
V control	1	1
Enable		2
GND	2	3
Output	3	4
NC		5
Vcc	4	6



PC board footprint

TYPE	DFV S1-OLH/OLHZ	DFV S1-OH/OHZ
Frequency Range	50 to 125 MHz	50 to 125 MHz

ELECTRICAL SPECIFICATIONS	DFV S1-OLH/OLHZ	DFV S1-OH/OHZ
supply voltage	3.3 V ± 5 %	5 V ± 5 %
supply current (no load)	≤ 40 mA	≤ 50 mA
output load (HCMOS)	15 pF	15 pF
duty cycle	40/60...60/40 % @ 50% level	40/60...60/40 % @ 50% level
rise/fall times (@ 15 pF load)	10 to 90 % : ≤ 3 ns	10 to 90 % : ≤ 3 ns
high/low levels	≥ 2.8 V / ≤ 0.3 V	≥ 4.5 V / ≤ 0.5 V
start up	≤ 10 ms @ 3.15 V	≤ 10 ms @ 4.75 V
SSB phase noise (1 Hz B.W.) (Typical values at 77.76 MHz)	-75 dBc/Hz @ 10 Hz -105 dBc/Hz @ 100 Hz -125 dBc/Hz @ 1 kHz -145 dBc/Hz @ 10 kHz -150 dBc/Hz @ 100 kHz	-75 dBc/Hz @ 10 Hz -105 dBc/Hz @ 100 Hz -125 dBc/Hz @ 1 kHz -145 dBc/Hz @ 10 kHz -150 dBc/Hz @ 100 kHz

FREQUENCY STABILITY			detailed tolerances [ppm]						
type	temperature range	model code	stability versus:				pulling range positive function	control voltage	
			temp.	@ 25°C	Vcc	load			ageing
DFV S1-OL	0 to 70°C	40B15	≤ ± 15	≤ ± 10	≤ ± 3	≤ ± 0.5	≤ ± 2	≥ ± 40	1.65 V ± 1.35 V
	-40 to 85°C	40B25	≤ ± 25						
		40E25	≤ ± 25						
DFV S1-O	0 to 70°C	50B15	≤ ± 15	≤ ± 10	≤ ± 3	≤ ± 0.5	≤ ± 2	≥ ± 50	2.5 V ± 2.0 V
	-40 to 85°C	50B25	≤ ± 25						
		50E25	≤ ± 25						
remarks	input impedance ≥ 10 kΩ, modulation bandwidth ≥ 10 kHz @ -3dB ageing is 1 st year at 25°C								

OPTIONS	CODE	
tri-state control on pin 2	Z	high or open = enable, low = high Z (6 pads package)

ORDERING CODE	type + option code + frequency + model code
Example	DFV S1-OLH 76.8 MHz 40B15