

CMOS SMD $7 \times 5 \times 1.8$ mm, 6 pad

50.01MHz ~ 640.0MHz

- Frequency range 50.01MHz to 640MHz
- **LVCMOS Output**
- **Supply Voltage 3.3 VDC**
- High Q fundamental crystal
- Ultra low jitter less than 1ps





DESCRIPTION

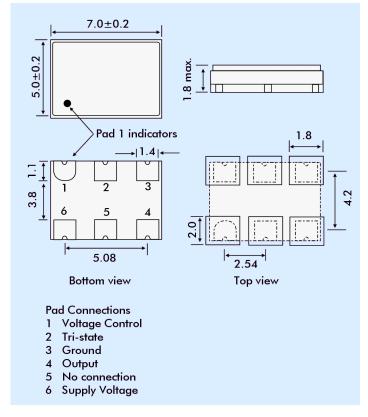
GF576 VCXOs, are packaged in an industry-standard, 6 pad, 7mm x 5mm SMD package. The VCXOs provide good phase jitter performance, less than 1ps.

SPECIFICATION

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Frequency Range	50.01MHz to 640.0MHz
Supply Voltage:	3.3 VDC ±5%
Output Logic:	LVCMOS
Integrated Phase Jitter:	0.4ps typical, 0.5ps maximum (for 156.250MHz)
Period Jitter RMS:	3.0ps typical (for 156.250MHz)
Period Jitter Peak to peak:	20ps typical (for 156.250MHz)
Phase Noise:	See table below
Initial Frequency Accuracy:	Tune to the nominal frequency with Vc= 1.65 ±0.2VDC
Output Voltage HIGH (1):	90% Vdd minimum
Output Voltage LOW (0):	10% Vdd maximum
Frequency Deviation Range:	±80ppm minimum
Temperature Stability:	See table
Output Load:	15p F
Start-up Time:	10ms maximum, 5ms typical
Duty Cycle:	50% ±5% measured at 50% Vdd
Rise/Fall Times:	0.7ns typical (15pF load)
Current Consumption	
<100MHz:	30mA maximum (15pF load)
>100MHz:	40mA maximum (15pF load)
Linearity:	10% maximum, 6% typical
Modulation Bandwidth:	25kHz minimum
Input Impedance:	60 k Ω minimum
Slope Polarity:	Monotonic and Positive. (An
(Transfer function)	increase of control voltage
	increases output frequency.)
Storage Temperature:	-50° to +100°C
Ageing:	±3ppm per year first year
	±2ppm per year thereafter
Enable/Disable (Tristate):	Pad 2, Enable high or 70% Vdd min. applied to Tri-state pad to enable output. 30% Vdd max. to disable output
	(high impedance)

Fully compliant

OUTLINE & DIMENSIONS



PHASE NOISE

Offset	Frequency 155.25MHz
10Hz	-62dBc/Hz
100Hz	-92dBc/Hz
1kHz	-120dBc/Hz
10kHz	-132dBc/Hz
100kHz	-128dBc/Hz
1MHz	-140dBc/Hz
10MHz	-150dBc/Hz

FREQUENCY STABILITY

RoHS Status:

Stability Code	Stability ±ppm	Temp. Range
Α	25	0°∼+70°C
В	50	0°∼+70°C
С	100	0°∼+70°C
D	25	-40°∼+85°C
Е	50	-40°∼+85°C
F	100	-40°~+85°C

If non-standard frequency stability is required Use 'I' followed by stability, i.e. I20 for ±20ppm over 'Industrial' temperature range -40~+85°C

PART NUMBER SCHEDULE

