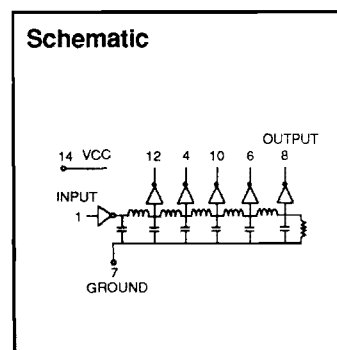


14 Pin DIL 5 Tap High Speed CMOS (HCT) Compatible Active Delay Lines

Tap Delays ±5% or ±2 nS	Total Delay ±5% or ±2 nS	Part Number	Tap Delays ±5% or ±2 nS	Total Delay ±5% or ±2 nS	Part Number
12* 17 22 27	32	EPA220-32	20 40 60 80	100	EPA220-100
12* 18 24 30	36	EPA220-36	25 50 75 100	125	EPA220-125
12* 19 26 33	40	EPA220-40	30 60 90 120	150	EPA220-150
12* 20 28 36	44	EPA220-44	35 70 105 140	175	EPA220-175
12* 21 30 39	48	EPA220-48	40 80 120 160	200	EPA220-200
12* 22 32 42	52	EPA220-52	50 100 150 200	250	EPA220-250
12* 24 36 48	60	EPA220-60	60 120 180 240	300	EPA220-300
15 30 45 60	75	EPA220-75	70 140 210 280	350	EPA220-350
19 38 57 76	95	EPA220-95			

* Inherent Delay • Delay times referenced from input to leading edges at 25°C, 5.0V.

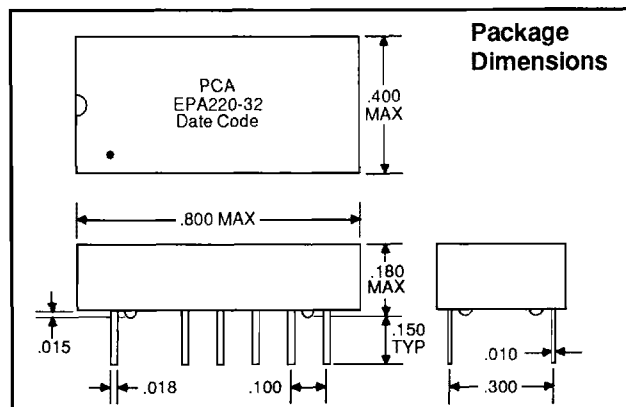
DC Electrical Characteristics Parameter	Test Conditions	Min	Max	Unit
V _{IH}	High Level Input Voltage	2.0		Volt
V _{IL}	Low Level Input Voltage		0.8	Volt
V _{OH}	High Level Output Voltage	4.0		Volt
V _{OL}	Low Level Output Voltage		0.3	Volt
I _L	Input Leakage Current		±1.0	uA
I _{CC}	Supply Current		15	mA
T _{RO}	Output Rise Time		4	nS
N _H	High Fanout	10		LSTTL Load



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	DC Supply Voltage	4.5	5.5	Volt
V _I	DC Input Voltage Range	0	V _{CC}	Volt
V _O	DC Output Voltage Range	0	V _{CC}	Volt
I _O	DC Output Source/Sink Current		25	mA
PW*	Pulse Width % of Total Delay	40		%
D*	Duty Cycle		40	%
T _A	Operating Free Air Temperature	0	70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25°C (Taps Unloaded)		
E _{IN}	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	150%
T _{RI}	Input Rise Time (0.75 - 2.4 Volts)	2.0 nS
P _{RR}	Pulse Repetition Rate @ T _d ≤ 200nS	1.0 MHz
	Pulse Repetition Rate @ T _d > 200nS	300 KHz
V _{CC}	Supply Voltage	5.0 Volts



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