### XO5160 - XO5164 Series

# 14 pin DIP, 3.3, 5.0 or 12.0 Volt, HCMOS/TTL/Sinewave, OCXO







- Standard DIP/DIL package offering tight stabilities, fast warm-up, and low current
- Ideal for PCS base stations, cellular base stations, phase locking, and SAR/SAT applications
- SMT Surfboard Option

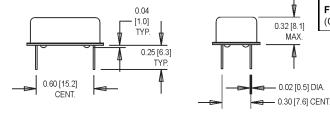
#### **Ordering Information** 00-0000 XO51xx V5 ח MHz **Product Series** XO5160 = 5V HCMOS/TTL XO5161 = 12V HCMOS/TTL XO5162 = 5V Sinewave XO5163 = 12V Sinewave XO5164 = 3.3V HCMOS **Operating Temperature A** = 0°C to +60°C $B = -20^{\circ}C \text{ to } +70^{\circ}C$ $C = -40^{\circ}C \text{ to } +85^{\circ}C$ Frequency Stability **H:** ±0.05 ppm (0.10 ppm pk-pk) **G:** ±0.075 ppm (0.15 ppm pk-pk) **A:** ±0.1 ppm (0.20 ppm pk-pk) **B:** ±0.15 ppm (0.30 ppm pk-pk) **C:** ±0.2 ppm (0.40 ppm pk-pk) **D:** ±0.25 ppm (0.50 ppm pk-pk) **E:** ±0.3 ppm (0.60 ppm pk-pk) **F:** ±0.5 ppm (1.00 ppm pk-pk) Frequency Adjustment R1 = Internal voltage with external potentiometer (Fig. 1) V5 = External voltage with external potentiometer (Fig. 2) Package Configuration **D**: 14 pin DIP S: Surfboard RoHS Compliance Blank: non RoHS compliant part -R: RoHS compliant part Frequency of Operation (Customer Specified)

### **PTH Package D**

0.80 [20.2] MAX.

0.42

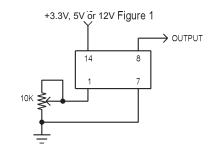
STANDOFFS, TYP.

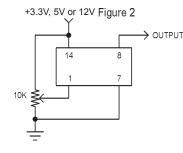


All dimensions

in inches [mm].

M7003Sxxx, M7006Sxxx, M7007Sxxx, M7008Sxxx & M7009Sxxx - Contact factory for datasheets.

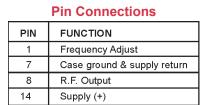


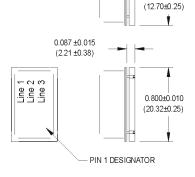


# 1 CENT. 7 0.52 0.22 [5.5] [13.2] 0.04 [1.0] DIA. 8

# SMTPackage S

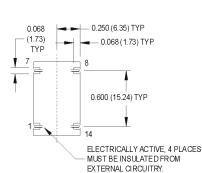
All dimensions in inches (mm).

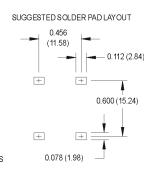




0.32 (8.1)

MAX





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0.500±0.010

# XO5160 - XO5164 Series





	Parameter		Symbol	Min.	Тур.	Max.	Units	Conditions/Notes
	Frequency Range		Fo	10.0000		40.0000	MHz	
	Operating Temperature		T <sub>A</sub>	-40		+85	°C	See Ordering Information & Table 1
	Frequency Stability			±0.05		±0.5	ppm	See Ordering Information & Table 1
	Short-Term Stability		1	1		5x10 <sup>-10</sup>		Tau = 0.1 to 30 secs.
	Frequency Vs. Aging		1	1		0.7	ppm	First year
					l	±4.0	ppm	For 10 years
	Frequency Vs. Supply		1	1		±0.1	ppm	For ±0.2 volt supply variation
	Frequency Vs. Load		1	1		±0.01	ppm	11,
	Supply Voltage	XO5160	$V_{dd}$	4.75	5.0	5.25	V	
		XO5161	V <sub>dd</sub>	11.5	12.0	12.5	V	
		XO5162	V <sub>dd</sub>	4.75	5.0	5.25	V	
		XO5163	V <sub>dd</sub>	11.5	12.0	12.5	V	
		XO5164	V <sub>dd</sub>	3.15	3.3	3.45	V	
	Supply Current	XO5160	I <sub>dd</sub>			70	mΑ	At +30°C
		XO5161	I <sub>dd</sub>		l	30	mA	At +30°C
		XO5162	I <sub>dd</sub>		l	70	mA	At +30°C
l su		XO5163	I <sub>dd</sub>		l	30	mΑ	At +30°C
Electrical Specifications		XO5164	I <sub>dd</sub>		l	110	mA	At +30°C
	Turn-On Current		I <sub>to</sub>			250	mA	After 10 secs.
	Warm-Up Time					1		2 min after power up following 4
	·				l	7		hour off time – reference to
						2x10 <sup>-7</sup>		frequency after 1 hour of operation
	Tuning Voltage	XO5160	V <sub>T</sub>	0.5	l	5.0	V	
		XO5161	V <sub>⊤</sub>	0	l	5.0	V	
		XO5162	$V_{T}$	0.5	l	5.0	V	
		XO5163	$V_{T}$	0	l	5.0	V	
		XO5164	$V_T$	0		3.3	V	
	Frequency Adjustment			±4.0			ppm	Over tuning voltage range
	Output Level			1		2	Vpk-pk	Sinewave 50 Ohm load
	Symmetry		Sym	45/55		55/45	%	Ref. To 1/2 V <sub>dd</sub> HCMOS output logic
	Output Load		$R_{L}$		l	15	pF	XO5160, XO5161, XO5164 only
			$R_{L}$			10	LSTTL	XO5160, XO5161, XO5164 only
			R <sub>L</sub>		50	<u> </u>	Ohms	XO5162, XO5163 only
	Rise/Fall Time (10% to 90%)		Tr/Tf			7	nS	1-40 MHz (Frequency dependent) HCMOS output logic
	Logic Level "0"		$V_{OL}$			10% Vdd	V	HCMOS output logic
	Logic Level "1"		$V_{OH}$	90% Vdd			V	HCMOS output logic
	Phase Noise (Typical) 10 MHz				l			
	1 Hz				-70		dBc/Hz	Offset from carrier
	10 Hz				-100		dBc/Hz	Offset from carrier
	100 Hz				-130		dBc/Hz	Offset from carrier
	1 kHz				-140		dBc/Hz	Offset from carrier
	10 kHz				-145		dBc/Hz	Offset from carrier
<u> </u>	100 kHz				-150		dBc/Hz	Offset from carrier
Environmental	Vibration		2000 Hz, 10 g					T
	Storage Temperature		-55°C to +125°C					
	Hermeticity		Per MIL-STD-202, Method 112					
<u>5</u>	Solderability		Per EIAJ-STD-002					
<u> </u>	Max Soldering Cor	ditions	+245°C for 10 secs. Max. (DIP version only)					
۳ ا	Max Soldering Conditions		+220°C for 10 secs. Max. (SMT version only)					
L	VOCAGO VOCAGO: TTL Load, cooled disput diagram #2							

XO5160-XO5162: TTL Load – see load circuit diagram #1. HCMOS Load – see load circuit diagram #2.

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