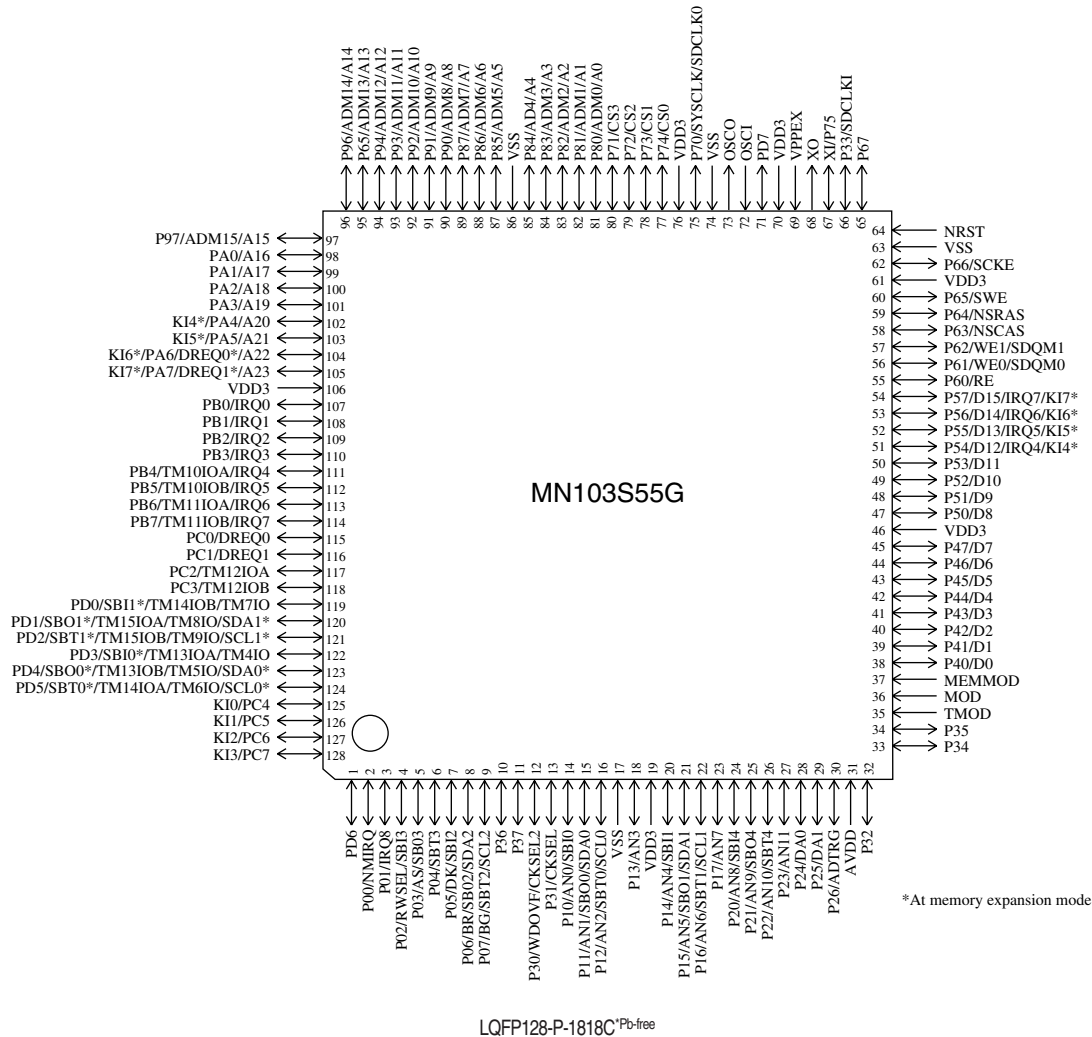


□ MN103S55G

Type	MN103S55G (under development)
Command ROM (×64-bit)	128 K-byte
Data RAM (×32-bit)	16 K-byte
Package	LQFP128-P-1818C *Pb-free
Minimum Instruction Execution Time	25.0 ns (at 2.7 V to 3.6 V, 40 MHz)
Interrupts	<ul style="list-style-type: none"> • RESET • IRQ × 9 • NMI • Timer × 30 • SIF × 10 • I²C × 3 • KEY • DMA × 12 • WDT • A/D • System error
Timer Counter	<p>8-bit timer × 10</p> <ul style="list-style-type: none"> Reload-down count Cascade connection possible (usable as a 16-bit to 32-bit timer) <p>16-bit timer × 6</p> <ul style="list-style-type: none"> Up-down count Input capture function PWM generating function Compare/capture register 2-ch. <p>Time base timer × 1, Free run counter</p> <p>Watchdog timer × 1</p>
DMA Controller	<p>Number of channels: 4</p> <p>Unit of transfer: 8/16/32 bits</p> <p>Max. Transfer cycles: 65535</p> <p>Starting factor: external interrupt, timer factor, serial transmission/reception factor, I²C transmission/reception factor, external transmission request factor, A/D conversion finish, software factor</p> <p>Transfer method: 2-bus cycle transfer</p> <p>Addressing modes: fixed, increment, decrement</p> <p>Transfer modes: word transfer, burst transfer, intermittent transfer</p>
Serial Interface	<p>UART/synchronous/multi-master I²C interface selective: 3</p> <p>UART/synchronous interface selective: 2</p>
I/O Pins	I/O 109 • Common use
A/D Inputs	10-bit × 12-ch.
D/A Inputs	8-bit × 2-ch.
Electrical Characteristics	T.B.D.

Pin Assignment



SupportTool

In-circuit Emulator	PX-ICE103S55	
On-board Development Tools	PX-ODB103S-O	
Flash Memory Built-in Type	Type	MN103SF55G (under development)
	Command ROM (× 64-bit)	128 K-byte
	Data RAM (× 32-bit)	16 K-byte
	Minimum instruction execution time	25.0 ns (at 2.7 V to 3.6 V, 40 MHz)
	Package	LQFP128-P-1818C *Pb-free