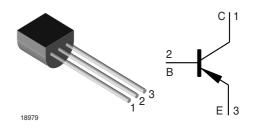


Small Signal Transistors (PNP)

Features

- PNP Silicon Epitaxial Planar Transistors for switching and AF amplifier applications.
- These transistors are subdivided into three groups A, B, and C according to their current gain. The type BC556 is available in groups A and B, however, the types BC557 and BC558 can be supplied in all three groups. As complementary types, the NPN transistors BC546...BC548 are recommended.
- On special request, these transistors are also manufactured in the pin configuration TO-18.



Mechanical Data

Case: TO-92 Plastic case Weight: approx. 177 mg **Packaging Codes/Options:** BULK / 5 k per container 20 k/box TAP / 4 k per Ammopack 20 k/box

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
Collector - base voltage		BC556	- V _{CBO}	80	V
		BC557	- V _{CBO}	50	V
		BC558	- V _{CBO}	30	V
			- V _{CBO}	30	V
Collector - emitter voltage		BC556	- V _{CES}	80	V
		BC557	- V _{CES}	50	V
		BC558	- V _{CES}	30	V
			- V _{CES}	30	V
			- V _{CEO}	65	V
			- V _{CEO}	45	V
			- V _{CEO}	30	V
			- V _{CEO}	30	V
Emitter - base voltage			- V _{EBO}	5	V
Collector current			- I _C	100	mA
Peak colector current			- I _{CM}	200	mA
Peak emitter current			- I _{EM}	200	mA
Power dissipation	T _{amb} = 25 °C		P _{tot}	500 ¹⁾	mW
Peak base current			- I _{BM}	200	mA

¹⁾ Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case.

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BC556 to BC558

Vishay Semiconductors



Maximum Thermal Resistance

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to		$R_{ hetaJA}$	250 ¹⁾	°C/W
ambient air				
Junction temperature		T _j	150	°C
Storage temperature range		T _S	- 65 to + 150	°C

¹⁾ Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case.

Electrical DC Characteristics

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Small signal current gain (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{fe}			220	
Small signal current gain (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{fe}			330	
Small signal current gain (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{fe}			600	
Input impedance (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{ie}	1.6	2.7	4.5	kΩ
Input impedance (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{ie}	3.2	4.5	8.5	kΩ
Input impedance (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{ie}	6	8.7	15	kΩ
Output admittance (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{oe}		18	30	μS
Output admittance (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{oe}		30	60	μS
Output admittance (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{oe}		60	110	μS
Reverse voltage transfer ratio (current gain group A)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{re}		1.5 x 10 ⁻⁴		
Reverse voltage transfer ratio (current gain group B)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{re}		2 x 10 ⁻⁴		
Reverse voltage transfer ratio (current gain group C)	$-V_{CE} = 5 \text{ V}, -I_{C} = 2 \text{ mA}, f = 1 \text{ kHz}$		h _{re}		3 x 10 ⁻⁴		
DC current gain (current gain group A)	$- V_{CE} = 5 V$, $- I_{C} = 10 \mu A$		h _{FE}		90		
DC current gain (current gain group B)	$- V_{CE} = 5 \text{ V}, - I_{C} = 10 \mu\text{A}$		h _{FE}		150		
DC current gain (current gain group C)	$- V_{CE} = 5 V$, $- I_{C} = 10 \mu A$		h _{FE}		270		
DC current gain (current gain group A)	- V _{CE} = 5 V, - I _C = 2 mA		h _{FE}	110	180	220	
DC current gain (current gain group B)	- V _{CE} = 5 V, - I _C = 2 mA		h _{FE}	200	290	450	
DC current gain (current gain group C)	- V _{CE} = 5 V, - I _C = 2 mA		h _{FE}	420	500	800	
DC current gain (current gain group A)	- V _{CE} = 5 V, - I _C = 100 mA		h _{FE}		120		
DC current gain (current gain group B)	- V _{CE} = 5 V, - I _C = 100 mA		h _{FE}		200		
DC current gain (current gain group C)	- V _{CE} = 5 V, - I _C = 100 mA		h _{FE}		400		
Collector saturation voltage	$-I_{C} = 10 \text{ mA}, -I_{B} = 0.5 \text{ mA}$		V _{CEsat}		80	300	mV
	- I _C = 100 mA, - I _B = 5 mA		V _{CEsat}		250	650	mV

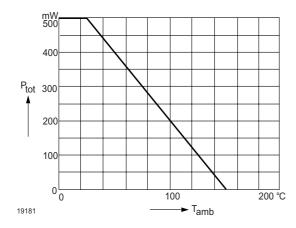


Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Base saturation voltage	$- I_C = 10 \text{ mA}, - I_B = 0.5 \text{ mA}$		V _{BEsat}		700		mV
	- I _C = 100 mA, - I _B = 5 mA		V _{BEsat}		900		mV
Base - emiter voltage	- V _{CE} = 5 V, - I _C = 2 mA		V_{BE}	600	660	700	mV
	- V _{CE} = 5 V, - I _C = 10 mA		V_{BE}			800	mV
Collector-emitter cut-off current	- V _{CE} = 80 V	BC556	I _{CES}		0.2	15	nA
	- V _{CE} = 50 V	BC557	I _{CES}		0.2	15	nA
	- V _{CE} = 30 V	BC558	I _{CES}		0.2	15	nA
	- V _{CE} = 80 V, T _j = 125 °C	BC556	I _{CES}			4	μΑ
	- V _{CE} = 50 V, T _j = 125 °C	BC557	I _{CES}			4	μΑ
	- V _{CE} = 30 V, T _j = 125 °C	BC558	I _{CES}			4	μΑ
		BC559	I _{CES}			4	μΑ

Electrical AC Characteristics

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Gain bandwidth product	$- V_{CE} = 5 V, - I_{C} = 10 mA,$		f _T		150		MHz
	f = 100 MHz						
Collector - base capacitance	- V _{CB} = 10 V, f = 1 MHz		C _{CBO}			6	pF
Noise figure	$- V_{CE} = 5 V, - I_{C} = 200 \mu A,$	BC556	F		2	10	dB
	$R_G = 2 k\Omega$, $f = 1 kHz$,						
	Δf = 200 Hz						
		BC557	F		2	10	dB
		BC558	F		2	10	dB
		BC559	F		1	4	dB
	$- V_{CE} = 5 V$, $- I_{C} = 200 \mu A$,	BC559	F		1.2	4	dB
	$R_G = 2 k\Omega$,						
	f = (30 to 15000) Hz						

Typical Characteristics ($T_{amb} = 25$ °C unless otherwise specified)





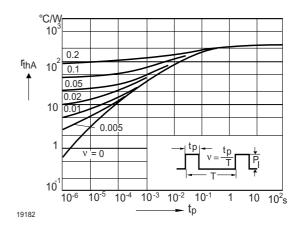


Figure 2. Pulse Thermal Resistance vs. Pulse Duration

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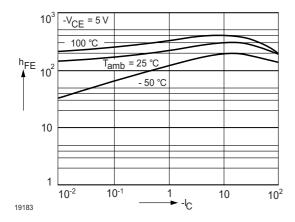


Figure 3. DC Current Gain vs. Collector Current

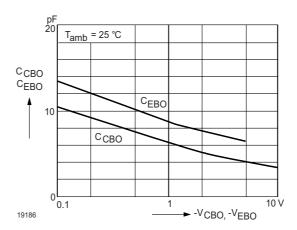


Figure 6. Collector Base Capacitance, Emitter base Capacitance vs. Bias Voltage

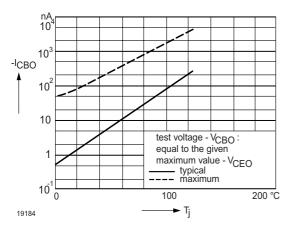


Figure 4. Collector-Base Cutoff Curent vs. Ambient Temperature

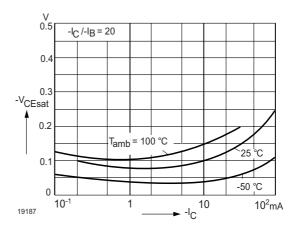


Figure 7. Collector Saturation Voltage vs. Collector Current

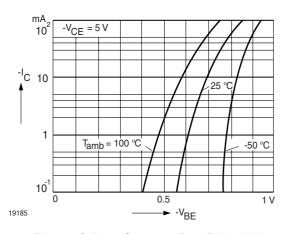


Figure 5. Collector Current vs. Base-Emitter Voltage

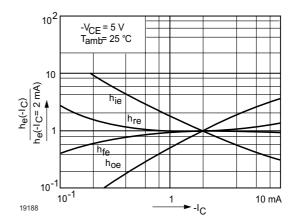


Figure 8. Relative h-Parameters vs. Collector Current



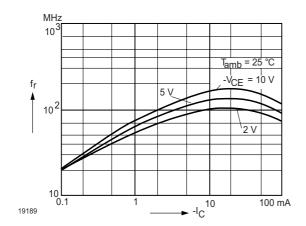
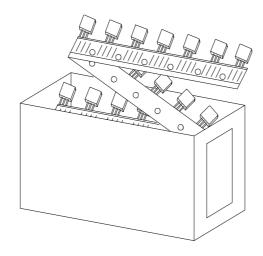
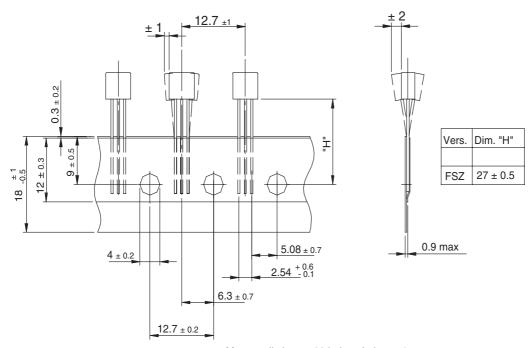


Figure 9. Gain-Bandwidth Product vs. Collector Current

Packaging for Radial Taping





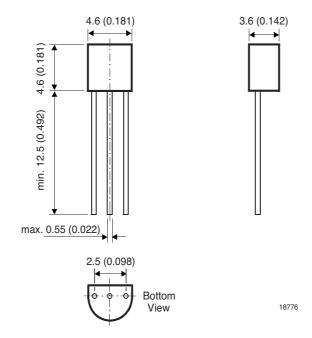


Measure limit over 20 index - holes: ± 1

18787



Package Dimensions in mm (Inches)



BC556 to BC558

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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