



FDPF33N25

250V N-Channel MOSFET

Features

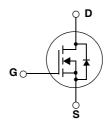
- 20A, 250V, $R_{DS(on)}$ = 0.094 Ω @ V_{GS} = 10 V
- Low gate charge (typical 36.8 nC)
- Low C_{rss} (typical 39 pF)
- · Fast switching
- · 100% avalanche tested
- · Improved dv/dt capability

Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficient switched mode power supplies and active power factor correction.





Absolute Maximum Ratings

Symbol	Parameter Drain-Source Voltage			FDPF33N25	Unit
V _{DSS}				250	V
I _D	Drain Current	- Continuous (T _C = 25 - Continuous (T _C = 10		20 12	A A
I _{DM}	Drain Current	- Pulsed	(Note 1)	80	A
V _{GSS}	Gate-Source voltage	ge		±30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	918	mJ
I _{AR}	Avalanche Current		(Note 1)	20	A
E _{AR}	Repetitive Avalance	he Energy	(Note 1)	9.4	mJ
dv/dt	Peak Diode Recov	ery dv/dt	(Note 3)	4.5	V/ns
P_{D}	Power Dissipation	(T _C = 25°C) - Derate above 25°C		94 0.76	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		ırpose,	300	°C

Thermal Characteristics

Symbol	Parameter	Min.	Max.	. Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		1.32	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		62.5	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDPF33N25	FDPF33N25	TO-220F	-	-	50

Electrical Characteristics $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
Off Charac	teristics			II.	ı	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	250			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C		0.25		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 250V, V _{GS} = 0V V _{DS} = 200V, T _C = 125°C			1 10	μ Α μ Α
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	-		100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	-		-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 10A		0.077	0.094	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40V, I _D =10A (Note 4)	-	26.6		S
Dynamic C	haracteristics					
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V,		1640	2135	pF
C _{oss}	Output Capacitance	f = 1.0MHz		330	430	pF
C _{rss}	Reverse Transfer Capacitance			39	59	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 125V, I _D = 33A		35	80	ns
t _r	Turn-On Rise Time	$R_G = 25\Omega$		230	470	ns
t _{d(off)}	Turn-Off Delay Time			75	160	ns
t _f	Turn-Off Fall Time	(Note 4, 5)		120	250	ns
Qg	Total Gate Charge	V _{DS} = 200V, I _D = 33A		36.8	48	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10V		10		nC
Q _{gd}	Gate-Drain Charge	(Note 4, 5)		17		nC
Drain-Sour	ce Diode Characteristics and Maximur	n Ratings		ı		
I _S	Maximum Continuous Drain-Source Diode Forward Current				33	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				132	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 10A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 33A		220		ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s $ (Note 4)		1.71		μС

NOTES:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 3.67mH, I_{AS} = 20A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}C$
- 3. $I_{SD} \le$ 20A, di/dt \le 200A/ μ s, $V_{DD} \le$ BV $_{DSS}$, Starting T_J = 25°C
- 4. Pulse Test: Pulse width $\leq 300 \mu s, \, \text{Duty Cycle} \leq 2\%$
- 5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

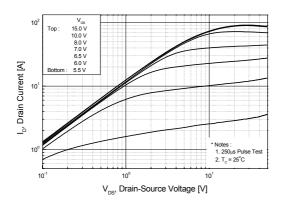


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

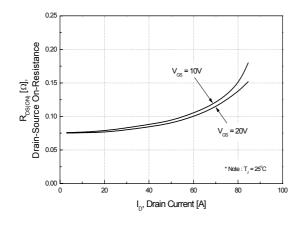


Figure 5. Capacitance Characteristics

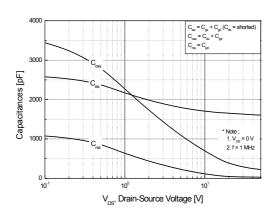


Figure 2. Transfer Characteristics

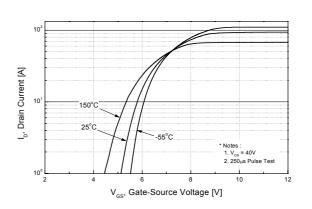


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

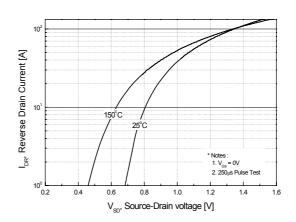
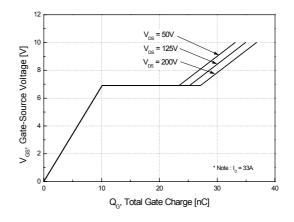


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

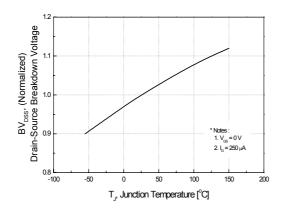


Figure 8. On-Resistance Variation vs. Temperature

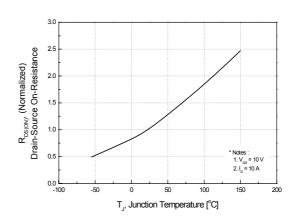


Figure 9. Maximum Safe Operating Area

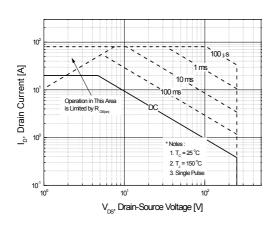


Figure 10. Maximum Drain Current vs. Case Temperature

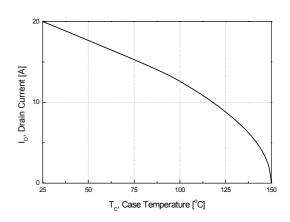
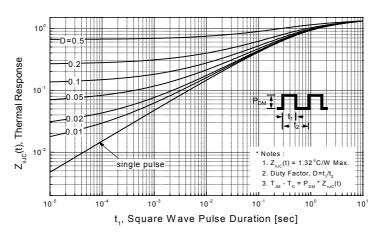
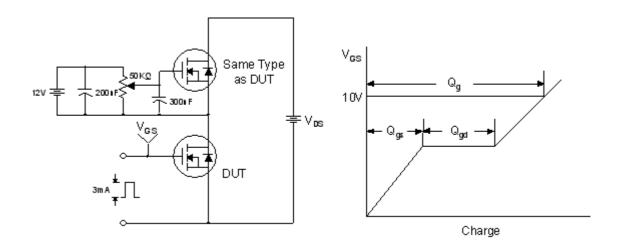


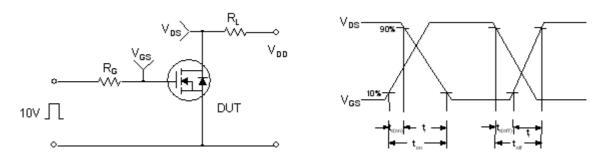
Figure 11. Transient Thermal Response Curve



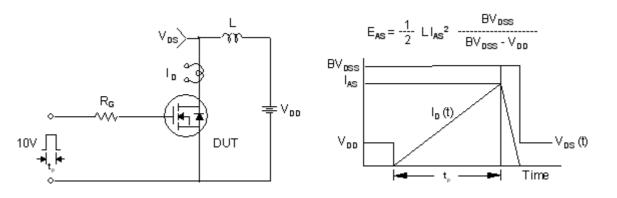
Gate Charge Test Circuit & Waveform



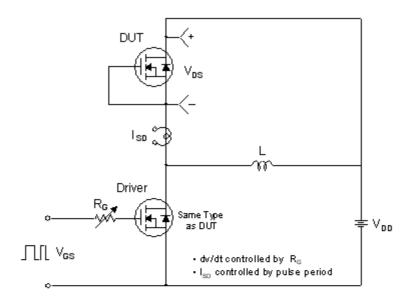
Resistive Switching Test Circuit & Waveforms

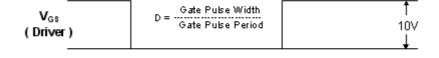


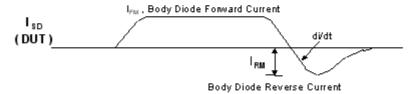
Unclamped Inductive Switching Test Circuit & Waveforms

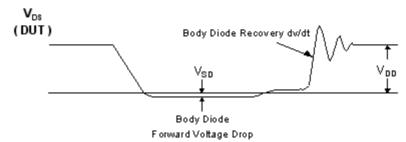


Peak Diode Recovery dv/dt Test Circuit & Waveforms









Mechanical Dimensions TO-220F 3.30 ± 0.10 2.54 ± 0.20 $10.16 \; {\pm}0.20$ $\emptyset 3.18 \pm 0.10$ (7.00)(0.70) 6.68 ± 0.20 Φ 15.87 ±0.20 15.80 ±0.20 (1.00x45°) MAX1.47 9.75 ± 0.30 0.80 ± 0.10 0.35 ± 0.10 0.50 +0.100 2.76 ±0.20 2.54TYP 2.54TYP [2.54 ±0.20] [2.54 ±0.20] 4.70 ± 0.20 9.40 ± 0.20

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EnSigna™	ImpliedDisconnect™	OCXPro™	μSerDes™	UHC™
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