



MIC2545A/2549A

Programmable Current Limit High-Side Switch

General Description

The MIC2545A and MIC2549A are integrated high-side power switches optimized for low loss DC power switching and other power management applications, including Advanced Configuration and Power Interface (ACPI). The MIC2545A/49A is a cost-effective, highly integrated solution that requires few external components to satisfy USB and ACPI requirements.

Load current management features include a precision, resistor-programmable output current-limit and a soft-start circuit, which minimizes inrush current when the switch is enabled. Thermal shutdown, along with adjustable current-limit, protects the switch and the attached device.

The MIC2545A/49A's open-drain flag output is used to indicate current-limiting or thermal shutdown to a local controller. The MIC2549A has an additional internal latch which turns the output off upon thermal shutdown, providing robust fault control. The enable signal is compatible with both 3V and 5V logic, and is also used as the thermal shutdown latch reset for the MIC2549A.

The MIC2545A and MIC2549A are available in active-high and active-low enable versions in 8-pin DIP, SOIC, and TSSOP packages.

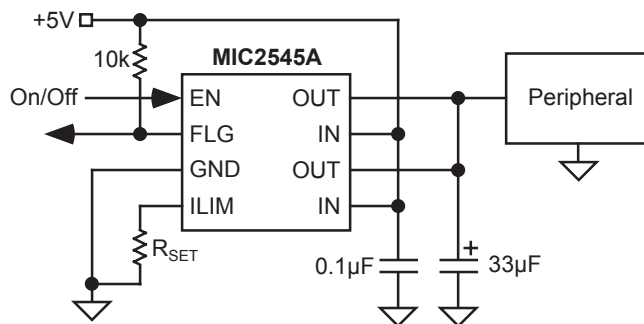
Features

- 2.7V to 5.5V input
- Adjustable current-limit up to 3A
- Reverse current flow blocking
- 90 μ A typical on-state supply current
- 1 μ A typical off-state supply current
- 50m Ω maximum on-resistance
- Open-drain fault flag
- Thermal shutdown
- Thermal shutdown output latch (MIC2549A)
- 2ms (slow) soft-start turn-on, fast turnoff
- Available with active-high or active-low enable

Applications

- USB power distribution
- PCI Bus Power Switching
- Notebook PC
- ACPI power distribution
- PC card hot swap applications
- Inrush current-limiting

Typical Application



Typical Advanced Configuration and Power Interface (ACPI) Application

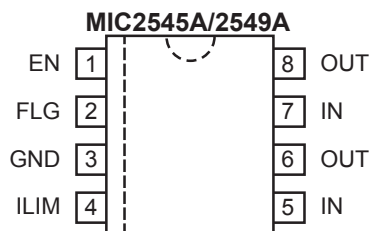


UL Recognized Component

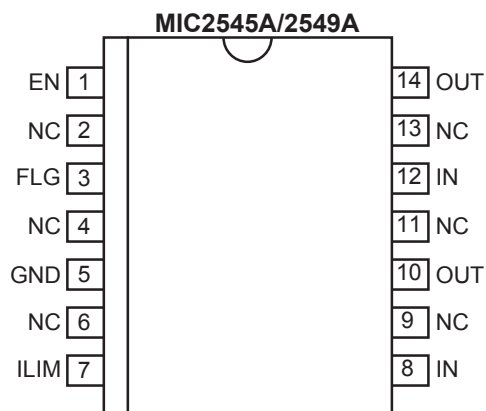
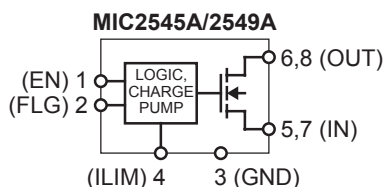
Ordering Information

Part Number	Enable	Latch*	Temperature Range	Package	Pb-Free
MIC2545A-1BM	Active High		-40°C to +85°C	8-lead SOP	
MIC2545A-1BN	Active High		-40°C to +85°C	8-pin DIP	
MIC2545A-1BTS	Active High		-40°C to +85°C	14-lead TSSOP	
MIC2545A-2BM	Active Low		-40°C to +85°C	8-lead SOP	
MIC2545A-2BN	Active Low		-40°C to +85°C	8-pin DIP	
MIC2545A-2BTS	Active Low		-40°C to +85°C	14-lead TSSOP	
MIC2545A-1YM	Active High		-40°C to +85°C	8-lead SOP	■
MIC2545A-1YN	Active High		-40°C to +85°C	8-pin DIP	■
MIC2545A-1YTS	Active High		-40°C to +85°C	14-lead TSSOP	■
MIC2545A-2YM	Active Low		-40°C to +85°C	8-lead SOP	■
MIC2545A-2YN	Active Low		-40°C to +85°C	8-pin DIP	■
MIC2545A-2YTS	Active Low		-40°C to +85°C	14-lead TSSOP	■
MIC2549A-1BM	Active High	■	-40°C to +85°C	8-lead SOP	
MIC2549A-1BN	Active High	■	-40°C to +85°C	8-pin DIP	
MIC2549A-1BTS	Active High	■	-40°C to +85°C	14-lead TSSOP	
MIC2549A-2BM	Active Low	■	-40°C to +85°C	8-lead SOP	
MIC2549A-2BN	Active Low	■	-40°C to +85°C	8-pin DIP	
MIC2549A-2BTS	Active Low	■	-40°C to +85°C	14-lead TSSOP	
MIC2549A-1YM	Active High	■	-40°C to +85°C	8-lead SOP	■
MIC2549A-1YN	Active High	■	-40°C to +85°C	8-pin DIP	■
MIC2549A-1YTS	Active High	■	-40°C to +85°C	14-lead TSSOP	■
MIC2549A-2YM	Active Low	■	-40°C to +85°C	8-lead SOP	■
MIC2549A-2YN	Active Low	■	-40°C to +85°C	8-pin DIP	■
MIC2549A-2YTS	Active Low	■	-40°C to +85°C	14-lead TSSOP	■

Pin Configuration



8-Lead SOP (M)
8-Pin DIP (N)



14-Lead TSSOP (TS)

Pin Description

Pin Number SOP, DIP	Pin Number TSSOP	Pin Name	Pin Function
1	1	EN	Enable (Input): Logic-compatible enable input (-1 version is active high, -2 version is active low). High input >1.8V typical; low input <1.4V typical. Do not float. MIC2549A only: Also resets thermal shutdown latch.
2	3	FLG	Fault Flag (Output): Active-low, open-drain output. Indicates overcurrent or thermal shutdown. MIC2549A only: Latched low on thermal shutdown.
3	5	GND	Ground: Supply return.
4	7	ILIM	Current Limit: Sets current limit threshold using an external resistor (R_{SET}) connected to ground. $76.8\Omega < R_{SET} < 459\Omega$.
5, 7	8, 12	IN	Supply Input: Output MOSFET drain. Also powers internal circuitry. Both IN pins must be externally connected together.
6, 8	10, 14	OUT	Switch Output: Output MOSFET source. Both OUT pins must be externally connected together.
	2,4,6,9,11,13	NC	Not internally connected

Absolute Maximum Ratings⁽¹⁾

Supply Voltage (V_{IN})	+7.0V
Output Voltage (V_{OUT})	+7.0V
Output Current (I_{OUT})	Internally Limited
Enable Input (V_{EN})	−0.3V to +7V
Fault Flag Voltage (V_{FLG})	+7.0V
Fault Flag Current (I_{FLG})	50mA
Storage Temperature (T_S)	−65°C to +150°C
Junction Temperature (T_J)	Internally Limited
Lead Temperature (soldering 5 sec.)	260°C
ESD Rating ⁽³⁾	2004V

Operating Ratings⁽²⁾

Supply Voltage (V_{IN})	+2.7V to +5.5V
Ambient Temperature Range (T_A)	−40°C to +85°C
Package Thermal Resistance	
PDIP (θ_{JA})	130°C/W
SOIC (θ_{JA})	160°C/W
TSSOP (θ_{JA}) ⁽⁴⁾	100°C/W
Current Limit Set Range ⁽⁵⁾	0.5A to 3A

Electrical Characteristics

$V_{IN} = +5V$; $T_A = 25^\circ C$, **bold** indicates −40°C to +85°C; unless noted

Symbol	Parameter	Condition	Min	Typ	Max	Units
	Supply Current	Switch off, OUT = open ⁽⁶⁾		0.75	5	μA
		Switch on, OUT = open ⁽⁶⁾		90	125 150	μA μA
V_{EN}	Enable Input Voltage	Enable high transition ⁽⁶⁾	2.4	1.6		V
		Enable low transition ⁽⁶⁾		1.5	0.8	V
	Enable Input Current	$V_{EN} = V_{OH(min)} = 2.4V$		0.01	1	μA
		$V_{EN} = V_{OL(max)} = 0.8V$		0.01	1	μA
	Enable Input Capacitance	Note 7		1		pF
$R_{DS(on)}$	Switch Resistance	$I_{OUT} = 500mA$		35	50	m Ω
	Current Limit Factor	$I_{LIMIT} = 0.5A$ to 3A, $V_{OUT} = 1V$ to 4V ⁽⁵⁾	184	230	276	V
		$I_{LIMIT} = 1A$ to 2.5A, $V_{OUT} = 1V$ to 4V ⁽⁵⁾	195	230	264	V
	Output Leakage Current	Switch off, $V_{OUT} = 0V$		1	10	μA
t_{ON}	Output Turn-On Delay	$R_L = 10\Omega$, $C_L = 1\mu F$, Figures 1a, 1b	1	2	5	ms
t_R	Output Turn-On Rise Time	$R_L = 10\Omega$, $C_L = 1\mu F$, Figure 1a, 1b	0.75	1.8	4.9	ms
t_{OFF}	Output Turnoff Delay	$R_L = 10\Omega$, $C_L = 1\mu F$, Figure 1a, 1b		25		μs
t_F	Output Turnoff Fall Time	$R_L = 10\Omega$, $C_L = 1\mu F$, Figure 1a, 1b		23		μs
	Overtemperature Threshold Shutdown	T_J increasing		135		°C
		T_J decreasing		120		°C
	Error Flag Off Current	$V_{FLG} = 5V$		0.01	1	μA
	EN Pulse Reset Width	MIC2549A thermal shutdown latch ⁽⁷⁾	1			μs
t_{SU}	V_{IN} to EN Set-Up	MIC2549A ⁽⁷⁾ , Figure 1c	0			μs
	Current-Limit Response Time	$V_{OUT} = 0V$ to $I_{OUT} = 2A$, $I_{SET} = 1A$ ⁽⁷⁾		40		μs
V_{OL}	Error Flag Output Resistance	$I_{FLG} = 10mA$		4	15	Ω

Notes:

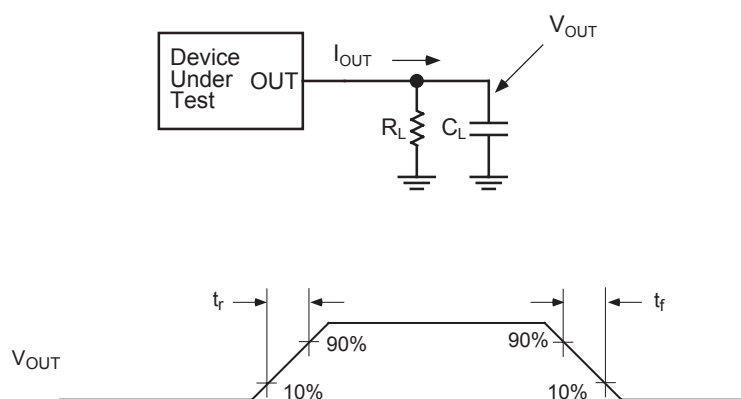
- Exceeding the absolute maximum ratings may damage the device.
- The device is not guaranteed to function outside its operating ratings.
- Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.
- Assumes 4 layer PCB is used.

5. Current limit is determined by: $I_{LIMIT} = \frac{230}{R_{SET}}$, where R_{SET} is in ohms, and 230 is typical current limit factor in volts.

6. Off is $\leq 0.8V$ and on is $\geq 2.4V$ for the MIC2545A-1 and MIC2549A-1. Off is $\geq 2.4V$ and on is $\leq 0.8V$ for the MIC2545A-2 and MIC2549A-2. The enable input has about 100mV of hysteresis.

7. Guaranteed by design but not production tested.

Test Circuit



Functional Characteristics Test Circuit

Timing Diagrams

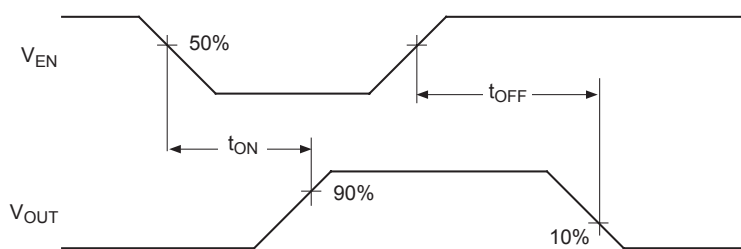


Figure 1a. MIC2545A/49A-2

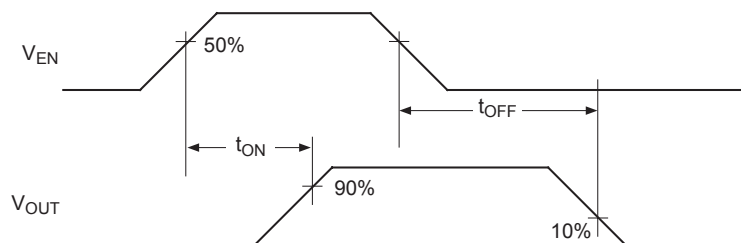


Figure 1b. MIC2545A/49A-1

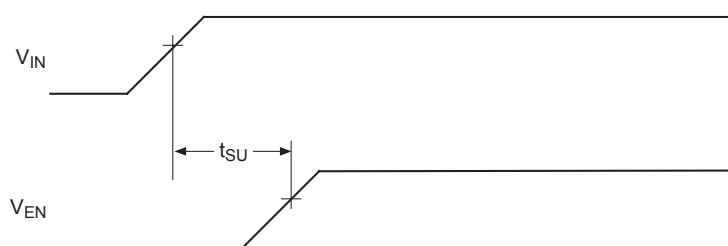


Figure 1c. Input-to-Enable Setup Timing

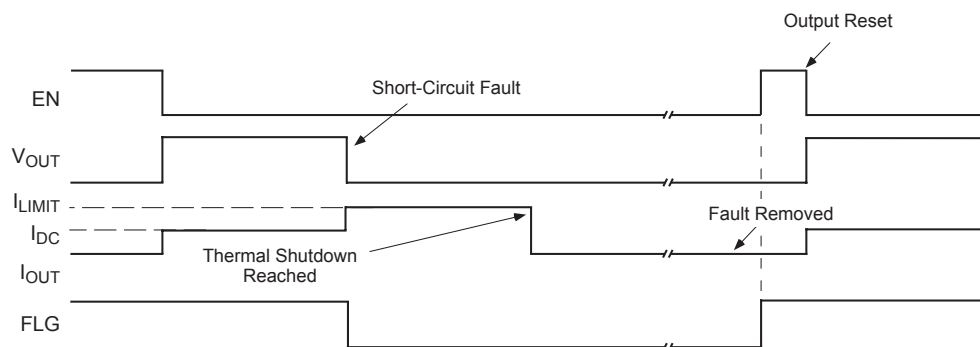


Figure 2a. MIC2549A-2 Timing: Output Is Reset By Toggling EN

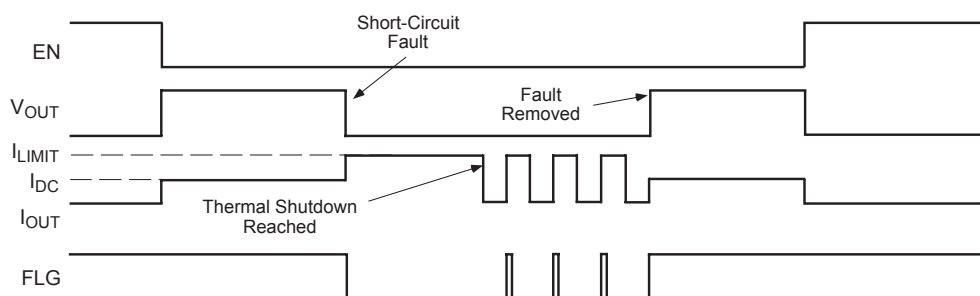
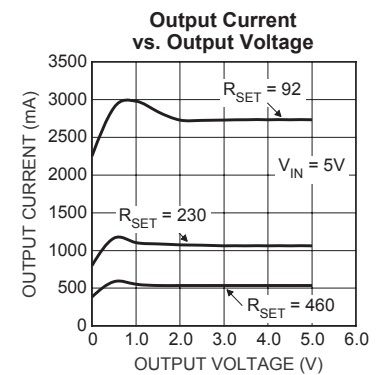
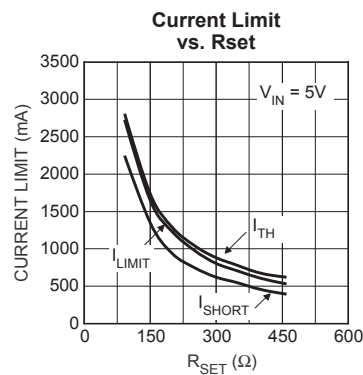
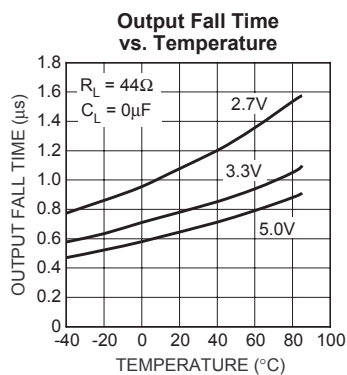
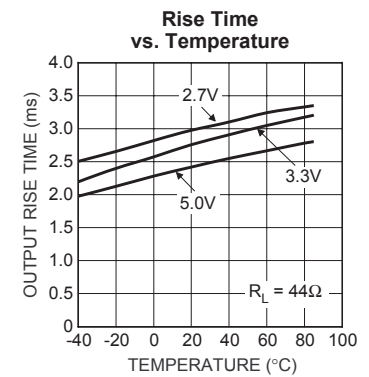
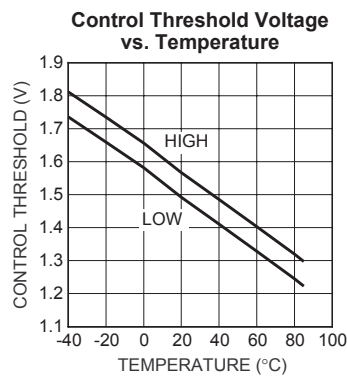
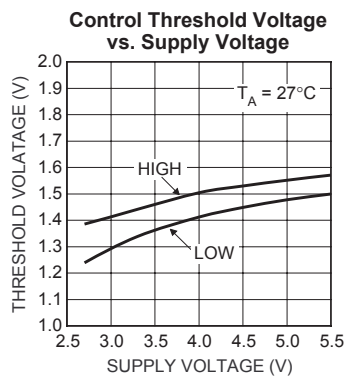
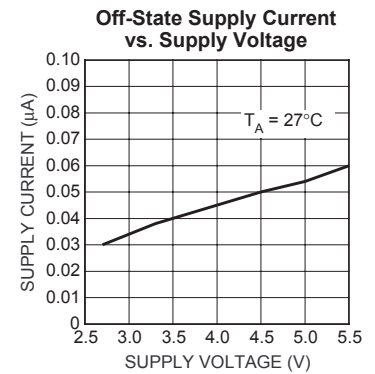
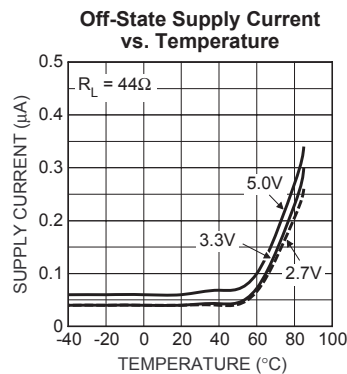
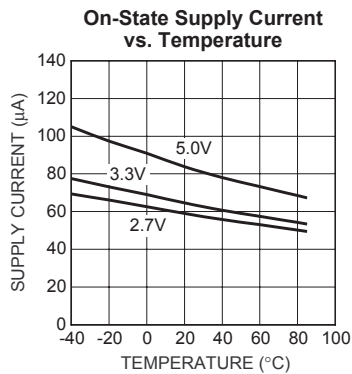
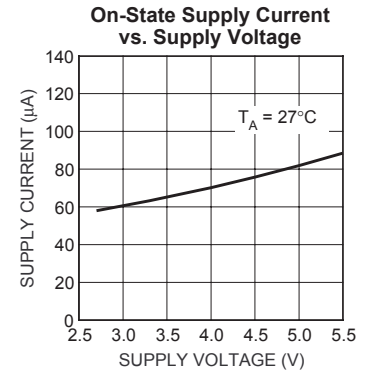
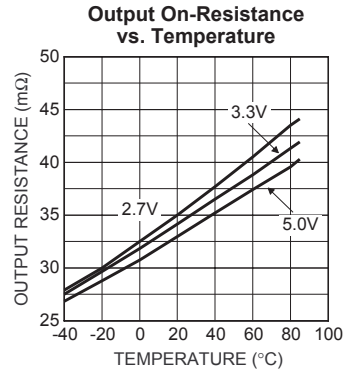
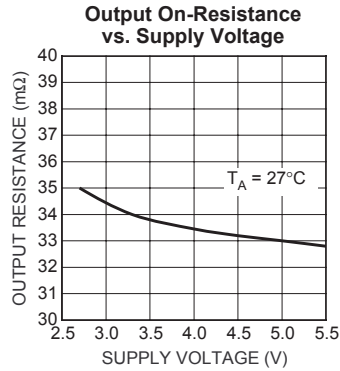
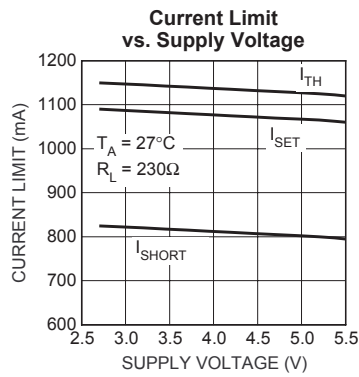
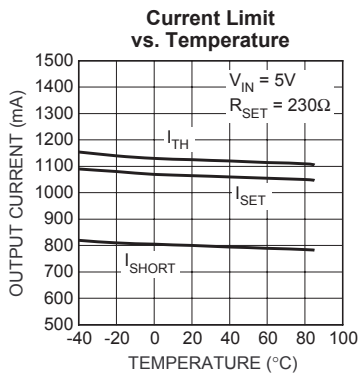


Figure 2b. MIC2545A-2 Timing

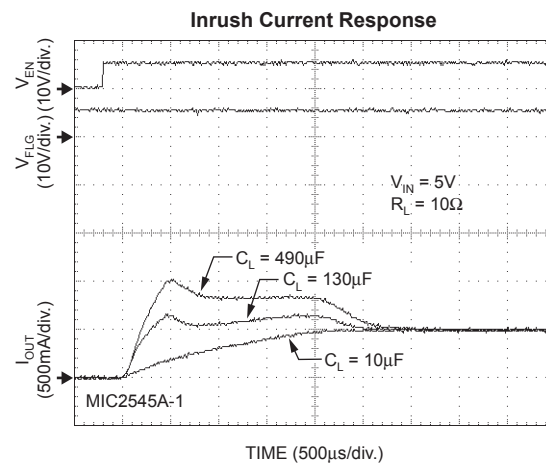
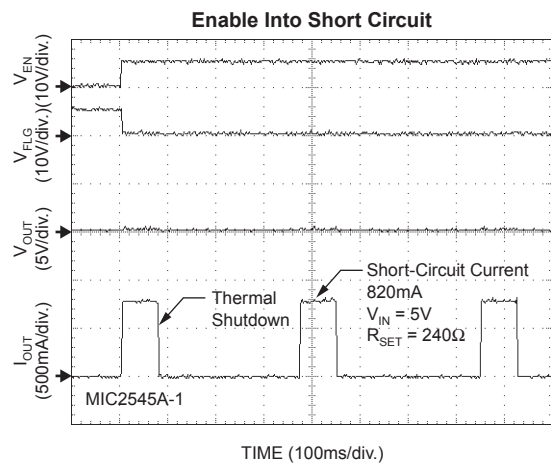
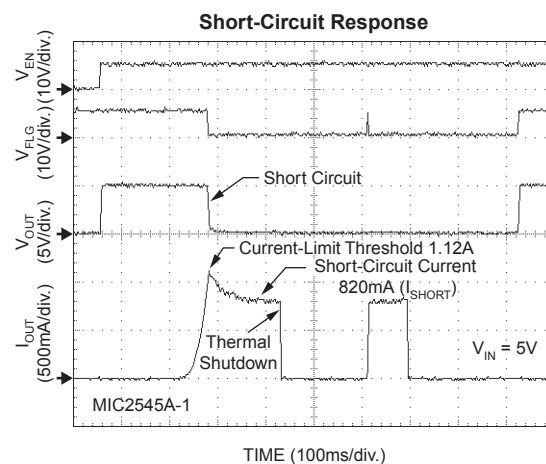
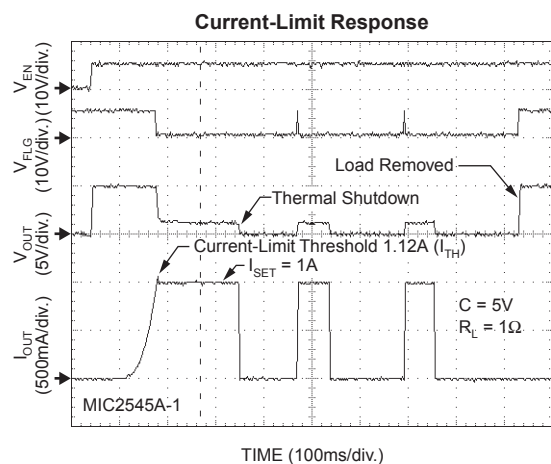
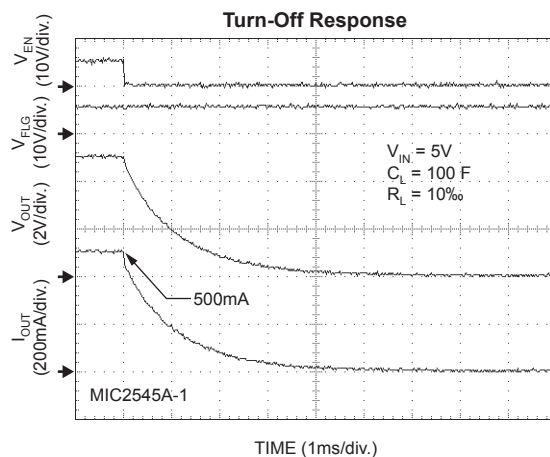
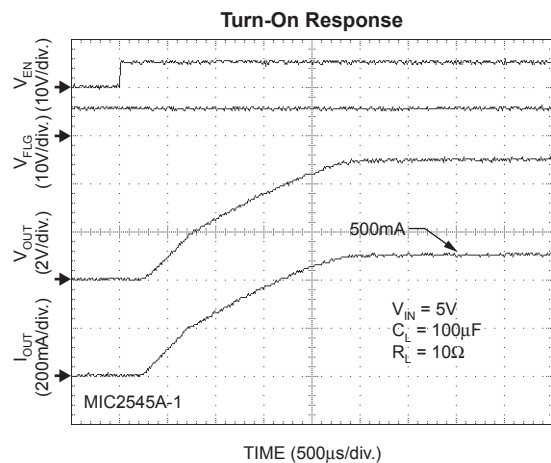
Typical Characteristics



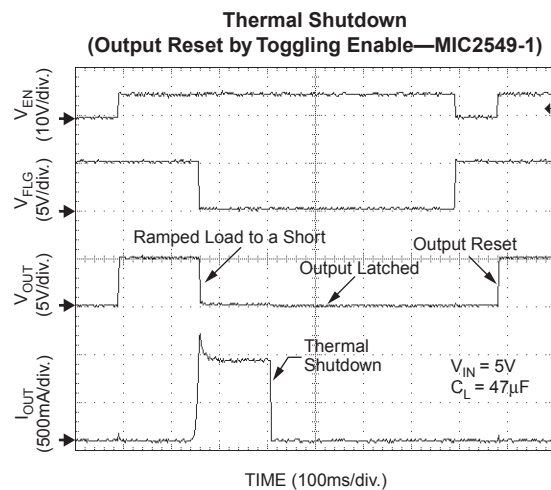
Typical Characteristics cont'd



Functional Characteristics



Functional Characteristics cont'd



May 2011



Functional Description

The MIC2545A and MIC2549A are high-side N-channel switches available with active-high or active-low enable inputs. Fault conditions turn off or inhibit turn-on of the output transistor and activate the open-drain error flag transistor making it sink current to ground.

Input and Output

IN is the power supply connection to the logic circuitry and the drain of the output MOSFET. OUT is the source of the output MOSFET. In a typical circuit, current flows from IN to OUT toward the load. If V_{OUT} is greater than V_{IN} , current will flow from OUT to IN since the switch is bidirectional when enabled. The output MOSFET and driver circuitry are also designed to allow the MOSFET source to be externally forced to a higher voltage than the drain ($V_{OUT} > V_{IN}$) when the switch is disabled. In this situation, the MIC2545A/49A avoids undesirable current flow from OUT to IN. Both IN pins must be connected together, and both OUT pins must be connected together.

Thermal Shutdown

Thermal shutdown shuts off the output MOSFET and signals the fault flag if the die temperature exceeds 135°C. 10°C of hysteresis prevents the switch from turning on until the die temperature drops to 125°C. Overtemperature detection functions only when the switch is enabled.

The MIC2549A features an internal latch which causes the part to remain off after thermal shutdown until a reset pulse is provided via the enable pin (pin 1). While in current-limit, the thermal shutdown latch prevents on/off cycling of the output. Refer to Figure 2 for timing diagram. The flag remains low until reset.

Enable Input

EN must be driven logic high or logic low, or be pulled high or low for a clearly defined input. Floating the input may cause unpredictable operation. EN should not be allowed to go negative with respect to GND.

Current-Limit Operation

The current limit is user adjustable with an external set resistor. Current limiting in the range of 500mA to 3A is available with a set point accuracy of better than $\pm 30\%$ ($\pm 20\%$ for I_{SET} 1 to 2.5A). The current-limit circuit prevents damage to the output MOSFET and external load.

The current-limit response of the MIC2545A/49A is based on the type of load that is applied to the output and is defined in three parts:

1. The first mode of operation is where the device enters a constant-current mode preventing further increases in output current. The value of this current, I_{LIMIT} , is defined by the value of R_{SET} as explained further in this section.
2. When a short circuit is applied to the output of an enabled device the output current immediately folds back to a value less than I_{LIMIT} called I_{SHORT} . This further protects the load and reduces device power dissipation. Refer to the "Typical Characteristics: Output Current vs. Output Voltage" graph for details.
3. When a load is increased, the output current will proportionally increase up to the current-limit threshold, I_{TH} , as shown in the "Functional Characteristics: Current-Limit Response" graph. The device in this case will supply current slightly higher than the current-limit set point defined by R_{SET} . As the load is increased further the current folds back to I_{LIMIT} .

The nominal current-limit value, I_{LIMIT} , is set with an external resistor between ILIM (pin 4) and GND (pin 3). For a desired current-limit, the value of the external set resistor is given by:

$$R_{SET} = \left(\frac{230}{I_{LIMIT}} \right)$$

where:

$$76.8\Omega < R_{SET} < 459\Omega$$

For example, to set a 1A nominal current-limit, R_{SET} is calculated as:

$$\left(\frac{230}{1A} \right) = 230\Omega$$

Refer to the "Typical Characteristics: Current-Limit vs R_{SET} " graph for more details.

Current through R_{SET} increases with output current. The voltage across R_{SET} could be monitored with a high impedance comparator to provide an indication of output current. R_{SET} should be between 76.8 Ω and 459 Ω (1% resistor value).

Fault Flag

FLG is an N-channel, open-drain MOSFET output. The fault-flag is active (low) for current-limit or thermal shutdown conditions. The flag output MOSFET is capable of sinking a 10mA load to typically 100mV above ground. For applications with $V_{IN} \leq 3.6V$, it is recommended that flag current be limited to 5mA or less.

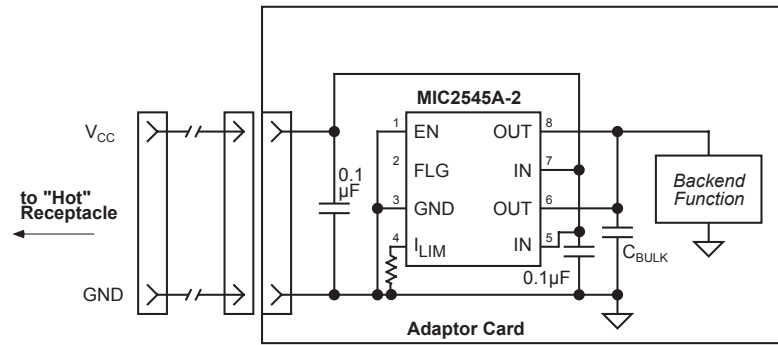
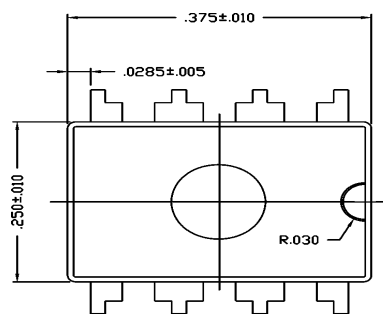
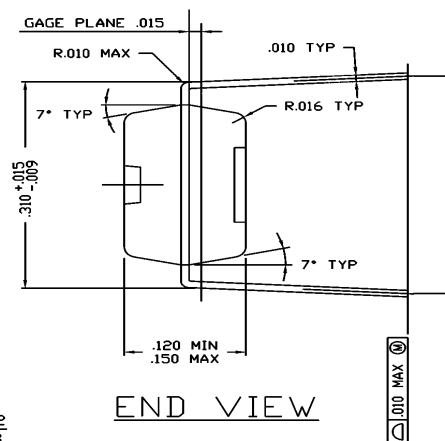


Figure 5. Hot-Plug Application

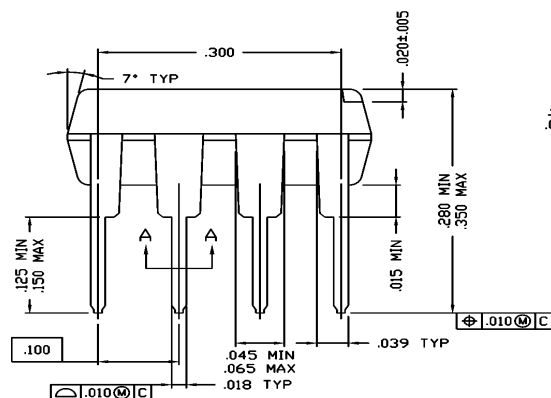
Package Information



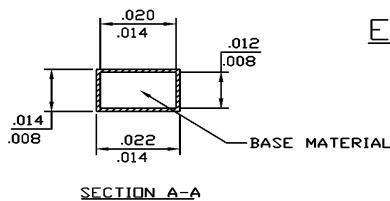
TOP VIEW



END VIEW

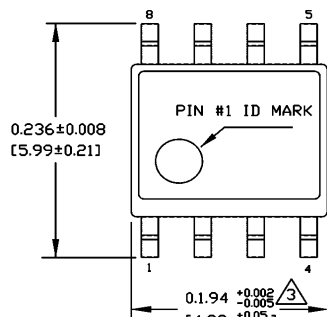


SIDE VIEW

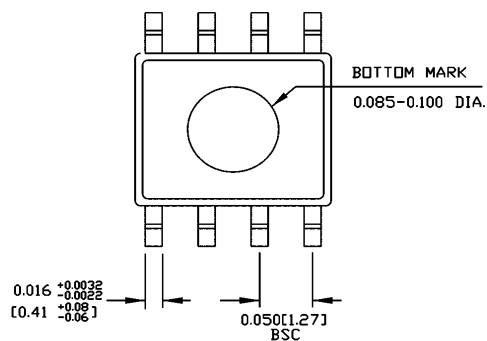


SECTION A-A

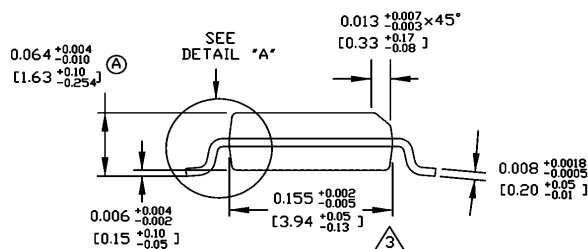
8-Pin DIP (N)



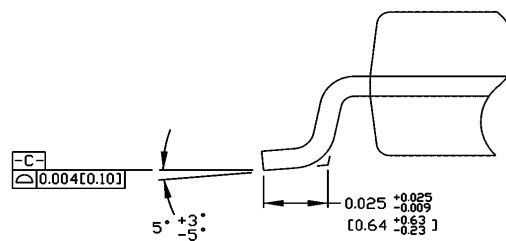
TOP VIEW



BOTTOM VIEW

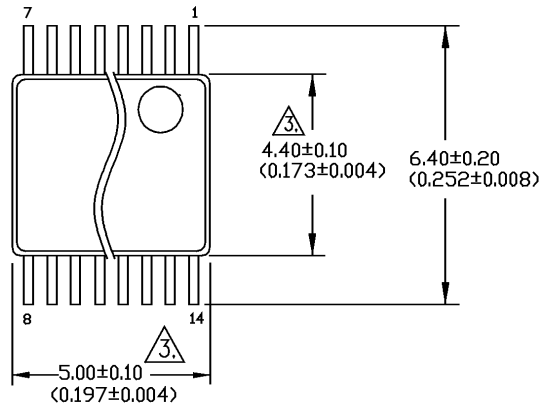


END VIEW

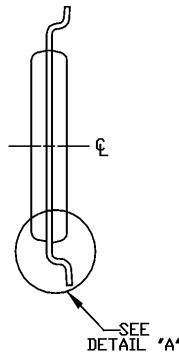


DETAIL "A"

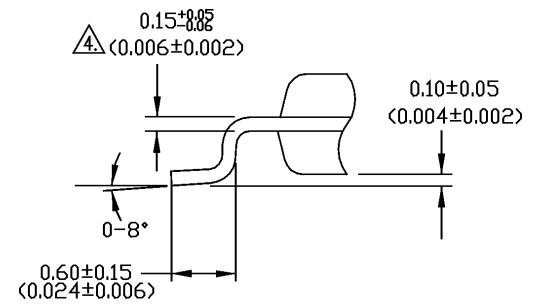
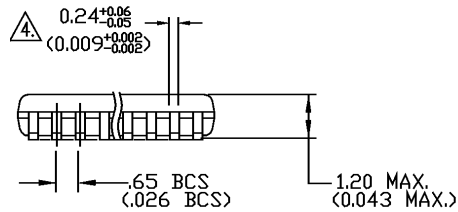
8-Pin SOIC (M)



TOP VIEW



END VIEW

DETAIL 'A'
(VIEW ROTATED 90° C.W.)

SIDE VIEW

14-Pin TSSOP (TS)

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