

NOTES:

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P6-P5-P4) : (J6-J3) (P3-P2-P1) : (J2-J1) : 1CT : 1± 3%

: 1.41CT : 1 ± 3%

2.0 INDUCTANCE: (P6-P4) (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias : 696uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT) : 0.3 MAX. @ 1MHz P3-P1 (WITH J2 AND J1 SHORT) : 0.6 MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6, P5, P4) TO (J6, J3) (P3, P2, P1) TO (J2, J1) : 30pf MAX @ 1MHz : 30pf MAX @ 1MHz

5.0 DC RESISTANCE: (J6-J3)=(J2-J1): 1.2 ohms Max. Stewart Connector Systems

http://www.stewartconnector.com

InNet Technologies inc. http://www.innet-tech.com

SHEET

DRAWING NO. SI - 402551 OF 4

6.0 INSERTION LOSS: 100KHz TO 100MHz : 1.1 dB TYP

100KHz TO 10MHz

7.0 RETURN LOSS: : 18 dB MIN. 1MHz TO 30MHz

30MHz TO 60MHz : 12 dB MIN.

8.0 VOLTAGE WITHSTAND: (J1, J2) TO (P1, P3) : 1500 VAC (J3, J6) TO (P4,P6) : 1500 VAC

: 3.0 nS MAX 9.0 RISE TIME: OUTPUT VOLTAGE = 1 V peak

PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100 MHz : 30 dB MIN

11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz : 35 dB TYP

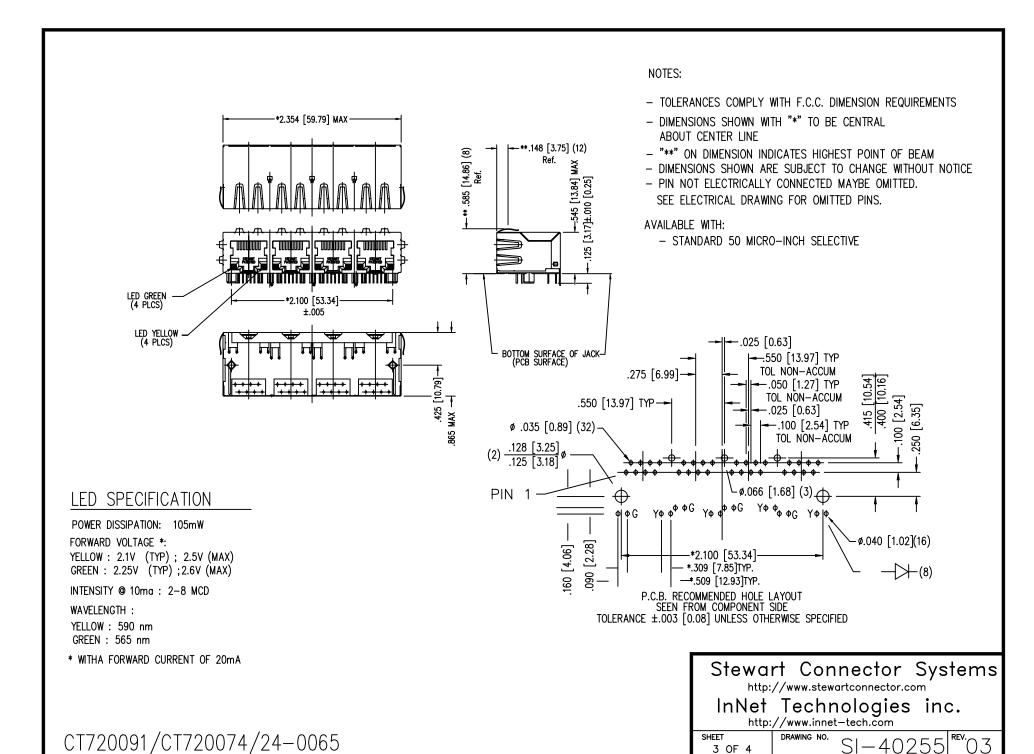
Stewart Connector Systems

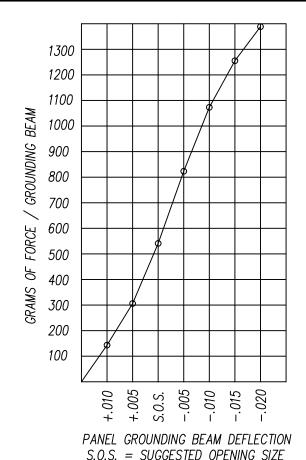
http://www.stewartconnector.com

InNet Technologies inc. http://www.innet-tech.com

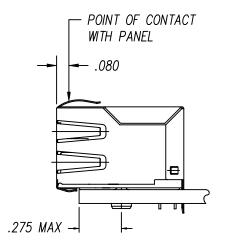
DRAWING NO.

SHEET 2 OF 4 SI - 40255

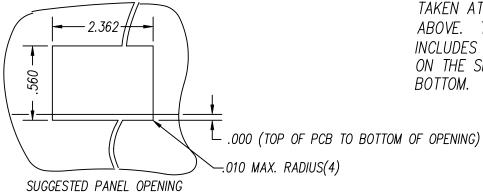




CT720035X1/24-001701



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .005 ON THE BOTTOM.



Stewart Connector Systems

http://www.stewartconnector.com

InNet Technologies inc. http://www.innet-tech.com

SHEET DRAWING NO.

4 OF 4

SI-40255 REV.03