

Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

3

Anode

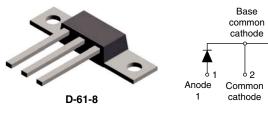
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Anode

2

VS-110CNQ045APbF



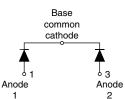
VS-110CNQ045ASMPbF





VS-110CNQ045ASLPbF





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Common

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Anode

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PRODUCT SUMMARY 2 x 55 A I_{F(AV)} 45 V V_{R}

FEATURES

- 150 °C T_J operation
- Center tap module
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	110	A		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	5400	А		
V _F	55 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.5	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-110CNQ045APbF	UNITS		
Maximum DC reverse voltage	V _R	45	V		
Maximum working peak reverse voltage	V _{RWM}	45	v		

* Pb containing terminations are not RoHS compliant, exemptions may apply





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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg forward current 50 %		50 % duty cycle at $T_{a} = 125 $ °C	% duty cycle at T _C = 125 °C, rectangular waveform		А
See fig. 5 per device	'F(AV)	$I_{F(AV)}$ 50 % duty cycle at T_{C} = 125 °C, rectangular waveform		110	
Maximum peak one cycle		I _{FSM} 5 μs sine or 3 μs rect. pulse Following any rated 10 ms sine or 6 ms rect. pulse rated V _{RRM} applied	° ,	5400	•
non-repetitive surge current per leg See fig. 7	IFSM			800	A
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.7 mH		54	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		8	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
			0.54		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	110 A	1j=25 C	T _J = 25 °C	V
See fig. 1	VFM (*)	55 A	T.I = 125 °C	0.5	v
		110 A	IJ = 120 C	0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 \text{ °C}$	3	mA	
See fig. 2	IRM (''	T _J = 125 °C	V _R = Rated V _R	350	ШA
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3800	pF
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		- R _{thJC}	DC operation See fig. 4	0.5	°C/W
Maximum thermal resistance, junction to case per package			DC operation	0.25	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight				7.8	g
				0.28	oz.
Mounting torque	minimum			40 (35)	kgf · cm
(D-61-8 only)	maximum			58 (50)	(lbf \cdot in)
Marking device			Case style D-61	110CN	Q045A
			Case style D-61-8-SM	110CNQ045/	
			Case style D-61-8-SL	110CNQ	045ASL



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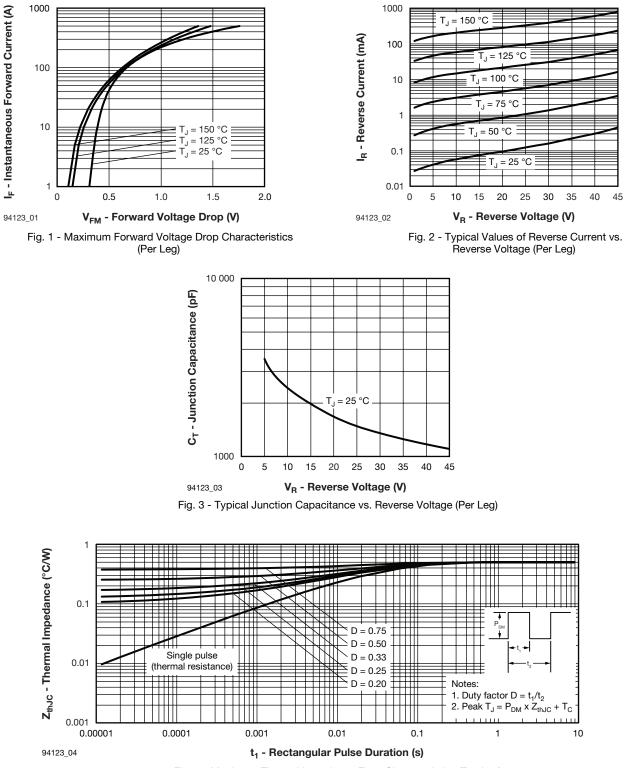
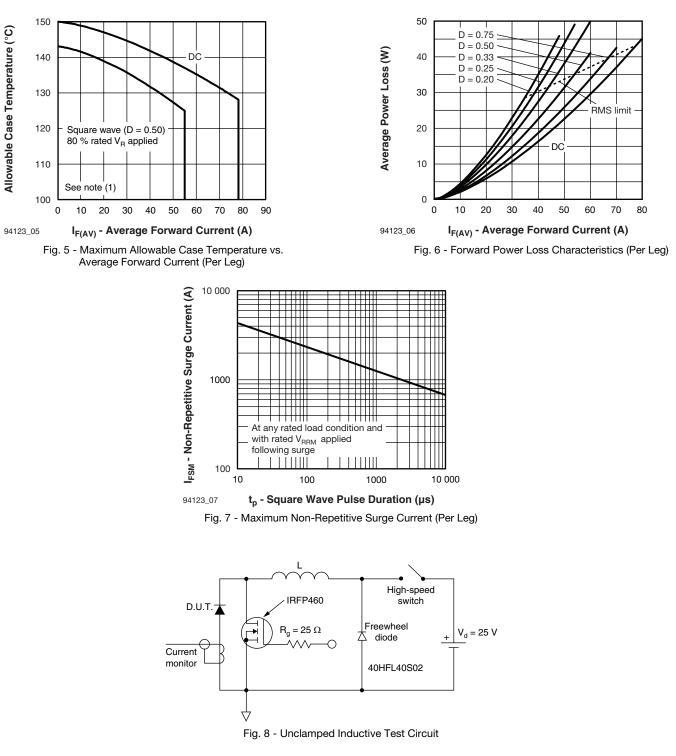


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

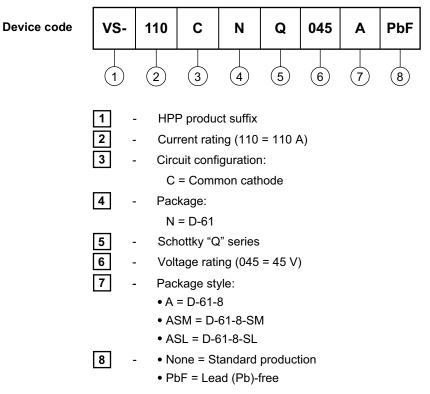


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ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95354				
Part marking information	www.vishay.com/doc?95356			



Vishay

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