

UN604

Silicon PNP epitaxial planer transistor
 Silicon NPN epitaxial planer transistor

For DC–DC converter

■ Features

- Two elements incorporated into one package.
- Reduction of the mounting area and assembly cost by one half.
- Automatic mounting possible through 12mm wide emboss-taping supply.

■ Basic Part Number of Element

- M261L+M262L (Name of developed product)

■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	± 10	V
Collector to emitter voltage	V_{CEO}	± 10	V
Emitter to base voltage	V_{EBO}	± 7	V
Collector current	I_C	± 1.5	A
Peak collector current	I_{CP}	± 2	A
Total power dissipation	P_T^{*1}	1	W
Junction temperature	T_j	150	°C
Storage temperature	T_{sg}	-55 to +150	°C

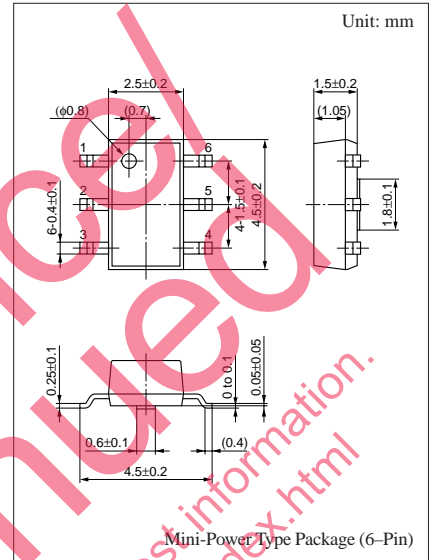
*1 Printed circuit board: Copper foil area of 4cm² or more and thickness of 1.7mm for the collector portion.

■ Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V_{CBO}	$I_C = \pm 10\mu A, I_E = 0$	± 10			V
Collector to emitter voltage	V_{CEO}	$I_C = \pm 1mA, I_B = 0$	± 10			V
Emitter to base voltage	V_{EBO}	$I_E = \pm 10\mu A, I_C = 0$	± 5			V
Collector cutoff current	I_{CBO}	$V_{CB} = \pm 7V, I_E = 0$			± 1	μA
Forward current transfer ratio	h_{FE}	$V_{CE} = \pm 1V, I_C = \pm 400mA^{*2}$	200		700	
Collector to emitter saturation voltage	$V_{CE(sat)}$	$I_C = \pm 1A, I_B = \pm 25mA^{*2}$		± 0.24	± 0.35	V
Transition frequency	f_T	$V_{CB} = \pm 6V, I_E = \pm 50mA, f = 200MHz$		190		MHz
Collector output capacitance	C_{ob}	$V_{CB} = \pm 10V, I_E = 0, f = 1MHz$		65		pF
Forward voltage	V_F^{*1}	$I_F = \pm 500mA$			± 1.3	V

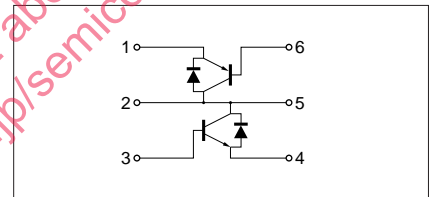
*1 Applicable to the built-in diode.

*2 Pulse test



Marking Symbol: 60

Internal Connection



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