

$\pm 5V$ FET Input 220MHz Video Buffer



CLM4135 / CLM4335

FEATURES

- Slew Rate 2700V/ μ s
- Wide Range Single or Dual Supply Operation
- Wide Power Bandwidth DC to 220MHz
- Low Phase Non-Linearity 2°
- Fast Rise Times 1.5ns
- High Input Resistance $10^{10}\Omega$

APPLICATIONS

- Video Line Driver
- High Speed ATE
- Coaxial Cable Driver
- Isolation Buffer
- High Frequency Filter
- Flash A/D Buffer
- Video DAC Buffer
- Op Amp Booster

GENERAL DESCRIPTION

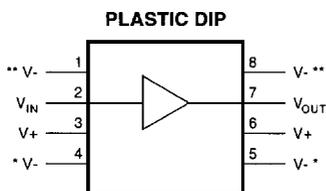
The CLM4135 family is a high speed, FET input, $\pm 5V$ supply buffer designed to provide high current drive at frequencies from DC to over 220MHz. The CLM4135 family has a slew rate of 2700V/ μ s and is capable of providing 80mA output current.

The family is intended to fulfill a wide range of buffer applications such as video distribution high speed line drivers, video impedance transformation, op amp isolation buffers for driving reactive loads and high impedance input buffers for high speed ADCs and comparators.

ORDERING INFORMATION

Part	Package	Temperature Range
CLM4135N	NO8A (Plastic PDip 8 Lead)	-40°C to +85°C
CLM4335N	NO8A (Plastic PDip 8 Lead)	-40°C to +85°C
CLM4135M	M16A (SOIC 16 Lead)	-40°C to +85°C
CLM4335M	M16A (SOIC 16 Lead)	-40°C to +85°C

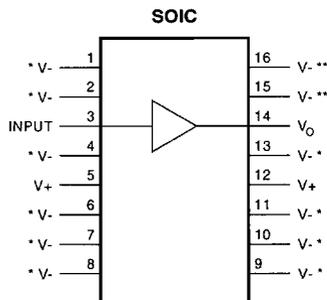
CONNECTION DIAGRAMS



* HEAT SINK PINS
** PIN 8 MUST BE CONNECTED TO NEGATIVE SUPPLY

PACKAGE NO8A

1A-42



* HEAT SINK PINS
** PIN 15 AND PIN 16 MUST BE CONNECTED TO NEGATIVE SUPPLY

PACKAGE M16A

1A-43

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage	±10	ESD Tolerance (Note 3)	±2000V
Input Voltage	±Vsupply	Thermal Resistance (θ_{JA}) (Note 6)	
Storage Temperature Range	-65°C to +150°C	N Package	50°C/W
Lead Temperature		M Package	60°C/W
(Soldering 10 seconds)	260°C	Maximum Junction Temperature	150°C
Power Dissipation	(Note 4)		

DC ELECTRICAL CHARACTERISTICS

The following specifications apply for Supply Voltage = ±5V, $V_{CM} = 0$, $R_L \geq 100K\Omega$ and $R_S = 50\Omega$ unless otherwise noted.

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^\circ C$.

SYMBOL	CHARACTERISTICS	TYP	CLM4135	CLM4335	UNITS	CONDITIONS
			Limit (Note 5)	Limit (Note 5)		
A_{V1}	Voltage Gain 1	0.95	0.92	0.90	V/V Min	$R_L = 1K\Omega$, $V_{IN} = \pm 4V$
A_{V2}	Voltage Gain 2	0.90	0.80	0.80		$R_L = 50\Omega$, $V_{IN} = \pm 3V$
V_{OS}	Offset Voltage	15	25	50	mV Max	$R_L = 1K\Omega$
I_B	Input Bias Current	1	350	700	pA Max	$R_L = 1K\Omega$, $R_S = 10k\Omega$
R_{IN}	Input Resistance	10^{10}	10^{10}	10^{10}	Ω	$R_L = 50\Omega$
C_{IN}	Input Capacitance	3.0			pF	
R_O	Output Resistance	3	10	10	Ω Max	$I_{OUT} = \pm 10mA$
I_S	Supply Current	10	14	14	mA Max	$R_L = \infty$
V_{O1}	Output Swing 1	4.5	4	4	$\pm V$ Min	$R_L = 1K$
V_{O2}	Output Swing 2	4	3.5	3.5		$R_L = 100\Omega$
I_{OUT}	Output Current	80	50	50	mA	$R_L = 50\Omega$

AC ELECTRICAL CHARACTERISTICS

The following specifications apply for Supply Voltage = ±5V, $V_{CM} = 0$, $R_L \geq 100K\Omega$ and $R_S = 50\Omega$ unless otherwise noted.

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^\circ C$.

SYMBOL	CHARACTERISTICS	TYP	CLM4135	CLM4335	UNITS	CONDITIONS
			Limit (Note 5)	Limit (Note 5)		
SR_1	Slew Rate 1	2700	2000	1500	V/ μS	$V_{IN} = \pm 4V$, $R_L = 1K\Omega$ (Note 2)
SR_2	Slew Rate 2	1500	1000	800		$V_{IN} = \pm 4V$, $R_L = 50\Omega$ (Note 2)
SS_{BW}	Small Signal Bandwidth	220	175	125	MHz	$V_{IN} = \pm 100mV_{PP}$, $R_L = 1K\Omega$ $C_L \leq 10pF$
LS_{BW}	Large Signal Bandwidth	80	60	40		$V_{IN} = \pm 11V$, $R_L = 1K$ $C_L \leq 10pF$
P_{BW}	Power Bandwidth	60	30	20		$V_{IN} = \pm 8V$, $R_L = 50\Omega$ $C_L \leq 10pF$
t_r , t_f	Rise Time Fall Time	1.6	2.5	3.5	ns	$R_L = 50\Omega$, $C_L \leq 10pF$ $V_O = 100mV_{PP}$
t_{pd}	Propagation Delay Time	2.1			ns	$R_L = 50\Omega$, $C_L \leq 10pF$ $V_O = 100mV_{PP}$
O_s	Overshoot	10			%	$R_L = 50\Omega$, $C_L \leq 10pF$ $V_O = 100mV_{PP}$

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.

Note 2: Slew rate is measured with 50 Ω source impedance at 25°C. For accurate measurements, the input slew rate should be at least 5000V/ μs .

Note 3: The test circuit consists of the human body model of 120pF in series with 1500 Ω .

Note 4: The maximum power dissipation is a function of $T_{J(MAX)}$, θ_{JA} and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A)/\theta_{JA}$.

Note 5: Limits are guaranteed by testing, correlation or periodic characterization.

Note 6: For M & N package, θ_{JA} is measured by soldering the unit directly on a printed circuit board and V pins are connected to 2 square inches of 2 oz copper.

TYPICAL PERFORMANCE CHARACTERISTICS

