



TTL OUTPUT • FULL-SIZE AND HALF-SIZE DIP CRYSTAL CLOCK OSCILLATORS ACT and AHT

FEATURES:

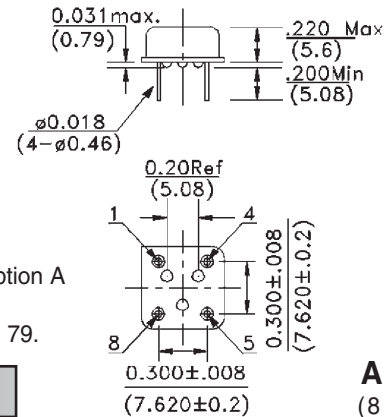
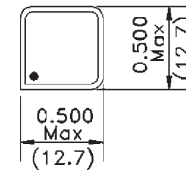
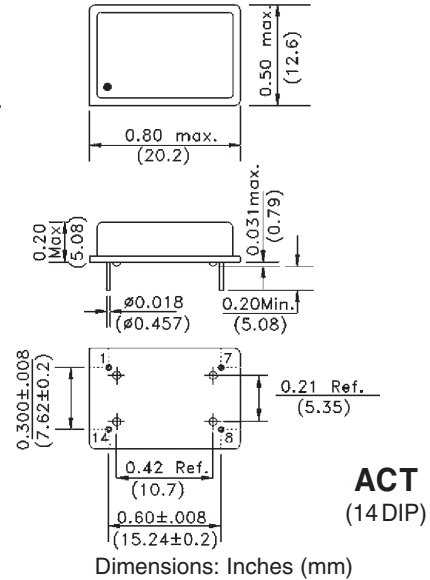
- Hermetically sealed metal package.
- TTL output.
- Case Ground for EMI protection.
- Tristate enable/disable options.

APPLICATIONS:

- Provide Clock Signals for Microprocessors and Digital Circuits.
- TTL output for low EMI applications.

STANDARD SPECIFICATIONS

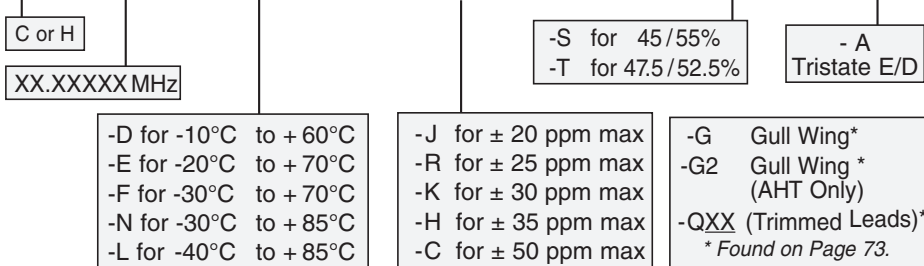
PARAMETERS	SPECIFICATIONS
Package Type	ACT (14 DIP) and AHT (8 DIP)
Frequency Range (F_0)	500 kHz - 70 MHz
Operating Temperature (T_{OPR})	0°C to +70°C (See Options)
Storage Temperature (T_{STO})	-55°C to +125°C
Frequency Stability ($\Delta F/F_0$)	± 100 ppm max. (See Options)
Supply Voltage (V_{dd})	5 Vdc ± 10%
Input Current (I_{dd})	30 mA max. $F_0 \leq 25$ MHz (Without Load) 70 mA max. $F_0 \sim 25.01$ MHz - 70 MHz
Duty Cycle or Symmetry (at 1.4 Vdc)	45/55% max. for $F \leq 8.0$ MHz 40/60% max. for $F > 8.0$ MHz (See Options)
Rise and Fall Times (T_R/T_F)	10 ns max. for $F \leq 30$ MHz 6 ns max. for $F > 30$ MHz (See Options)
Output Load	10TTL max.
Output Voltage (V_{OH}) (V_{OL})	2.4 Vdc min. 0.4 Vdc max.
Start-up Time (T_{osc})	10 ms max.
Tristate Function (V_{IH}) (Option "-A" ONLY) (V_{IL})	"1" or Open: Oscillation $\geq 2.2V$ "0": Output disabled in high impedance (HiZ) $< 0.8V$
Output Disable/ Enable Time	100 ns max. (for Option "- A" ONLY)



(*) Pin 1 has internal pull-up resistor which allows Pin 1 to be left floating (enable high) Option A
For test circuit, waveforms, please see page 67.
Environmental and mechanical specifications on page 68, Group 1. Marking, see page 79.

ORDERING OPTIONS

AHT - Frequency - Temperature - Overall Frequency Stability - Duty Cycle - Tristate - Value Added



PIN #	PIN #	FUNCTION
ACT	AHT	
1	1	NC or Tristate E/D
7	4	GND / Case
8	5	Output
14	8	Vdd