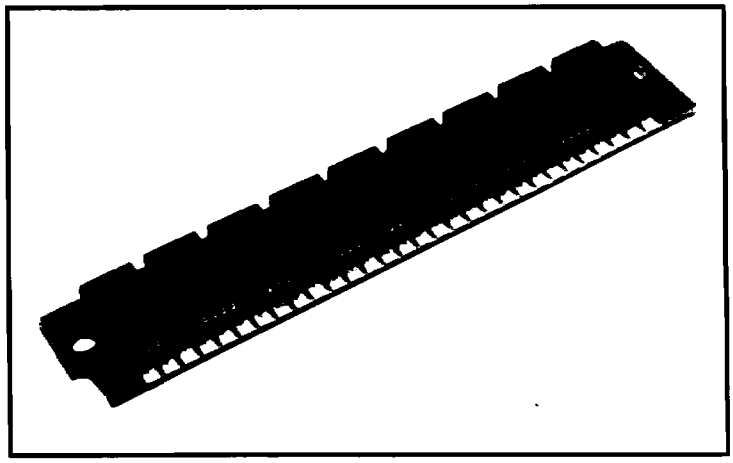
ACCUTEK MICROCIRCUIT CORPORATION

AK49256S/AK49256G
262,144 x 9 bit NMOS
Dynamic Random Access Memory

DESCRIPTION

The Accutek AK49256 high density memory module is a random access memory organized in 256K x 9 bit words. The assembly consists of nine 256K x 1 DRAMs in plastic leaded chip carriers (PLCC) mounted to a printed circuit board. The module can be configured as a leadless 30 pad SIMM or a leaded 30 pin SIP. This packaging approach provides a 6 to 1 density increase over standard DIP packaging.

The operation of the AK49256 is identical to nine 256K dynamic RAMs. For the lower eight bits, the data input is tied to the data output and brought out separately for each device, with common $\overline{RAS}$, $\overline{CAS}$ and $\overline{WE}$ control. This common I/O feature dictates the use of early-write cycles to prevent contention of D and Q. Since the Write-Enable ($\overline{WE}$) signal must always go low before $\overline{CAS}$ in a write cycle, Read-Write and Read-Modify-Write operation is not possible. For the ninth bit, the data input (D_9) and data output (Q_9) pins are brought out separately and controlled by a separate $\overline{CAS}_9$ for that bit. Bit nine is generally used for parity.



FEATURES

- 262,144 x 9 bit organization
- Optional 30 Pad SIMM (Single In-line Memory Module) or 30 Pin leaded SIP (Single In-line Package)
- JEDEC approved pinout
- Common $\overline{CAS}$, $\overline{RAS}$ and $\overline{WE}$ control for eight common D and Q lines
- Separate $\overline{CAS}$ control for one separate pair of D and Q lines
- 3.15 Watt active and 205 mW standby (max)
- Operating free air temperature: 0°C to 70°C
- Upward compatible with AK491024

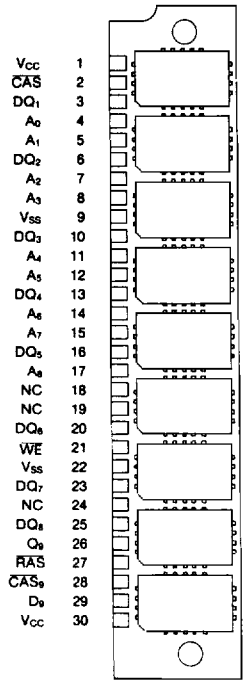
PIN NOMENCLATURE

DQ_1 - DQ_8	Data In/Data Out
D_9	Data In - Parity Bit
Q_9	Data Out - Parity Bit
A_0 - A_8	Address Inputs
$\overline{CAS}$	Column Address Strobe
$\overline{RAS}$	Row Address Strobe
$\overline{WE}$	Write Enable
Vcc	5v Supply
Vss	Ground
NC	No Connection

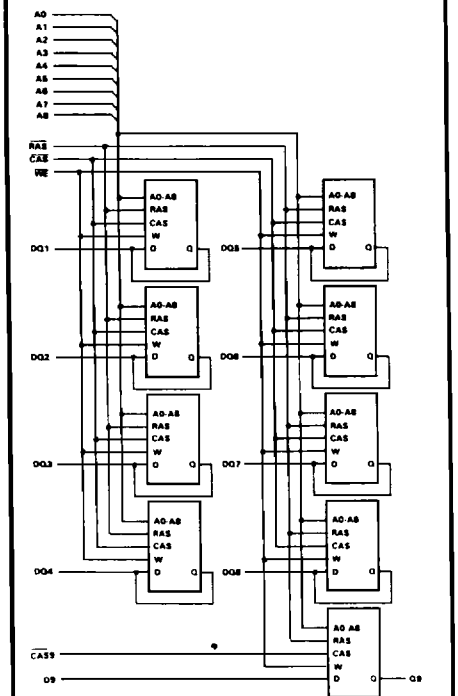
MODULE OPTIONS

Leadless SIMM: AK49256S Single Inline Memory Module
Leaded SIP: AK49256G Single Inline Package

PIN ASSIGNMENT



FUNCTIONAL DIAGRAM



ORDERING INFORMATION
PART NUMBER CODING INTERPRETATION

Position: 1 2 3 4 5 6 7 8

1 - Product
- AK = ACCUTEK Memory

2 - Type
- 4 = Dynamic RAM
- 6 = Static RAM

3 - Organization/Word Width
- 1 = by 1
- 4 = by 4
- 8 = by 8
- 9 = by 9

4 - Size/Bits Depth
- 256 = 256K
- 1024 = 1MEG
- 4096 = 4MEG

5 - Package Type
- G = Single In-Line Package (SIP)-leaded
- S = Single In-Line Memory Module (SIMM)
- D = Dual In-Line Package (DIP)

6 - Special Designation
- P = Page Mode
- N = Nibble Mode
- K = Static Column Mode

7 - Separator
- - = Commercial (0° to +70°C)
- M = Military Equivalent Screened (-55° to +125°C)
- I = Industrial Temperature Tested (-40° to +85°C)
- X = Burned-In

8 - Speed (first two significant digits)
- 60 = 60ns
- 70 = 70ns
- 80 = 80ns
- 10 = 100ns
- 12 = 120ns
- 15 = 150ns
- etc.

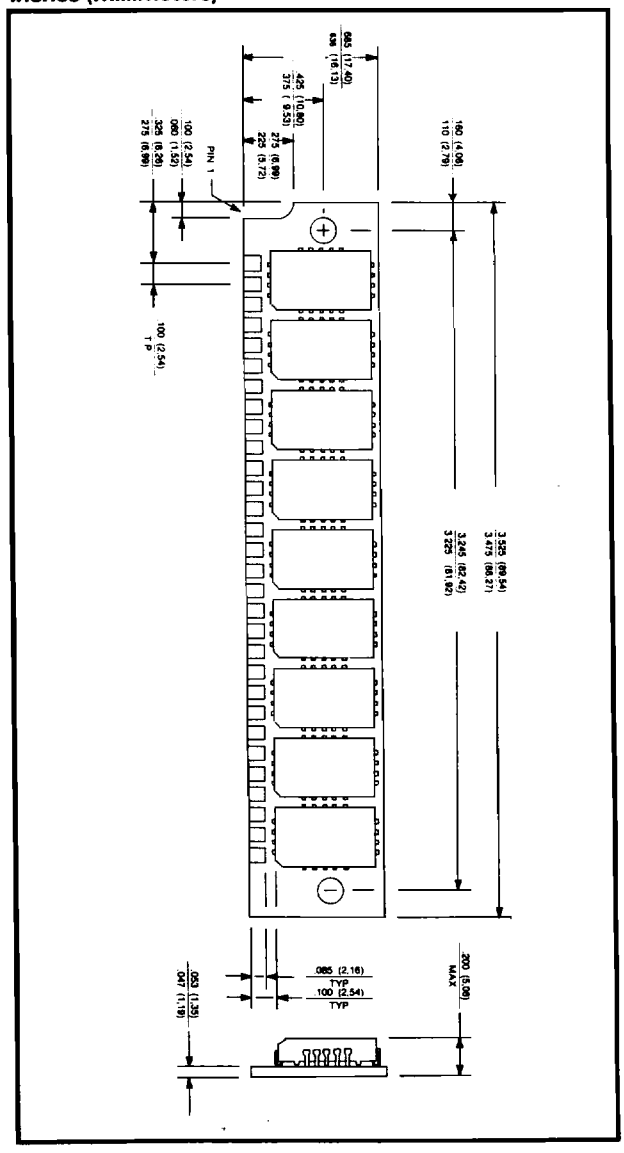
The numbers and coding combinations on this page do not include all variations available, but are shown as examples of most widely used variations. Contact Accutek if other information is required.

EXAMPLES:

AK48256SP-12
256K x 8 Dynamic RAM, SIMM, Page Mode, Commercial, 120nS Access Time

AK49256GP-10
256K x 9 Dynamic RAM, leaded SIP, Page Mode Commercial, 100nS Access Time

MECHANICAL DIMENSIONS
inches (millimeters)



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