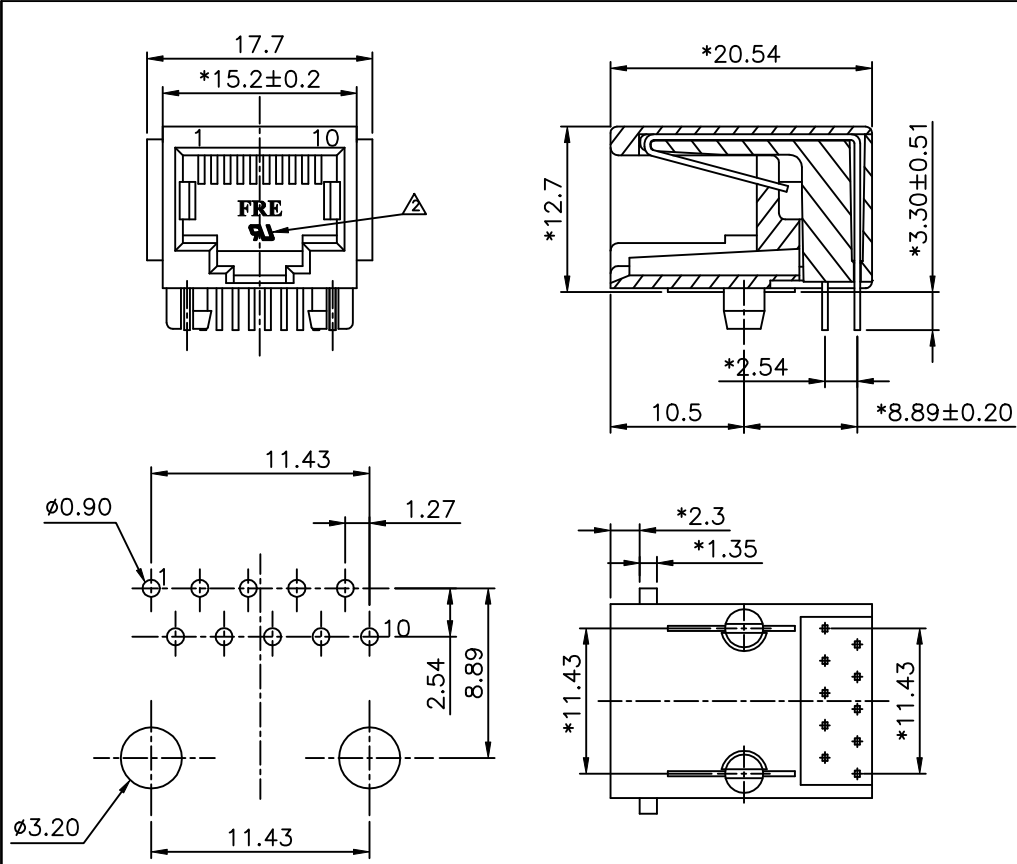




PC Board Layout
Component Side Shown

△ LEAD FREE P/N: E590X-000XXX-L
8- 8 CONTACTS
1-10 CONTACTS
0-PBT+30%G.F.
3-FR52
1- 3u" 2- 6u"
3-15u" 4-30u"
5-50u" 6-Pd 20u"
3-WITH PANEL STOP
4-WITHOUT PANEL STOP
J-WITHOUT PANEL STOP,W/O "FRE"LOGO

NOTES:

ELECTRICAL:

1. VOLTAGE RATING: 125 VAC RMS.
2. CURRENT RATING: 1.5 AMP.
3. CONTACT RESISTANCE: 50 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH: 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING MATERIAL: PBT+3%G.F. OR FR52.
2. CONTACT MATERIAL: PHOSPHOR BRONZE t=0.35mm
3. PLATING: GOLD PLATING OVER NICKEL.
4. OPERATING LIFE: 750 CYCLES MIN.
5. PCB RETENTION PRE-SOLDER: 1 LB MIN.
6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE: -40°C TO +85°C.
2. OPERATION: 0°C TO 70°C.

MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

CUL FILE NO. E163191

PART NUMBER: E590X-000XXX

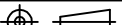
8- 8 CONTACTS
1-10 CONTACTS

1- 3u" 2- 6u"
3-15u" 4-30u"
5-50u" 6-Pd 20u"

0-PBT BK
△ 3-FR52 BK

3-WITH PANEL STOP
△ 4-WITHOUT PANEL STOP
△ J-WITHOUT PANEL STOP,W/O "FRE"LOGO

REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD
△1		ADD P/N	CZH	
△2		DRAWING MODIFICATION	8/22/2002	
△3		ADD P/N	2/26/2003	
△4		ADD P/N	7/16/2003	
△5		ADD P/N	12/3/04	
△6		ADD P/N	03/05/05	

DETACHED LISTS	MM (INCH)		DFTO sammy	DATE 04/27/02	FULL RISE ELECTRONIC CO., LTD		
	TOLERANCES EXCEPT AS NOTED		CHKDQIANPU	DATE2005/3/5			
			MFO	DATE	TITLE 20.57 PCB JACK 10P		
			APPVLLUSHENG	DATE2005/3/5			
			MATERIAL :		DRAWING NO. GE593092 /PART NO. SEE NOTE		
	ANGLES ±		QT'Y :				
			FINISH :		DO NOT SCALE DRAWING		
	THIRD ANGLE PROJECTION		SCALE : 2:1				