



# THCV218

V-by-One® HS High-speed video data receiver

#### **General Description**

THCV218 is designed to support video data transmission between the host and display.

One high-speed lane can carry up to 32bit data and 3 bits of synchronizing signals at a pixel clock frequency from 20MHz to 85MHz.

This chip, which has two high-speed data lanes, can receive video data up to 1080p/10b/60Hz.

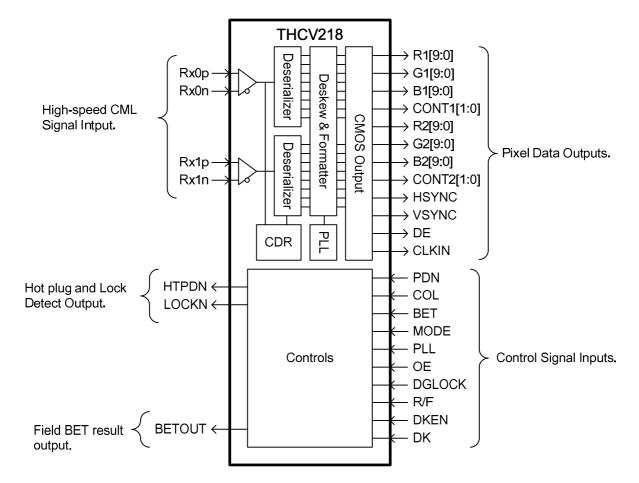
The maximum serial data rate is 3.4Gbps/lane.

Width	Link	CMOS IO Frequency
24bit and 32bit	Si/So Di/Do	20MHz to 85MHz
	Di/So	40MHz to 170MHz

#### Features

- Color depth selectable: 24(8\*3)/32(10\*3)bit
- Single-in/Single-out, Dual-in/Single-out, and Dual-in/Dual-out selectable.
- AC coupling for high-speed lines
- CORE 1.8v, CMOS IO 3.3v
- Package: TFBGA105
- Wide frequency range
- CDR requires no external frequency reference
- 3.3V logic compatible output interface.

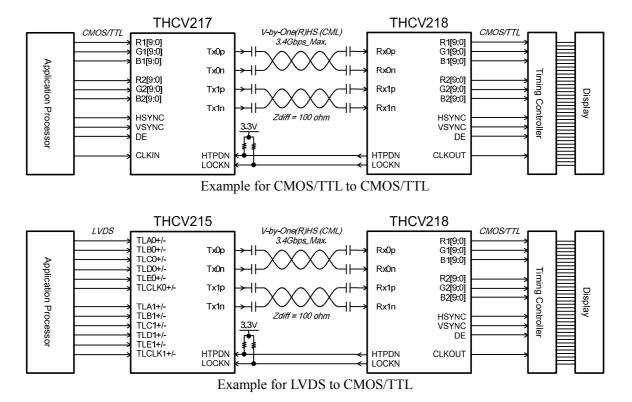
Si/So: Single-in/Single-out, Di/So: Dual-in/Single-out, Di/So: Dual-in/Single-out



# <u>Block Diagram</u>



## **Example System Diagram**



### Notices and Requests

- 1. The product specifications described in this material are subject to change without prior notice.
- 2. The circuit diagrams described in this material are examples of the application which may not always apply to the customer's design. We are not responsible for possible errors and omissions in this material. Please note if errors or omissions should be found in this material, we may not be able to correct them immediately.
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- 8. Customers are asked, if required, to judge by themselves if this product falls under the category of strategic goods under the Foreign Exchange and Foreign Trade Control Law.

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