

Pin Definition:

1. Gate
2. Drain
3. Source

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)	I _D (A)
100	10 @ V _{GS} =10V	81

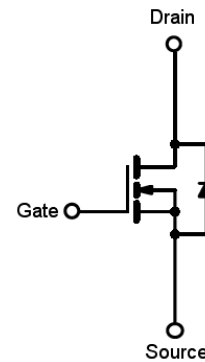
Features

- Advanced Trench Technology
- Low R_{DS(ON)} 10mΩ (Max.)
- Low gate charge typical @ 154nC (Typ.)
- Low Crss typical @ 170pF (Typ.)

Ordering Information

Part No.	Package	Packing
TSM85N10CZ C0	TO-220	50pcs / Tube

Block Diagram



N-Channel MOSFET

Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _{GS}	±25	V	
Continuous Drain Current	I _D	T _C =25°C	81	A
		T _C =70°C	65	
		T _A =25°C	8.7	
		T _A =70°C	7	
Drain Current-Pulsed Note 1	I _{DM}	320	A	
Avalanche Current, L=0.3mH	I _{AS} , I _{AR}	64	A	
Avalanche Energy, L=0.3mH	E _{AS} , E _{AR}	620	mJ	
Maximum Power Dissipation	P _D	T _C =25°C	210	W
		T _C =70°C	130	
		T _A =25°C	2.4	
		T _A =70°C	1.5	
Storage Temperature Range	T _{STG}	-55 to +150	°C	
Operating Junction Temperature Range	T _J	-55 to +150	°C	

* Limited by maximum junction temperature

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Case	Rθ _{JC}	0.6	°C/W
Thermal Resistance - Junction to Ambient	Rθ _{JA}	52.5	°C/W

Notes: Surface mounted on FR4 board t ≤ 10sec

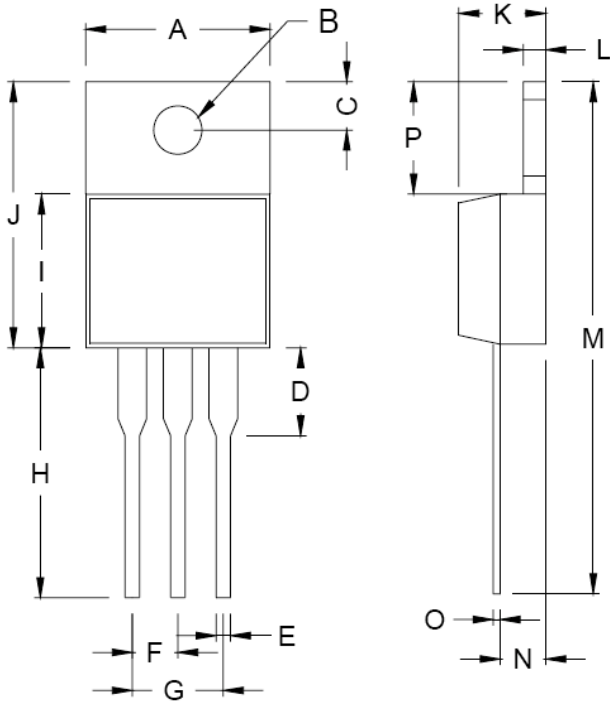
Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV_{DSS}	100	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 40A$	$R_{DS(ON)}$	--	9	10	mΩ
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	2	3	4	V
Zero Gate Voltage Drain Current	$V_{DS} = 80V, V_{GS} = 0V$	I_{DSS}	--	--	1	μA
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}	--	--	±100	nA
Dynamic						
Total Gate Charge	$V_{DS} = 30V, I_D = 40A,$ $V_{GS} = 10V$	Q_g	--	154	--	nC
Gate-Source Charge		Q_{gs}	--	4	--	
Gate-Drain Charge		Q_{gd}	--	45	--	
Input Capacitance	$V_{DS} = 30V, V_{GS} = 0V,$ $f = 1.0MHz$	C_{iss}	--	3900	--	pF
Output Capacitance		C_{oss}	--	300	--	
Reverse Transfer Capacitance		C_{rss}	--	170	--	
Switching						
Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 30V,$ $R_G = 6\Omega$	$t_{d(on)}$	--	38	--	nS
Turn-On Rise Time		t_r	--	65	--	
Turn-Off Delay Time		$t_{d(off)}$	--	218	--	
Turn-Off Fall Time		t_f	--	72	--	
Drain-Source Diode Characteristics and Maximum Rating						
Drain-Source Diode Forward Voltage	$V_{GS}=0V, I_S=20A$	V_{SD}	-	0.8	1.2	V
Reverse Recovery Time	$I_S = 40A, T_J=25^\circ C$ $di/dt = 100A/\mu s$	t_{fr}		62		nS
Reverse Recovery Charge		Q_{fr}		130		nC

Notes:

1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
2. $R\theta_{JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R\theta_{JC}$ is guaranteed by design while $R\theta_{CA}$ is determined by the user's board design. $R\theta_{JA}$ shown below for single device operation on FR-4 in still air

TO-220 Mechanical Drawing



DIM	TO-220 DIMENSION			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.000	10.500	0.394	0.413
B	3.740	3.910	0.147	0.154
C	2.440	2.940	0.096	0.116
D	-	6.350	-	0.250
E	0.381	1.106	0.015	0.040
F	2.345	2.715	0.092	0.058
G	4.690	5.430	0.092	0.107
H	12.700	14.732	0.500	0.581
J	14.224	16.510	0.560	0.650
K	3.556	4.826	0.140	0.190
L	0.508	1.397	0.020	0.055
M	27.700	29.620	1.060	1.230
N	2.032	2.921	0.080	0.115
O	0.255	0.610	0.010	0.024
P	5.842	6.858	0.230	0.270

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