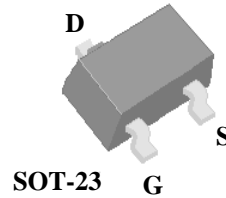


## AP2301AGN-HF

- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ RoHS Compliant & Halogen-Free

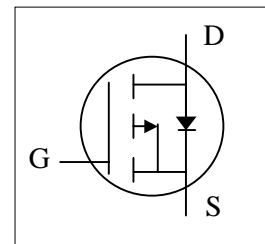


$BV_{DSS}$	-20V
$R_{DS(ON)}$	97mΩ
$I_D$	- 3.3A

### Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-23 package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	- 20	V
$V_{GS}$	Gate-Source Voltage	+8	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 4.5V	-3.3	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current <sup>3</sup> , $V_{GS}$ @ 4.5V	-2.7	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-15	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	1.38	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	90	°C/W

## AP2301AGN-HF

Electrical Characteristics @  $T_j=25^\circ\text{C}$  (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-4.5V, I_D=-3A$	-	-	97	$m\Omega$
		$V_{GS}=-2.5V, I_D=-2.6A$	-	-	130	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.3	-	-1	V
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V, I_D=-3A$	-	10	-	S
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-16V, V_{GS}=0V$	-	-	-10	$\mu A$
$I_{GSS}$	Gate-Source Leakage	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge	$I_D=-3A$	-	8.5	21	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=-10V$	-	1.2	-	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$V_{GS}=-4.5V$	-	3	-	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=-10V$	-	10	-	ns
$t_r$	Rise Time	$I_D=-1A$	-	20	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	27	-	ns
$t_f$	Fall Time	$V_{GS}=-5V$	-	22	-	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0V$	-	660	1470	pF
$C_{oss}$	Output Capacitance	$V_{DS}=-10V$	-	135	-	pF
$C_{rss}$	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	120	-	pF
$R_g$	Gate Resistance	$f=1.0\text{MHz}$	-	7.2	-	$\Omega$

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{SD}$	Forward On Voltage <sup>2</sup>	$I_S=-0.8A, V_{GS}=0V$	-	-	-1.2	V
$t_{rr}$	Reverse Recovery Time	$I_S=-3A, V_{GS}=0V,$	-	24	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	11	-	nC