

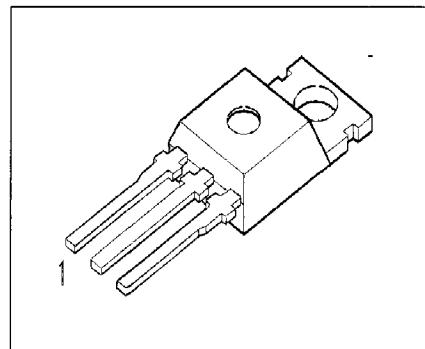
New Jersey Semi-Conductor Products, Inc.

20 STERN AVE.
SPRINGFIELD, NEW JERSEY 07081
U.S.A.

TELEPHONE: (973) 376-2922

- N channel
- Enhancement mode
- Avalanche-rated

BUZ 41 A



Pin 1	Pin 2	Pin 3
G	D	S

Type	V _{DS}	I _D	R _{DS(on)}	Package
BUZ 41 A	500 V	4.5 A	1.5 Ω	TO-220 AB

Maximum Ratings

Parameter	Symbol	Values	Unit
Continuous drain current	I _D	4.5	A
T _C = 36 °C			
Pulsed drain current	I _{Dpuls}		
T _C = 25 °C			
Avalanche current, limited by T _{jmax}	I _{AR}		
Avalanche energy, periodic limited by T _{jmax}	E _{AR}	8	mJ
Avalanche energy, single pulse I _D = 4.5 A, V _{DD} = 50 V, R _{GS} = 25 Ω L = 28.4 mH, T _j = 25 °C	E _{AS}	320	
Gate source voltage	V _{GS}	± 20	V
Power dissipation	P _{tot}	75	W
T _C = 25 °C		-55 ... + 150	°C
Operating temperature	T _j		
Storage temperature	T _{stg}	-55 ... + 150	
Thermal resistance, chip case	R _{thJC}	≤ 1.67	K/W
Thermal resistance, chip to ambient	R _{thJA}	75	
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56	

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Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Drain- source breakdown voltage $V_{GS} = 0 \text{ V}$, $I_D = 0.25 \text{ mA}$, $T_j = 25^\circ\text{C}$	$V_{(\text{BR})\text{DSS}}$	500	-	-	V
Gate threshold voltage $V_{GS}=V_{DS}$, $I_D = 1 \text{ mA}$	$V_{GS(\text{th})}$	2.1	3	4	
Zero gate voltage drain current $V_{DS} = 500 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_j = 25^\circ\text{C}$ $V_{DS} = 500 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_j = 125^\circ\text{C}$	I_{DSS}	-	0.1	1	μA
Gate-source leakage current $V_{GS} = 20 \text{ V}$, $V_{DS} = 0 \text{ V}$	I_{GSS}	-	10	100	nA
Drain-Source on-resistance $V_{GS} = 10 \text{ V}$, $I_D = 3 \text{ A}$	$R_{\text{DS(on)}}$	-	1.3	1.5	Ω

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Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Dynamic Characteristics

Transconductance $V_{DS} \geq 2 * I_D * R_{DS(on)max}$, $I_D = 3 \text{ A}$	g_{fs}	2.5	4.3	-	S
Input capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{iss}	-	850	1300	pF
Output capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{oss}	-	100	150	
Reverse transfer capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{rss}	-	40	60	
Turn-on delay time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	$t_{d(on)}$	-	15	20	ns
Rise time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	t_r	-	50	70	
Turn-off delay time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	$t_{d(off)}$	-	140	190	
Fall time $V_{DD} = 30 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 2.6 \text{ A}$ $R_{GS} = 50 \Omega$	t_f	-	50	70	

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Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Reverse Diode

Inverse diode continuous forward current $T_C = 25^\circ\text{C}$	I_S	-	-	4.5	A
Inverse diode direct current,pulsed $T_C = 25^\circ\text{C}$	I_{SM}	-	-	18	
Inverse diode forward voltage $V_{GS} = 0 \text{ V}, I_F = 9 \text{ A}$	V_{SD}	-	1	1.2	V
Reverse recovery time $V_R = 100 \text{ V}, I_F=I_S, dI_F/dt = 100 \text{ A}/\mu\text{s}$	t_{rr}	-	350	-	ns
Reverse recovery charge $V_R = 100 \text{ V}, I_F=I_S, dI_F/dt = 100 \text{ A}/\mu\text{s}$	Q_{rr}	-	3	-	μC