- Designed for Complementary Use with the BD249 Series
- 125 W at 25°C Case Temperature
- 25 A Continuous Collector Current
- 40 A Peak Collector Current
- Customer-Specified Selections Available

SOT-93 PACKAGE (TOP VIEW) B 1 C 2 3

Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT	
	BD250		-55		
Collector-emitter voltage (R_{BE} = 100 Ω)	BD250A	\ \ _V	-70	_v	
	BD250B	V _{CER}	-90	٧	
	BD250C		-115		
	BD250		-45		
Collector-emitter voltage (I _C = -30 mA)	BD250A	\ \ _V	-60	v	
	BD250B	V _{CEO}	-80		
	BD250C		-100		
Emitter-base voltage			-5	V	
Continuous collector current			-25	Α	
Peak collector current (see Note 1)	I _{CM}	-40	Α		
Continuous base current			-5	Α	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			125	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3	W	
Unclamped inductive load energy (see Note 4)			90	mJ	
Operating junction temperature range			-65 to +150	°C	
Storage temperature range	T _{stg}	-65 to +150	°C		
Lead temperature 3.2 mm from case for 10 seconds	T _L	°C			

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$

- 2. Derate linearly to 150°C case temperature at the rate of 1 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 24 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = -0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = -20 V.

BD250, BD250A, BD250B, BD250C PNP SILICON POWER TRANSISTORS

JUNE 1973 - REVISED MARCH 1997

electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT	
V _{(BR)CEO}	Collector-emitter breakdown voltage		$I_C = -30 \text{ mA}$ $I_B = 0$	BD250	-45				
		30 mΔ		BD250A	-60			V	
		(see Note 5)		BD250B	-80				
		`		BD250C	-100				
	V	V _{CE} = -55 V	V _{BE} = 0	BD250			-0.7		
	Collector-emitter	$V_{CE} = -70 \text{ V}$	$V_{BE} = 0$	BD250A			-0.7	mA	
ICES	cut-off current	V _{CE} = -90 V	$V_{BE} = 0$	BD250B			-0.7		
		V _{CE} = -115 V	$V_{BE} = 0$	BD250C			-0.7		
1	Collector cut-off	V _{CE} = -30 V	I _B = 0	BD250/250A			-1	mA	
ICEO	current	V _{CE} = -60 V	$I_B = 0$	BD250B/250C			-1	IIIA	
I _{EBO}	Emitter cut-off current	V _{EB} = -5 V	I _C = 0				-1	mA	
	Carroni	V _{CE} = -4 V	I _C = -1.5 A		25				
h _{FE}	Forward current	$V_{CE} = -4 V$	$I_{\rm C} = -15 {\rm A}$	(see Notes 5 and 6)	10				
''FE	transfer ratio	$V_{CE} = -4 V$	$I_C = -25 \text{ A}$	(555 115155 5 4114 5)	5				
V	Collector-emitter	I _B = -1.5 A	I _C = -15 A	(N-1 F I O)			-1.8	V	
V _{CE(sat)}	saturation voltage	I _B = -5 A	$I_{C} = -25 \text{ A}$	(see Notes 5 and 6)			-4	V	
V	Base-emitter	V _{CE} = -4 V	I _C = -15 A	(N-t E (C)			-2	V	
V_{BE}	voltage	V _{CE} = -4 V	$I_C = -25 A$	(see Notes 5 and 6)			-4	V	
h	Small signal forward	V 10 V	V 10 V	I _C = -1 A	f = 1 kHz	25			
h _{fe}	current transfer ratio	V _{CE} = -10 V	IC = -1 M	I = I K Z	23				
lh I	Small signal forward	V _{CE} = -10 V	Ι 4 Δ	f = 1 MHz	3				
h _{fe}	current transfer ratio	ACF = 310 A	I _C = -1 A	1 — 1 1911 12	3				

NOTES: 5. These parameters must be measured using pulse techniques, $t_p = 300 \,\mu s$, duty cycle $\leq 2\%$.

thermal characteristics

	PARAMETER			MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			42	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = -5 A	$I_{B(on)} = -0.5 A$	$I_{B(off)} = 0.5 A$		0.2		μs
t _{off}	Turn-off time	$V_{BE(off)} = 5 V$	$R_L = 5 \Omega$	t_p = 20 µs, dc \leq 2%		0.4		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN VS COLLECTOR CURRENT TCS636AD TCS636AD

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

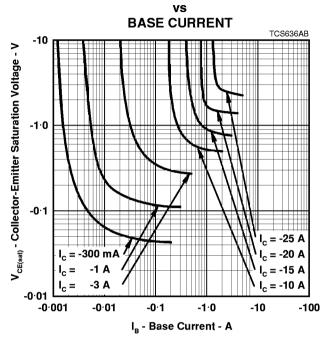
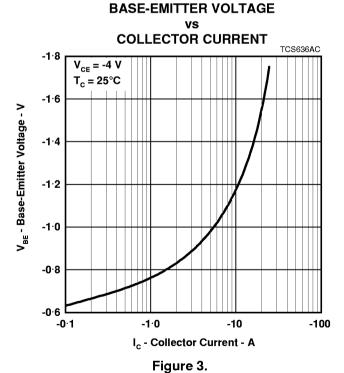
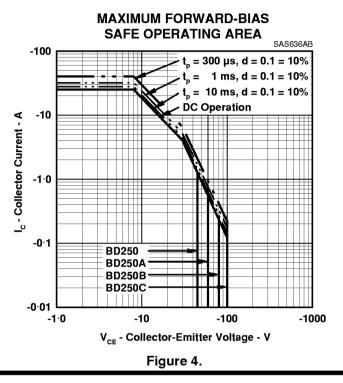


Figure 2.

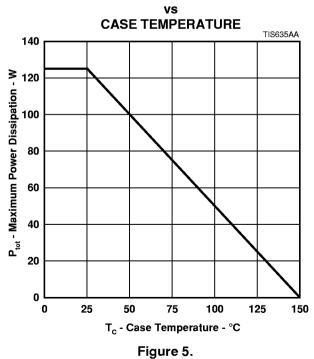


MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



PRODUCT INFORMATION

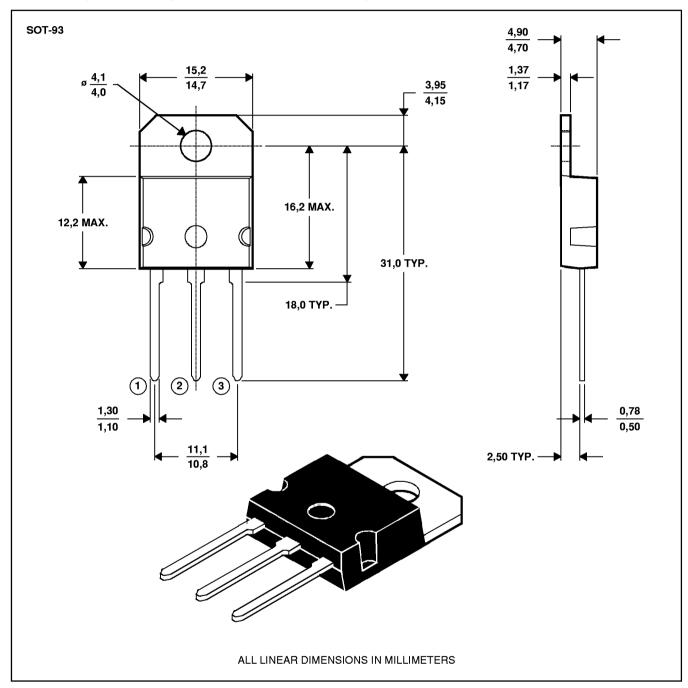
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MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

MDXXAW



BD250, BD250A, BD250B, BD250C PNP SILICON POWER TRANSISTORS

JUNE 1973 - REVISED MARCH 1997

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