

CYStech Electronics Corp.

Spec. No. : C193LD Issued Date : 2012.12.11

Revised Date : Page No. : 1/3

3.0Amp. Glass Passivated Super Fast Rectifiers

SF31G thru SF38G

Features

- High current capability
- High reliability
- Low forward voltage drop
- High surge current capability

Mechanical Data

• Case: Molded plastic DO-201AD

• Terminals: Solder plated, solderable per MIL-STD-750 method 2026

Polarity: Indicated by cathode band.Epoxy: UL94V-0 rate flame retardant

• Weight: 0.041 oz., 1.15 gram

Maximum Ratings and Electrical Characteristics

(Rating at 25°C ambient temperature unless otherwise specified.)

		Type								
Parameter	Symbol	SF	SF	SF	SF	SF	SF	SF	SF	Units
		31G	32G	33G	34G	35G	36G	37G	38G	
Repetitive peak reverse voltage	Vrrm	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	VR	50	100	150	200	300	400	500	600	V
Maximum instantaneous forward voltage, IF=3A (Note 1)	VF	0.95 1.3 1.7					.7	V		
Reverse Recovery Time	trr	35					ns			
Average forward rectified current	T	3								Α.
@TA=95°C	Ifav									A
Peak forward surge current @8.3ms	Ifsm	125								
single half sine wave superimposed										Α
on rated load (JEDEC method)										
Maximum DC reverse current										
$V_R=V_{RRM}, T_A=25^{\circ}C$ (Note 1)	Ir	5								μΑ
$V_R=V_{RRM}$, $T_A=100^{\circ}$ C (Note 1)		100							μA	
Storage temperature	Tstg	- 55 ∼ +150					°C			
Operating temperature	-55 ~ +150						$^{\circ}\!\mathbb{C}$			

Notes : 1. Pulse test, pulse width=300 μ sec, 2% duty cycle

2 . Reverse recovery test condition: IF=0.5A, IR=1.0A, IRR=0.25A



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Revised Date : Page No. : 2/3

Characteristic Curves

Fig.1- Forward Current Derating Curve

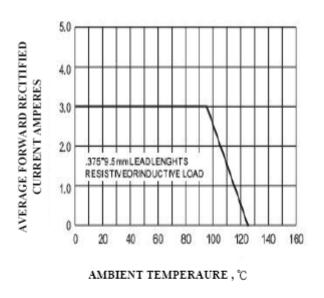
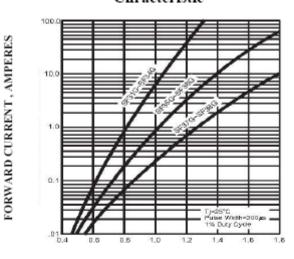
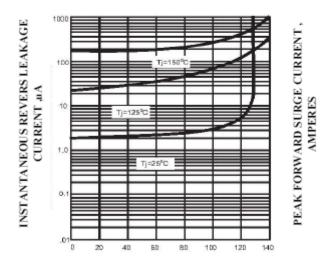


Fig.2- Typical Instantaneous Forward Chracteristic



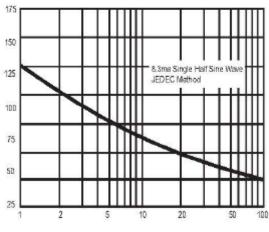
FORWARD VOLTAGE, VOLTS

Fig.3- Typical Reverse



Surge Current

Fig.4- Maximum Non -Repetitive



PERCENT OF RATED PEAK REVERSE VOLTAGE, %

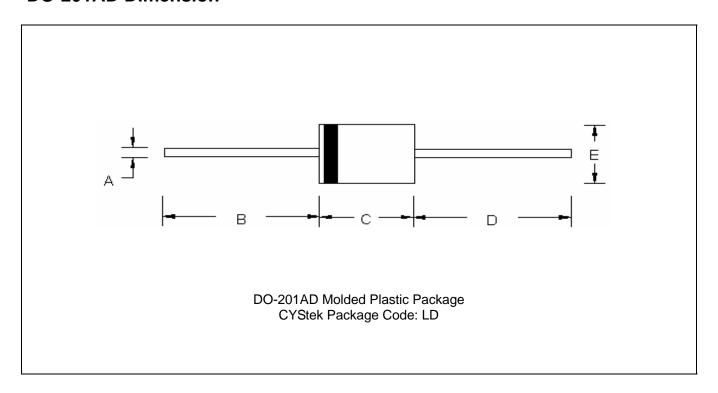


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Revised Date : Page No. : 3/3

DO-201AD Dimension



DIM	Inches		Millimeters		DIM	Incl	hes	Millimeters	
	Min.	Max.	Min.	Max.	וווט	Min.	Max.	Min.	Max.
Α	φ0.048	φ0.052	φ1.20	φ1.30	D	1.000	-	25.40	-
В	1.000	-	25.40	-	Е	φ0.197	φ0.220	φ5.00	φ5.60
С	0.285	0.375	7.20	9.50					·

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Axial leads, solderable per MIL-STD-202, Method 208 guaranteed.
- Mold Compound : Epoxy resin family, flammability solid burning class: UL94V-0

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