

International
IR Rectifier

**RADIATION HARDENED
POWER MOSFET
THRU-HOLE (TO-39)**

PD-97311

**IRHF67230
200V, N-CHANNEL
R₆ TECHNOLOGY**

Product Summary

Part Number	Radiation Level	R _{Ds(on)}	I _D
IRHF67230	100K Rads (Si)	0.145Ω	9.1A
IRHF63230	300K Rads (Si)	0.145Ω	9.1A

International Rectifier's R₆TM technology provides superior power MOSFETs for space applications. These devices have improved immunity to Single Event Effect (SEE) and have been characterized for useful performance with Linear Energy Transfer (LET) up to 90MeV/(mg/cm²). Their combination of very low R_{Ds(on)} and faster switching times reduces power loss and increases power density in today's high speed switching applications such as DC-DC converters and motor controllers. These devices retain all of the well established advantages of MOSFETs such as voltage control, ease of paralleling and temperature stability of electrical parameters.



Features:

- Low R_{Ds(on)}
- Fast Switching
- Single Event Effect (SEE) Hardened
- Low Total Gate Charge
- Simple Drive Requirements
- Ease of Parallelizing
- Hermetically Sealed
- Ceramic Package
- Light Weight

Absolute Maximum Ratings

Pre-Irradiation

	Parameter		Units
I _D @ V _{GS} = 12V, T _C = 25°C	Continuous Drain Current	9.1	A
I _D @ V _{GS} = 12V, T _C = 100°C	Continuous Drain Current	5.7	
I _{DM}	Pulsed Drain Current ①	36.4	
P _D @ T _C = 25°C	Max. Power Dissipation	25	W
	Linear Derating Factor	0.2	W/C
V _{GS}	Gate-to-Source Voltage	±20	V
E _{AS}	Single Pulse Avalanche Energy ②	23	mJ
I _{AR}	Avalanche Current ①	9.1	A
E _{AR}	Repetitive Avalanche Energy ①	2.5	mJ
dV/dt	Peak Diode Recovery dV/dt ③	4.8	V/ns
T _J	Operating Junction	-55 to 150	°C
T _{TSG}	Storage Temperature Range		
	Lead Temperature	300 (0.063in/1.6mm from case for 10s)	
	Weight	0.98 (Typical)	g

For footnotes refer to the last page

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1

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Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.22	—	$\text{V}/^\circ\text{C}$	Reference to 25°C , $\text{I}_D = 1.0\text{mA}$
$\text{R}_{\text{DS}(\text{on})}$	Static Drain-to-Source On-State Resistance	—	—	0.145	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 5.7\text{A}$ ④
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	—	4.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 1.0\text{mA}$
$\Delta \text{V}_{\text{GS}(\text{th})}/\Delta T_J$	Gate Threshold Voltage Coefficient	—	-9.6	—	$\text{mV}/^\circ\text{C}$	
g_{fs}	Forward Transconductance	5.0	—	—	S	$\text{V}_{\text{DS}} = 15\text{V}, \text{I}_{\text{DS}} = 5.7\text{A}$ ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	10	μA	$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		—	—	25		$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{T}_j = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
Q_g	Total Gate Charge	—	—	45	nC	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 9.1\text{A}$
Q_{gs}	Gate-to-Source Charge	—	—	12		$\text{V}_{\text{DS}} = 100\text{V}$
Q_{gd}	Gate-to-Drain ('Miller') Charge	—	—	30		
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	—	17	ns	$\text{V}_{\text{DD}} = 100\text{V}, \text{I}_D = 9.1\text{A}, \text{V}_{\text{GS}} = 12\text{V}, \text{R}_G = 7.5\Omega$
t_r	Rise Time	—	—	30		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	—	40		
t_f	Fall Time	—	—	25		
$\text{L}_{\text{S}} + \text{L}_{\text{D}}$	Total Inductance	—	7.0	—	nH	Measured from Drain lead (6mm/0.25in from package)to Source lead (6mm/0.25in from package)with Source wire interanally bonded from Source pin to Drain pad
C_{iss}	Input Capacitance	—	1374	—	pF	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = 25\text{V}$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	214	—		
C_{rss}	Reverse Transfer Capacitance	—	4.2	—		
R_g	Gate Resistance	—	1.1	—	Ω	$f = 1.0\text{MHz}$, open drain

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I_{S}	Continuous Source Current (Body Diode)	—	—	9.1	A	
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	36.4		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	$\text{T}_j = 25^\circ\text{C}, \text{I}_{\text{S}} = 9.1\text{A}, \text{V}_{\text{GS}} = 0\text{V}$ ④
t_{rr}	Reverse Recovery Time	—	—	317	ns	$\text{T}_j = 25^\circ\text{C}, \text{I}_{\text{F}} = 9.1\text{A}, \text{di/dt} \leq 100\text{A}/\mu\text{s}$ $\text{V}_{\text{DD}} \leq 50\text{V}$ ④
Q_{RR}	Reverse Recovery Charge	—	—	2.91	μC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $\text{L}_{\text{S}} + \text{L}_{\text{D}}$.				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	5.0	$^\circ\text{C}/\text{W}$	

Note: Corresponding Spice and Saber models are available on International Rectifier Web site.

For footnotes refer to the last page

Radiation Characteristics

IRHF67230

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-39 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_J = 25^\circ\text{C}$, Post Total Dose Irradiation ⁽⁵⁾⁽⁶⁾

	Parameter	Upto 300K Rads(Si) ¹		Units	Test Conditions ^⑧
		Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	4.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = 1.0\text{mA}$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
I_{DSS}	Zero Gate Voltage Drain Current	—	1.0	μA	$\text{V}_{\text{DS}} = 200\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ^④ On-State Resistance (TO-39)	—	0.145	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 5.7\text{A}$
V_{SD}	Diode Forward Voltage ^④	—	1.2	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 9.1\text{A}$

1. Part numbers IRHF67230, IRHF63230

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Typical Single Event Effect Safe Operating Area

LET (MeV/(mg/cm ²))	Energy (MeV)	Range (μm)	V _{DS} (V)			
			@ V _{GS} = 0V	@ V _{GS} = -5V	@ V _{GS} = -10V	@ V _{GS} = -15V
42 ± 5%	2450 ± 5%	205 ± 5%	200	200	200	190
61 ± 5%	825 ± 5%	66 ± 7.5%	200	200	200	190
90 ± 5%	1470 ± 5%	80 ± 5%	170	170	-	-

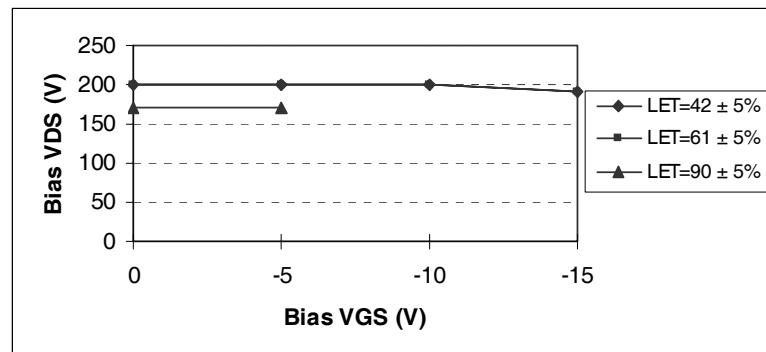


Fig a. Typical Single Event Effect, Safe Operating Area

For footnotes refer to the last page

IRHF67230

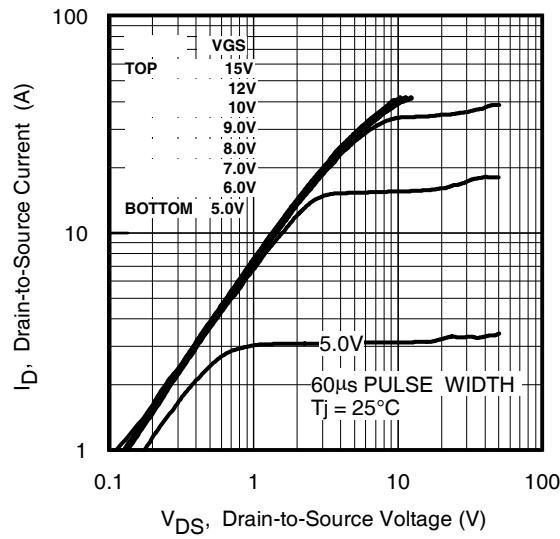


Fig 1. Typical Output Characteristics

Pre-Irradiation

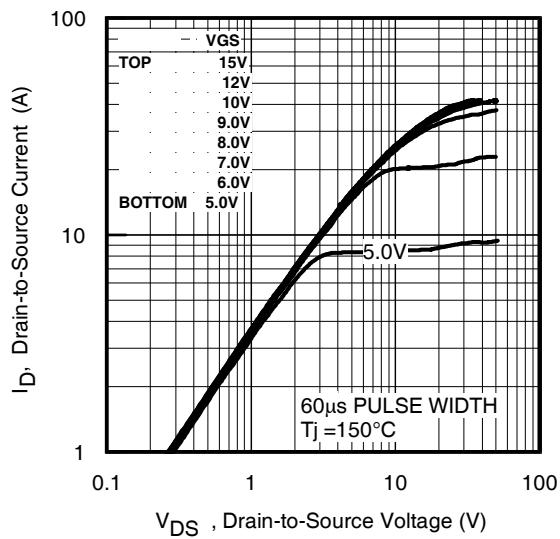


Fig 2. Typical Output Characteristics

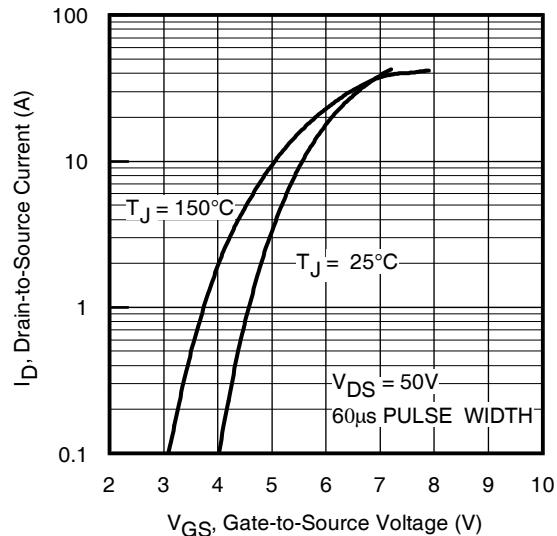


Fig 3. Typical Transfer Characteristics

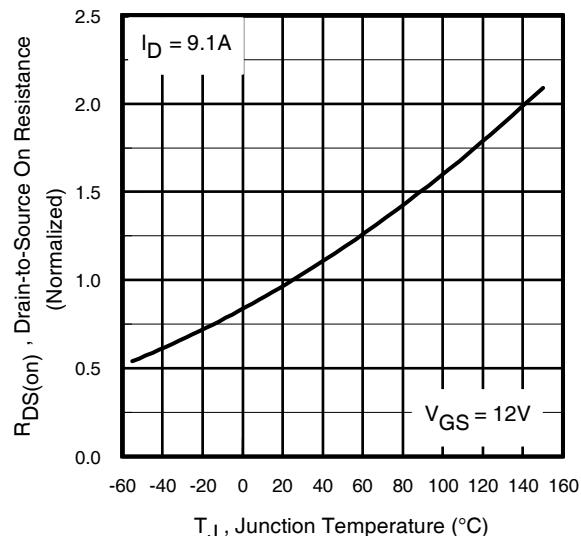


Fig 4. Normalized On-Resistance Vs. Temperature

Pre-Irradiation

IRHF67230

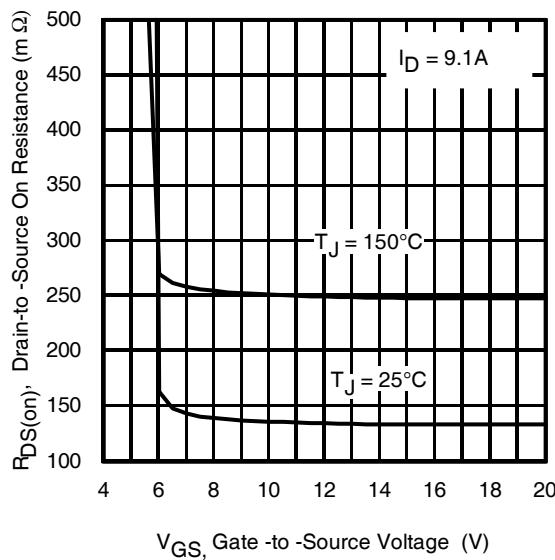


Fig 5. Typical On-Resistance Vs Gate Voltage

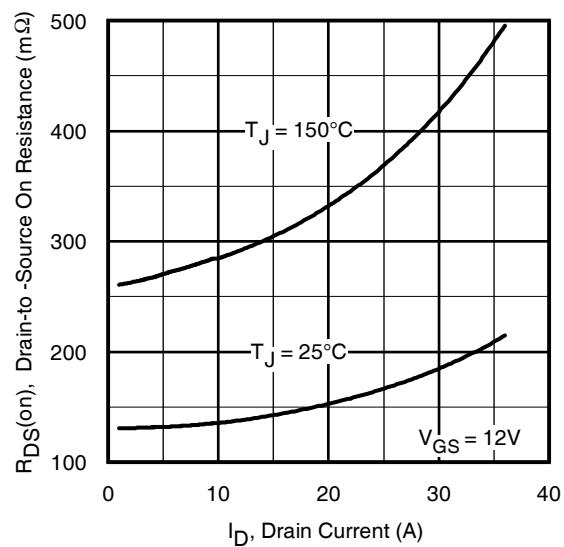


Fig 6. Typical On-Resistance Vs Drain Current

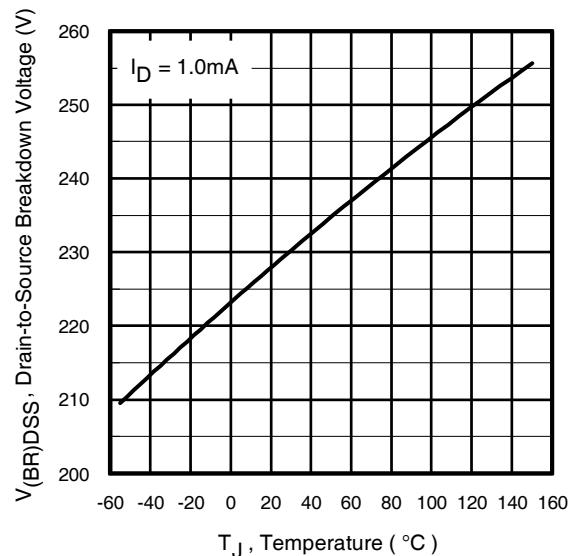


Fig 7. Typical Drain-to-Source Breakdown Voltage Vs Temperature

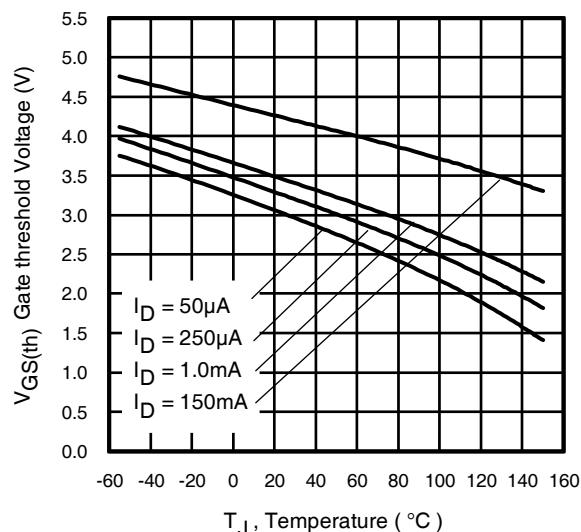


Fig 8. Typical Threshold Voltage Vs Temperature

IRHF67230

Pre-Irradiation

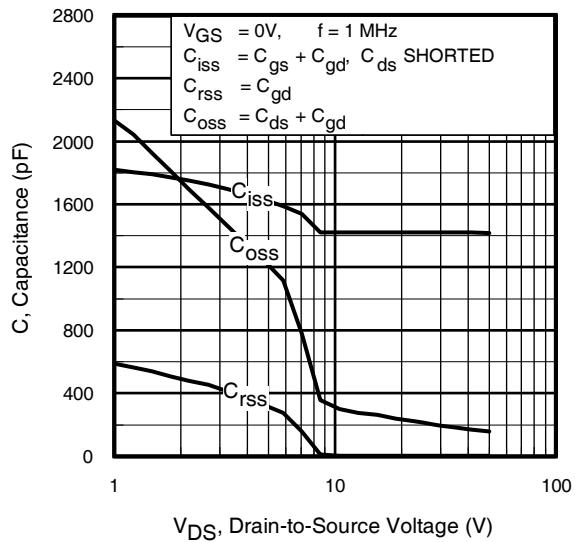


Fig 9. Typical Capacitance Vs.
Drain-to-Source Voltage

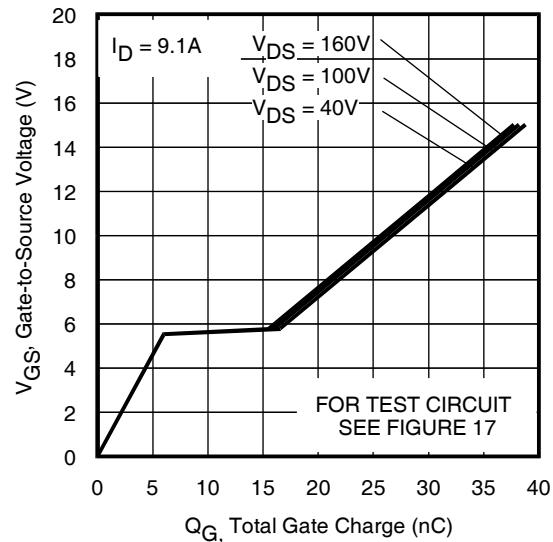


Fig 10. Typical Gate Charge Vs.
Gate-to-Source Voltage

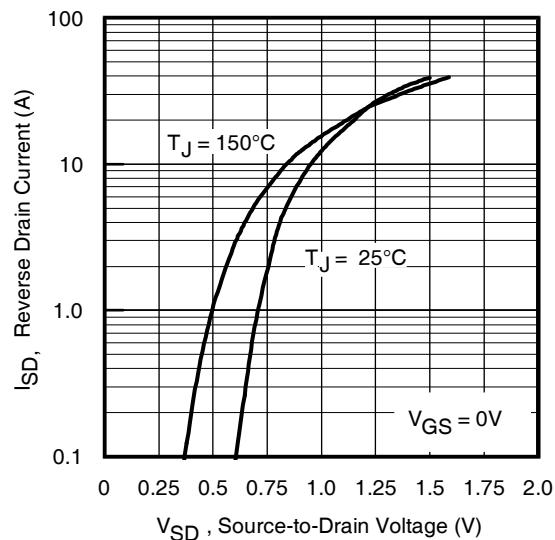


Fig 11. Typical Source-Drain Diode
Forward Voltage

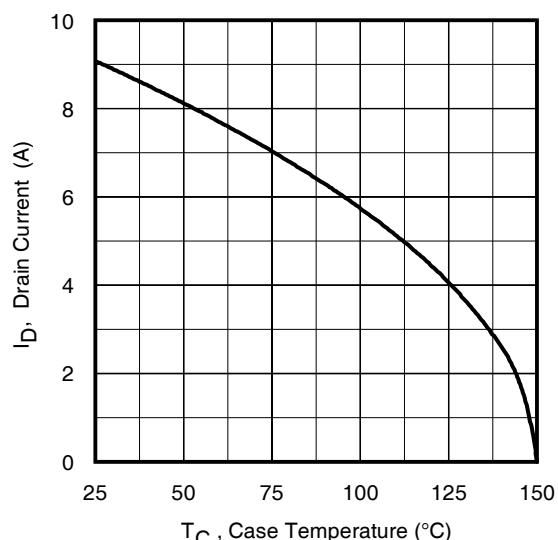


Fig 12. Maximum Drain Current Vs.
Case Temperature

Pre-Irradiation

IRHF67230

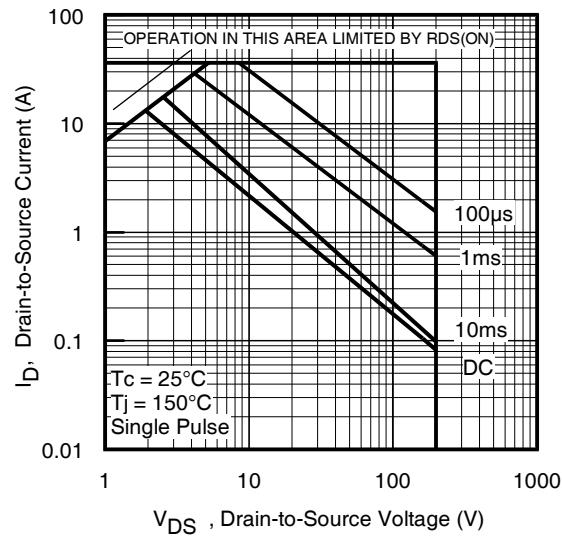


Fig 13. Maximum Safe Operating Area

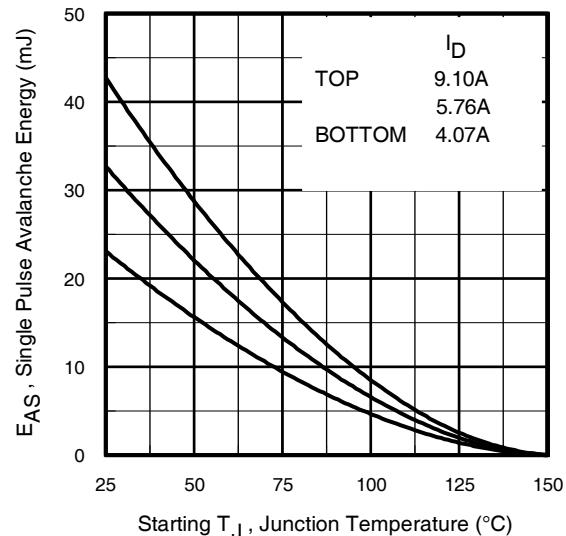


Fig 14. Maximum Avalanche Energy Vs. Drain Current

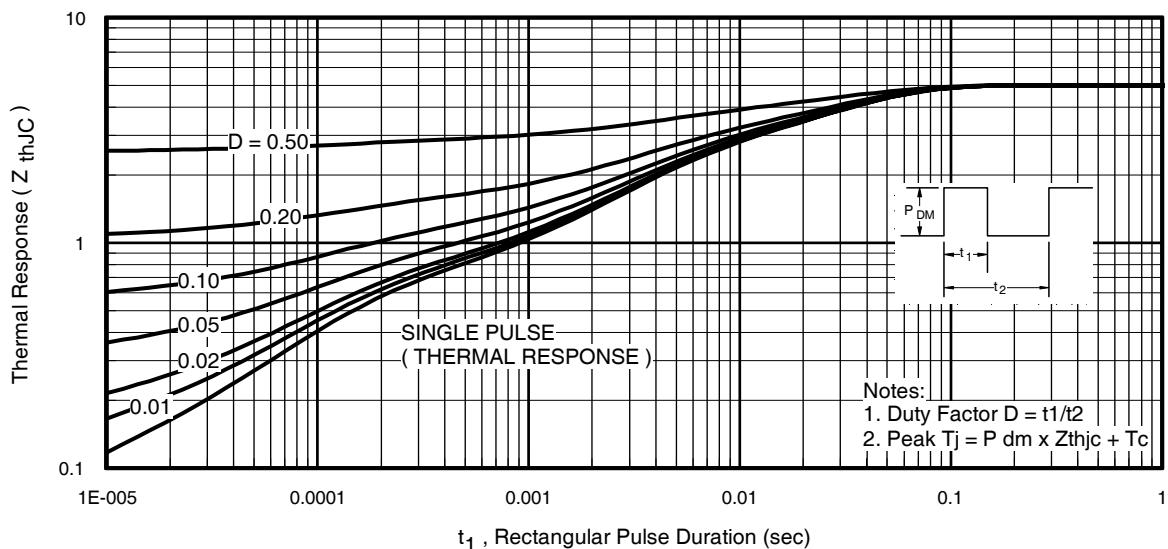
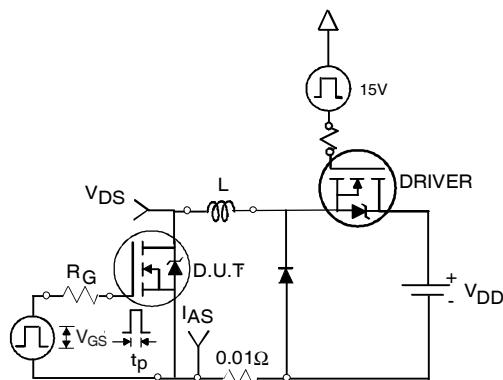
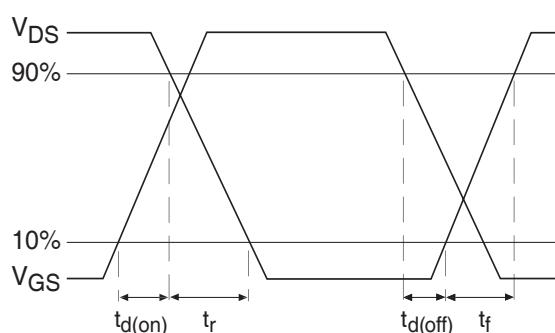
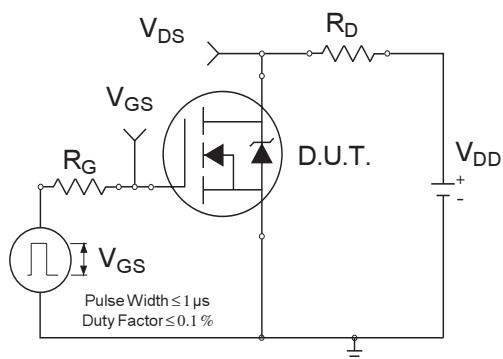
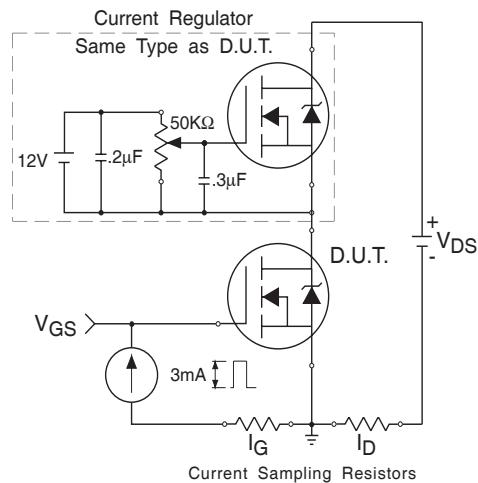
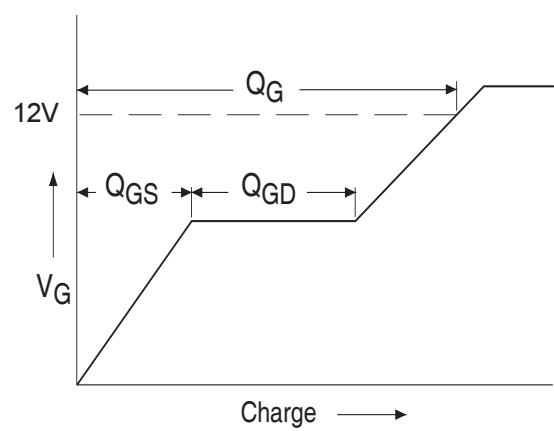
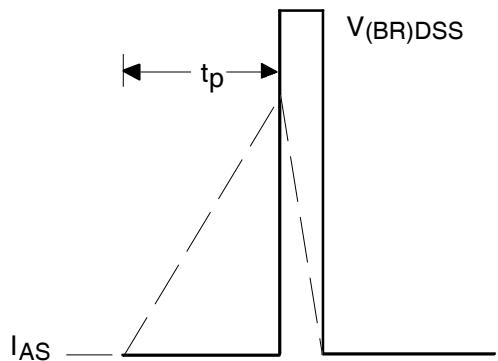


Fig 15. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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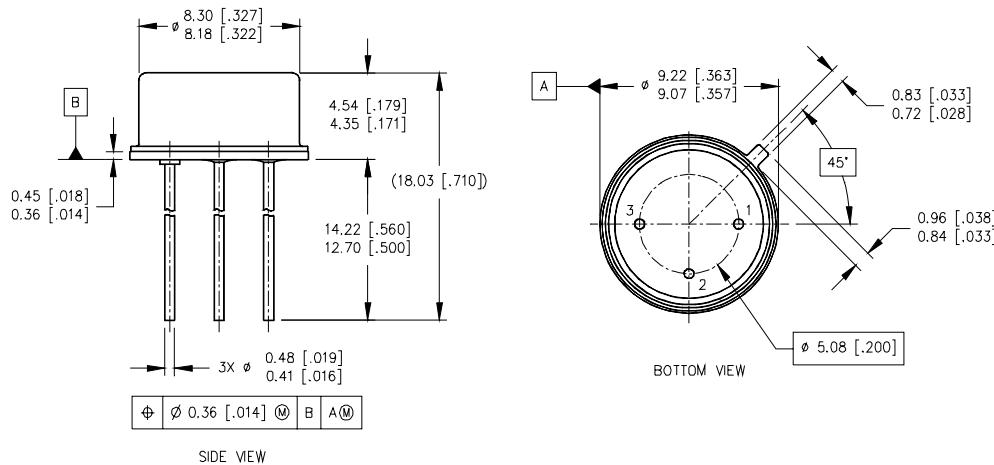
Pre-Irradiation



Footnotes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = 50V$, starting $T_J = 25^\circ C$, $L = 0.56mH$
Peak $I_L = 9.1A$, $V_{GS} = 12V$
- ③ $ISD \leq 9.1A$, $dI/dt \leq 347A/\mu s$,
 $V_{DD} \leq 200V$, $T_J \leq 150^\circ C$

- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
160 volt V_{DS} applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — TO-205AF (Modified TO-39)**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME 14.5M-1994.
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-205AF (TO-39).

LEGEND

- 1 - SOURCE
- 2 - GATE
- 3 - DRAIN

International
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