

MN1874878

Type	MN1874878		
ROM (×8-Bit)	48 K		
RAM (×8-Bit)	928		
Minimum Instruction Execution Time	0.5 μs at 2/3 frequency dividing (at 4.5 V to 5.5 V, 12 MHz)		
Interrupts	<ul style="list-style-type: none"> • RESET • External 0 • External 1 • External 2 • External 3 • Timer 0 • Timer 1 • Timer 2 • I²C • Serial • Remote Control • Line 21 • MOSD • COSD 		
Timer Counter	<p>Timer Counter 0 : 8-Bit × 1</p> <p>Clock Source .1/1, 1/4, 1/16, 1/64 of System Clock</p> <p>Interrupt Source Overflow of Timer Counter 0</p> <p>Timer Counter 1 : 8-Bit × 1</p> <p>Clock Source 1/2, 1/16, 1/64, 1/256, 1/512 of System Clock</p> <p>Interrupt Source Overflow of Timer Counter 1</p> <p>Time Base Counter</p> <p>Clock Source 1/4096 of System Clock</p> <p>Interrupt Source 1/1, 1/2, 1/4, 1/8 of Timer Counter 2</p> <p>Watchdog Counter for Clock (Clock function)</p> <p>AC Counter</p>		
Serial Interface	<p>Serial 0 : 8-Bit × 1 (Transmission / Reception of variable length, Transfer direction of MSB/LSB selectable, Colck Polarity selectable, Start Condition function)</p> <p>Clock Source System Clock</p> <p>I²C × 1 (Two bus line system)</p>		
I/O Pins	I/O	36	• Common use 29
	Input	3	• Common use 3
	High Voltage Output	7	• Nch Open-Drain (Breakdown Voltage 12 V) 7
A/D Inputs	5/7-Bit × 10ch (without S/H)		
PWM	14-Bit × 1ch (Repetition Cycle 16 μs, at 12 MHz), 8-Bit × 8ch (Repetition Cycle 32 μs, at 12 MHz), 7-Bit × 1ch (Repetition Cycle 16 μs, at 12 MHz) (PWM are 5 V, not connectable to 12 V systems)		
Special Ports	Hsync Detection, Remote Control Reception		
CRTC	Double OSD built-in (Menu OSD 12 × 18256 letters, Caption OSD 12 × 26176 letters)		
Notes	Remote Control Data Detection Circuit built-in, On-Chip synchronous separator for caption decoder		
Package	SDIP064-P-0750		

A/D Converter Characteristics

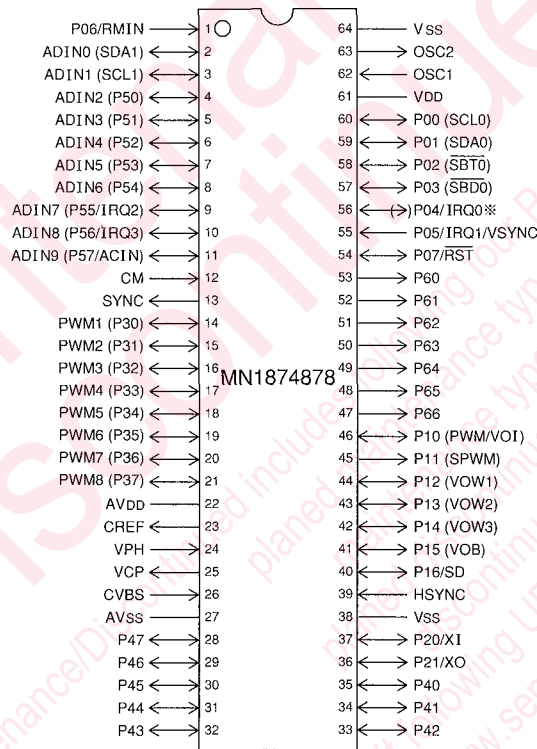
Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Time	TAD	tosc = 12 MHz	9/12			μs
Analog Input Voltage	VAD		VSS		VDD	V

(Ta = -20 °C to +70 °C, VDD = 5.0 V, VSS = 0 V)

Support Tool

In-Circuit Emulator	PX-ICE1870 / 80 + PX-PRB1876476, PX-PRB1879682
EPROM built-in Type	Type MN18P79682
	ROM (× 8-Bit) 96 K
	RAM (× 8-Bit) 1 248
	Minimum Instruction Execution Time 0.5 μs / 0.333 μs (at 4.5 V to 5.5 V, 12 MHz) Selectable
	Package SDIP064-P-0750

Pin Assignment



SDIP064-P-0750

※P04 • IRQ0 pin

TYPE A	Stand-By function is available	Input pin
TYPE B	Stand-By function is not available	I/O pin

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