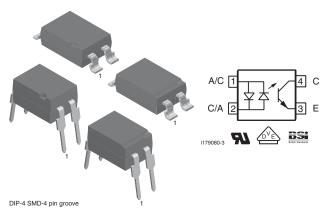


Vishay Semiconductors

Optocoupler, Phototransistor Output, AC Input, Low Input Current



DESCRIPTION

The SFH628A (DIP) and SFH6286 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared emitting diode, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400 V_{RMS} or DC.

FEATURES

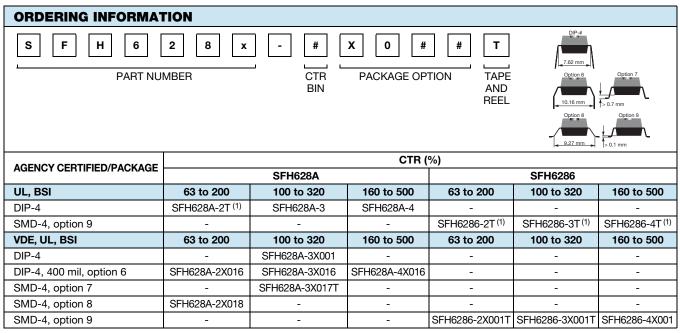
- High common mode interference immunity
- Isolation test voltage, 5300 V_{RMS}
- Low coupling capacitance
- Good CTR linearity depending on forward current
- Low CTR degradation
- High collector emitter voltage, $V_{CEO} = 55 \text{ V}$
- Compliant to RoHS Directive to 2002/95/EC and in accordance WEEE 2002/96/EC

APPLICATIONS

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines

AGENCY APPROVALS

- UL1577, file no. E52744 system code H
- DIN EN 60747-5-2 (VDE0884)/DIN EN 60747-5-5 (pending), available with option 1
- BSI IEC 60950; IEC 60065



Note

• Additional options may be possible, please contact sales office.

⁽¹⁾ Also available in tubes; do not add T to end.

Document Number: 83722 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Rev. 1.9, 24-Mar-11

www.vishay.com



(e3) RoHS

COMPLIANT

THE PRODUCT DESCRIBED HEREIN AND THIS DATASHEET ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input, Low Input Current



PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT	· · ·			
DC forward current		IF	± 50	mA
Surge forward current	t ≤ 10 µs	I _{FSM}	± 2.5	А
Power dissipation		P _{diss}	76	mW
OUTPUT	· · ·			
Collector emitter voltage		V _{CE}	55	V
Emitter collector voltage		V _{EC}	7	V
Collector current		IC	50	mA
Collector current	t _p ≤ 1 ms	Ι _C	100	mA
Power dissipation		P _{diss}	150	mW
COUPLER	· · ·			
Isolation test voltage		V _{ISO}	5300	V _{RMS}
Creepage distance			≥7	mm
Clearance distance			≥7	mm
Insulation thickness between			≥ 0.4	mm
Comparative tracking index per DIN IEC112/			175	
	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range		T _{stg}	- 55 to + 150	°C
Ambient temperature range		T _{amb}	- 55 to +100	°C
Soldering temperature ⁽¹⁾	max. 10 s, dip soldering distance	T _{sld}	260	°C

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = ± 5 mA		V _F		1.1	1.5	V
Capacitance	$V_R = 0 V, f = 1 MHz$		Co		45		pF
Thermal resistance			R _{thja}		1070		K/W
OUTPUT							
Collector emitter leakage current	V _{CE} = 10 V		I _{CEO}		10	200	nA
Collector emitter capacitance	$V_{CE} = 5 V$, f = 1 MHz		C _{CE}		7		pF
Thermal resistance			R _{thja}		500		K/W
COUPLER							
Collector emitter saturation voltage	$I_{\rm E} = \pm 1 \text{ mA}, I_{\rm C} = 0.5 \text{ mA}$	SFH628A-2	V _{CEsat}		0.25	0.4	V
	$I_F = \pm 1 \text{ IIIA}, I_C = 0.3 \text{ IIIA}$	SFH6286-2	H6286-2 V _{CEsat}	0.25	0.4	V	
		SFH628A-3	V _{CEsat}		0.25	0.4	V
	$I_{F} = \pm 1 \text{ mA}, I_{C} = 0.8 \text{ mA}$	SFH6286-3	V _{CEsat}		0.25	0.4	pF K/W nA pF K/W V V
	I _F = ± 1 mA, I _C = 1.25 mA	SFH628A-4	V _{CEsat}		0.25	0.4	V
		SFH6286-4	V _{CEsat}		0.25	0.4	V

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

This datasheet is subject to change without notice.

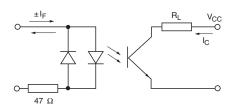
THE PRODUCT DESCRIBED HEREIN AND THIS DATASHEET ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input, Low Input Current

CURRENT TRANSFER R	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $						
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		SFH628A-2	CTR	63		200	%
	$V_{\rm F} = \pm 1$ IIIA, $V_{\rm CE} = 0.5$ V	SFH6286-2	CTR	63		200	%
		SFH628A-2	CTR	32	100		%
	$V_{\rm F} = \pm 0.5$ mA, $V_{\rm CE} = 1.5$ V	SFH6286-2	CTR	32	100		%
	$1 - 1 m \Lambda V = 0 E V$	SFH628A-3	CTR	100		320	% % % % %
I _C /I _F	$r_{\rm F} = \pm 1$ mA, $v_{\rm CE} = 0.5$ v	SFH6286-3	CTR	100		320	%
		SFH628A-3	CTR	50	160		%
	$V_{\rm F} = \pm 0.5$ mA, $V_{\rm CE} = 1.5$ V	SFH6286-3	CTR	50	160		%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH628A-4	CTR	160		500	%
		SFH6286-4	CTR	160		500	%
	$I_{F} = \pm 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH628A-4	CTR	80	250		%
		SFH6286-4	CTR	80	250		%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{on}			6	μs
Rise time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	tr			3.5	μs
Turn-off time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{off}			5.5	μs
Fall time	V_{CC} = 5 V, I _C = 2 mA, R _L = 100 Ω	t _f			5	μs



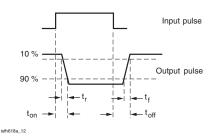
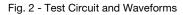


Fig. 1 - Test Circuit



SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
V _{IOTM}			10000			V
V _{IORM}			890			V
P _{SO}					400	mW
I _{SI}					275	mA
T _{SI}					175	°C
Creepage distance	Standard DIP-8		7			mm
Clearance distance	Standard DIP-8		7			mm
Creepage distance	400 mil DIP-8		8			mm
Clearance distance	400 mil DIP-8		8			mm
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4			mm

Note

isfh618a_11

• As per IEC 60747-5-5, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

This datasheet is subject to change without notice. THE PRODUCT DESCRIBED HEREIN AND THIS DATASHEET ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input, Low Input Current



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

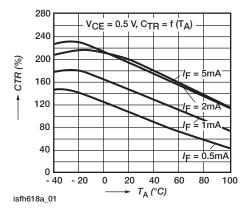


Fig. 3 - Current Transfer Ratio (typ.)

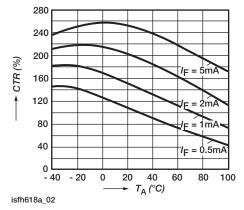


Fig. 4 - Current Transfer Ratio (typ.)

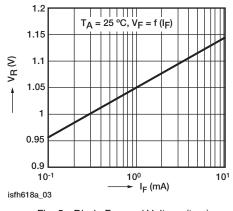


Fig. 5 - Diode Forward Voltage (typ.)

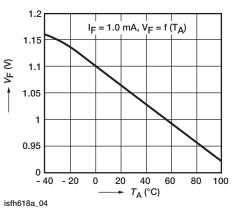
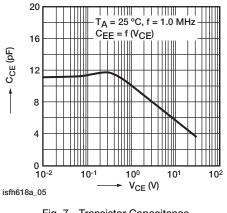


Fig. 6 - Diode Forward Voltage (typ.)





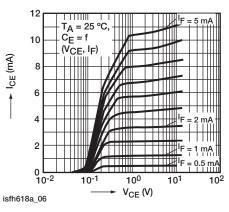


Fig. 8 - Output Characteristics

www.vishay.com 4 Document Number: 83722 Rev. 1.9, 24-Mar-11

This datasheet is subject to change without notice.



Optocoupler, Phototransistor Output, Vishay Semiconductors AC Input, Low Input Current

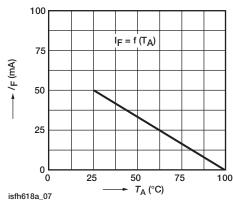
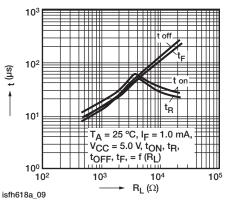
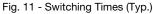


Fig. 9 - Permissible Forward Current Diode





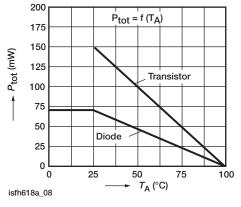
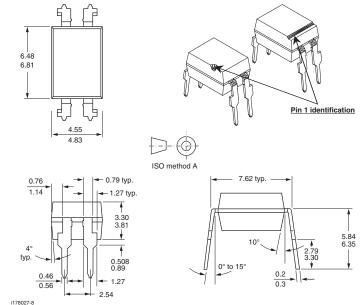


Fig. 10 - Permissible Power Dissipation





Document Number: 83722 Rev. 1.9, 24-Mar-11

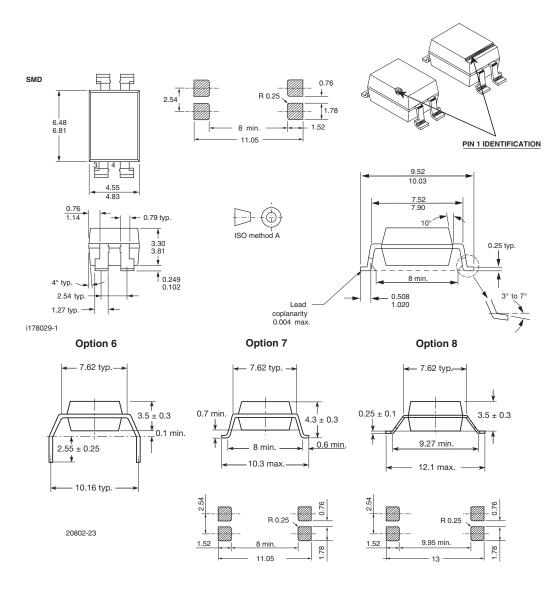
For technical questions, contact: optocoupleranswers@vishay.com

www.vishay.com 5

This datasheet is subject to change without notice. THE PRODUCT DESCRIBED HEREIN AND THIS DATASHEET ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

Vishay Semiconductors Optocoupler, Phototransistor Output, AC Input, Low Input Current





PACKAGE MARKING



Notes

- Only options 1, 7 and 8 are reflected in the package marking. •
- The VDE Logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking. ٠

www.vishay.com 6

Document Number: 83722 Rev. 1.9, 24-Mar-11



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.