

CMKDM8005

**SURFACE MOUNT SILICON
DUAL P-CHANNEL
ENHANCEMENT-MODE
MOSFET**



www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKDM8005 consists of dual P-Channel enhancement-mode silicon MOSFETs designed for high speed pulsed amplifier and driver applications. These MOSFETs offer very low $r_{DS(ON)}$ and low threshold voltage.



SOT-363 CASE

APPLICATIONS:

- Load switch/Level shifting
- Battery charging
- Boost switch
- Electro-luminescent backlighting

MAXIMUM RATINGS: ($T_A=25^\circ C$)

	SYMBOL		UNITS
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	8.0	V
Continuous Drain Current (Steady State)	I_D	650	mA
Continuous Source Current (Body Diode)	I_S	250	mA
Maximum Pulsed Drain Current	I_{DM}	1.0	A
Power Dissipation	P_D	350	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: ($T_A=25^\circ C$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{GSSF}, I_{GSSR}	$V_{GS}=4.5V, V_{DS}=0$			10	μA
I_{DSS}	$V_{DS}=16V, V_{GS}=0$			100	nA
BV_{DSS}	$V_{GS}=0, I_D=250\mu A$	20			V
$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5		1.0	V
V_{SD}	$V_{GS}=0, I_S=250mA$			1.1	V
$r_{DS(ON)}$	$V_{GS}=4.5V, I_D=350mA$		0.25	0.36	Ω
$r_{DS(ON)}$	$V_{GS}=2.5V, I_D=300mA$		0.37	0.5	Ω
$r_{DS(ON)}$	$V_{GS}=1.8V, I_D=150mA$			0.8	Ω
g_{FS}	$V_{DS}=10V, I_D=200mA$	0.2			S
C_{rss}	$V_{DS}=16V, V_{GS}=0, f=1.0MHz$		25		pF
C_{iss}	$V_{DS}=16V, V_{GS}=0, f=1.0MHz$		100		pF
C_{oss}	$V_{DS}=16V, V_{GS}=0, f=1.0MHz$		21		pF

R3 (3-June 2013)

CMKDM8005

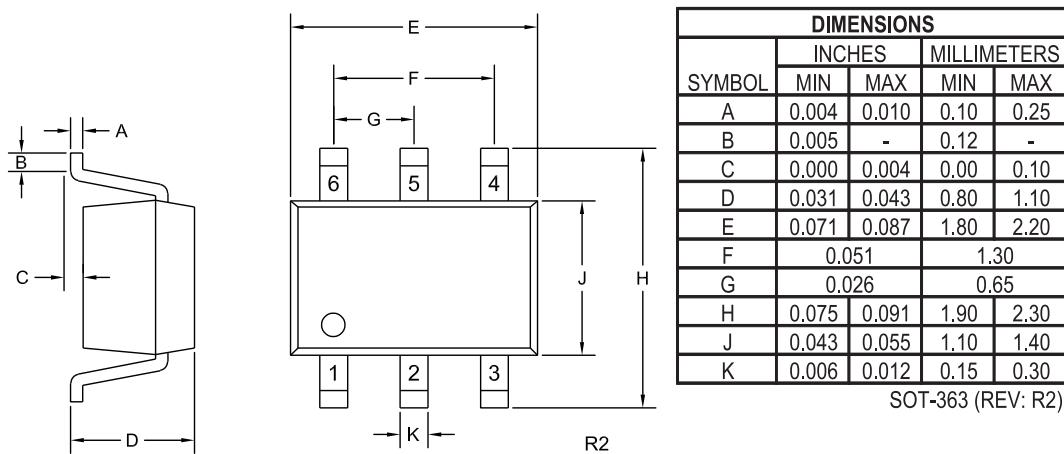
**SURFACE MOUNT SILICON
DUAL P-CHANNEL
ENHANCEMENT-MODE
MOSFET**

Central
Semiconductor Corp.™

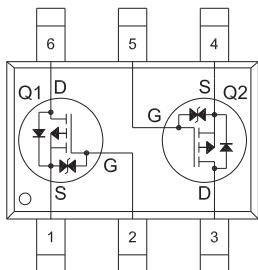
ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
$Q_g(\text{tot})$	$V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$	1.2		nC
Q_{gs}	$V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$	0.24		nC
Q_{gd}	$V_{DS}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$	0.36		nC
t_{on}	$V_{DD}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$, $R_G=10\Omega$	38		ns
t_{off}	$V_{DD}=10\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$, $R_G=10\Omega$	48		ns

SOT-363 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) Source Q1
- 2) Gate Q1
- 3) Drain Q1
- 4) Source Q2
- 5) Gate Q2
- 6) Drain Q2

MARKING CODE: C85M

R3 (3-June 2013)

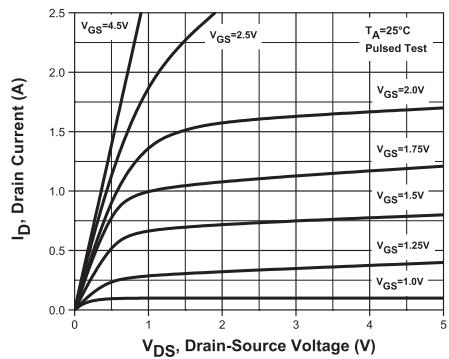
CMKDM8005

**SURFACE MOUNT SILICON
DUAL P-CHANNEL
ENHANCEMENT-MODE
MOSFET**

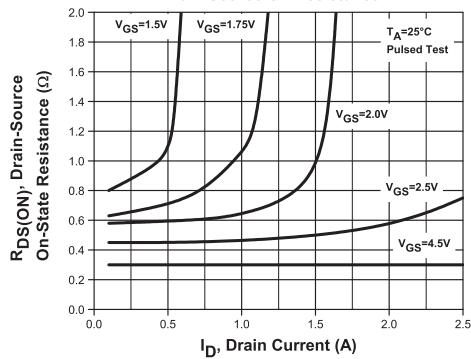


TYPICAL ELECTRICAL CHARACTERISTICS

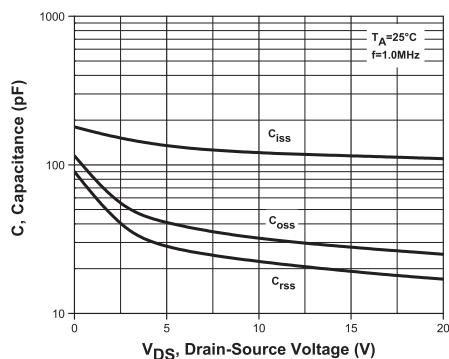
Output Characteristics



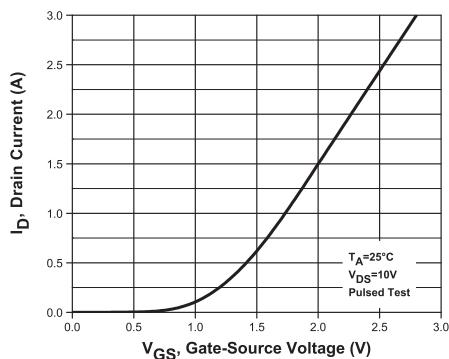
Drain Source On Resistance



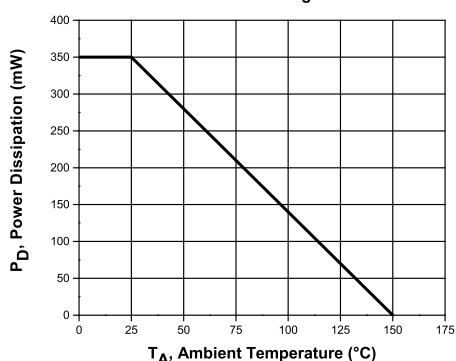
Capacitance



Transfer Characteristics



Power Derating



R3 (3-June 2013)