

UNISONIC TECHNOLOGIES CO., LTD

MGBR20U60C

Preliminary

DUAL MOS GATED BARRIER RECTIFIERS

DESCRIPTION

The UTC **MGBR20U60C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with high current capability, low forward voltage and high switching speed, etc.

FEATURES

* Ultra low forward voltage

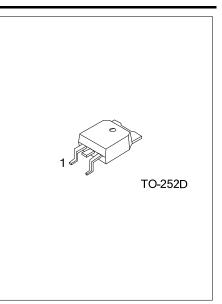
- * High switching speed
- * High current capability

SYMBOL

ORDERING INFORMATION

Ordering Number		Dealasta	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR20U60CL-TND-T	MGBR20U60CG-TND-T	TO-252D	А	К	А	Tube	
MGBR20U60CL-TND-R	MGBR20U60CG-TND-R	TO-252D	А	К	Α	Tape Reel	

MGBR20U60CL- <u>TNI</u>		(1) T: Tube, R: Tape Reel
	(2)Package Type	(2) TND: TO-252D
	(3)Lead Free	(3) L: Lead Free, G: Halogen Free



Preliminary

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V _{RM}	60	V
Working Peak Reverse Voltage		V _{RWM}	60	V
Peak Repetitive Reverse Voltage		V _{RRM}	60	V
Average Rectified Forward Current	Per Leg Total		10	А
		IO	20	А
Peak Forward Surge Current		I _{FSM}	250	А
Operating Junction Temperature		TJ	-40~+150	°C
Storage Temperature		T _{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	2.5	°C/W
Junction to Case	θις	2	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.60mA	60			V
Instantaneous Forward Voltage	V _{FM}	I _F =5A, T _J =25°C		0.40		V
		I _F =5A, T _J =125°C		0.37		V
		I _F =10A, T _J =25°C			0.49	V
		I _F =10A, T _J =125°C			0.44	V
Instantaneous Reverse Current (Note 1)	I _{RM}	V _{RM} =60V, T _J =25°C			500	μA
		V _{RM} =60V, T _J =125°C			20	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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