MGBR20V150C

**Preliminary** 

**DIODE** 

# **DUAL MOS GATED BARRIER RECTIFIER**

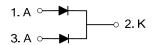
#### **DESCRIPTION**

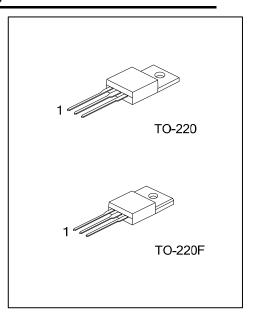
The UTC MGBR20V150C is a dual mos gated barrier rectifiers, it uses UT C's advanc ed tech nology to pro vide custom ers with lo w forward voltage drop and high switching speed, etc.

#### **FEATURES**

- \* Very low forward voltage drop
- \* High switching speed

### **SYMBOL**

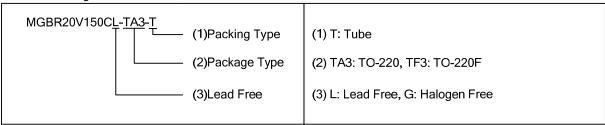




## **ORDERING INFORMATION**

Ordering Number		Doolsogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	lalogen Free Package		2	3	Packing	
MGBR20V150CL-TA3-T	MGBR20V150CG-TA3-T	TO-220	Α	K	Α	Tube	
MGBR20V150CL-TF3-T	MGBR20V150CG-TF3-T	TO-220F	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode, K: Cathode



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## ■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T<sub>A</sub>=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER SYMBOL		RATINGS	UNIT	
DC Blocking Voltage		V <sub>RM</sub> 150		V
Working Peak Reverse Voltage	V <sub>RWM</sub> 150		V	
Peak Repetitive Reverse Voltage	V <sub>RRM</sub> 150		V	
Average Destified Output Comment Day Device	Per Leg		10 A	
Average Rectified Output Current Per Device	Total 2	I <sub>O</sub>	0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I <sub>FSM</sub> 150		А
Operating Junction Temperature		T <sub>J</sub> -65∼	+150	°C
Storage Temperature		T <sub>STG</sub> -65	+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	_	$\theta_{JA}$	62.5	°C/W	
Lastinate Occasi	TO-220	0	2	°0/14/	
Junction to Case	TO-220F	$\theta_{JC}$	3.31	°C/W	

## ■ ELECTRICAL CHARACTERISTICS (PER LEG) (T<sub>A</sub> =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I <sub>R</sub> =0.50mA 150				V
Farment Valtage Days	VEM	I <sub>F</sub> =10A, T <sub>J</sub> =25°C			0.85	V
Forward Voltage Drop		I <sub>F</sub> =10A, T <sub>J</sub> =125°C			0.80	V
Lookage Compant (Note 4)	I IpM	V <sub>R</sub> =150V, T <sub>J</sub> =25°C			100	μΑ
Leakage Current (Note 1)		V <sub>R</sub> =150V, T <sub>.I</sub> =125°C			30	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

<sup>2.</sup> Thermal resistance junction to case mounted on heatsink.

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