

June 1998

Features

- 25A, 60V, $r_{DS(ON)} = 0.020\Omega$
- Total Dose
 - Meets Pre-RAD Specifications to 100K RAD (Si)
- Single Event
 - Safe Operating Area Curve for Single Event Effects
 - SEE Immunity for LET of 36MeV/mg/cm^2 with V_{DS} up to 80% of Rated Breakdown and V_{GS} of 10V Off-Bias
- Dose Rate
 - Typically Survives $3E9$ RAD (Si)/s at 80% BV_{DSS}
 - Typically Survives $2E12$ If Current Limited to I_{DM}
- Photo Current
 - 6.0nA Per-RAD(Si)/s Typically
- Neutron
 - Maintain Pre-RAD Specifications for $3E13$ Neutrons/cm²
 - Usable to $3E14$ Neutrons/cm²

Ordering Information

RAD LEVEL	SCREENING LEVEL	PART NUMBER/BRAND
10K	Commercial	FSF055D1
10K	TXV	FSF055D3
100K	Commercial	FSF055R1
100K	TXV	FSF055R3
100K	Space	FSF055R4

Formerly available as type TA17650.

Description

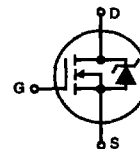
The Discrete Products Operation of Harris Semiconductor has developed a series of Radiation Hardened MOSFETs specifically designed for commercial and military space applications. Enhanced Power MOSFET immunity to Single Event Effects (SEE), Single Event Gate Rupture (SEGR) in particular, is combined with 100K RADS of total dose hardness to provide devices which are ideally suited to harsh space environments. The dose rate and neutron tolerance necessary for military applications have not been sacrificed.

The Harris portfolio of SEGR resistant radiation hardened MOSFETs includes N-Channel and P-Channel devices in a variety of voltage, current and on-resistance ratings. Numerous packaging options are also available.

This MOSFET is an enhancement-mode silicon-gate power field-effect transistor of the vertical DMOS (VDMOS) structure. It is specially designed and processed to be radiation tolerant. The MOSFET is well suited for applications exposed to radiation environments such as switching regulation, switching converters, motor drives, relay drivers and drivers for high-power bipolar switching transistors requiring high speed and low gate drive power. This type can be operated directly from integrated circuits.

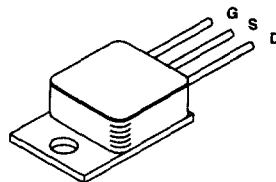
Reliability screening is available as either commercial, TXV equivalent of MIL-S-19500, or Space equivalent of MIL-S-19500. Contact Harris Semiconductor for any desired deviations from the data sheet.

Symbol



Package

TO-254AA



CAUTION: Beryllia Warning per MIL-S-19500
refer to package specifications.

 3
 RAD HARD SEGR
 MOSFETs

FSF055D, FSF055R

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

	FSF055D, FSF055R	UNITS
Drain to Source Voltage	60	V
Drain to Gate Voltage ($R_{GS} = 20\text{k}\Omega$)	60	V
Continuous Drain Current		
$T_C = 25^\circ\text{C}$	25	A
$T_C = 100^\circ\text{C}$	25	A
Pulsed Drain Current	200	A
Gate to Source Voltage	± 20	V
Maximum Power Dissipation		
$T_C = 25^\circ\text{C}$	125	W
$T_C = 100^\circ\text{C}$	50	W
Linear Derating Factor	1.14	W/ $^\circ\text{C}$
Single Pulsed Avalanche Current, $L = 100\mu\text{H}$, (See Test Figure)	200	A
Continuous Source Current (Body Diode)	25	A
Pulsed Source Current (Body Diode)	200	A
Operating and Storage Temperature	-55 to 150	$^\circ\text{C}$
Lead Temperature (During Soldering)	300	$^\circ\text{C}$
(Distance $> 0.063\text{in}$ (1.6mm) from Case, 10s Max)		

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Specifications $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS	
Drain to Source Breakdown Voltage	BV_{DSS}	$I_D = 1\text{mA}$, $V_{GS} = 0\text{V}$	60	-	-	V	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}$, $I_D = 1\text{mA}$	$T_C = -55^\circ\text{C}$	-	-	5.0	V
			$T_C = 25^\circ\text{C}$	1.5	-	4.0	V
			$T_C = 125^\circ\text{C}$	0.5	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48\text{V}$, $V_{GS} = 0\text{V}$	$T_C = 25^\circ\text{C}$	-	-	25	μA
			$T_C = 125^\circ\text{C}$	-	-	250	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}$	$T_C = 25^\circ\text{C}$	-	-	100	nA
			$T_C = 125^\circ\text{C}$	-	-	200	nA
Drain to Source On-State Voltage	$V_{DS(ON)}$	$V_{GS} = 12\text{V}$, $I_D = 25\text{A}$	-	-	0.525	V	
Drain to Source On Resistance	$r_{DS(ON)12}$	$I_D = 25\text{A}$, $V_{GS} = 12\text{V}$	$T_C = 25^\circ\text{C}$	-	0.014	0.020	Ω
			$T_C = 125^\circ\text{C}$	-	-	0.029	Ω
Turn-On Delay Time	$t_d(ON)$	$V_{DD} = 30\text{V}$, $I_D = 25\text{A}$, $R_L = 1.2\Omega$, $V_{GS} = 12\text{V}$, $R_{GS} = 2.35\Omega$	-	-	150	ns	
Rise Time	t_r		-	-	180	ns	
Turn-Off Delay Time	$t_d(OFF)$		-	-	140	ns	
Fall Time	t_f		-	-	60	ns	
Total Gate Charge	$Q_g(TOT)$	$V_{GS} = 0\text{V}$ to 20V	$V_{DD} = 30\text{V}$, $I_D = 25\text{A}$	-	-	290	nC
Gate Charge at 12V	$Q_g(12)$	$V_{GS} = 0\text{V}$ to 12V		-	140	190	nC
Threshold Gate Charge	$Q_g(TH)$	$V_{GS} = 0\text{V}$ to 2V		-	-	15	nC
Gate Charge Source	Q_{gs}			-	32	49	nC
Gate Charge Drain	Q_{gd}			-	61	89	nC
Plateau Voltage	$V_{(PLATEAU)}$	$I_D = 25\text{A}$, $V_{DS} = 15\text{V}$	-	6	-	V	
Input Capacitance	C_{ISS}	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	-	4300	-	pF	
Output Capacitance	C_{OSS}		-	2000	-	pF	
Reverse Transfer Capacitance	C_{RSS}		-	500	-	pF	
Thermal Resistance Junction to Case	$R_{\theta JC}$		-	-	0.88	$^\circ\text{C/W}$	
Thermal Resistance Junction to Ambient	$R_{\theta JA}$		-	-	40	$^\circ\text{C/W}$	

FSF055D, FSF055R

Source to Drain Diode Specifications

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Forward Voltage	V_{SD}	$I_{SD} = 25A$	0.6	-	1.8	V
Reverse Recovery Time	t_{rr}	$I_{SD} = 25A, di_{SD}/dt = 100A/\mu s$	-	-	290	ns

Electrical Specifications up to 100K RAD $T_C = 25^\circ C$, Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
Drain to Source Breakdown Volts (Note 3)	BV_{DSS}	$V_{GS} = 0, I_D = 1mA$	60	-	V
Gate to Source Threshold Volts (Note 3)	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 1mA$	1.5	4.0	V
Gate to Body Leakage (Notes 2, 3)	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	100	nA
Zero Gate Leakage (Note 3)	I_{DSS}	$V_{GS} = 0, V_{DS} = 48V$	-	25	μA
Drain to Source On-State Volts (Notes 1, 3)	$V_{DS(ON)}$	$V_{GS} = 12V, I_D = 25A$	-	0.525	V
Drain to Source On Resistance (Notes 1, 3)	$r_{DS(ON)12}$	$V_{GS} = 12V, I_D = 25A$	-	0.020	Ω

NOTES:

1. Pulse test, 300 μs Max.
2. Absolute value.
3. Insitu Gamma bias must be sampled for both $V_{GS} = 12V, V_{DS} = 0V$ and $V_{GS} = 0V, V_{DS} = 80\% BV_{DSS}$.

Single Event Effects (SEB, SEGR) (Note 4)

TEST	SYMBOL	ENVIRONMENT (NOTE 5)			APPLIED V_{GS} BIAS (V)	(NOTE 6) MAXIMUM V_{DS} BIAS (V)
		ION SPECIES	TYPICAL LET (MeV/mg/cm)	TYPICAL RANGE (μ)		
Single Event Effects Safe Operating Area	SEESOA	Ni	26	43	-20	60
		Br	37	36	-10	60
		Br	37	36	-15	48
		Br	37	36	-20	36
		I	60	31	0	60
		I	60	31	-5	48
		I	60	31	-10	36
		I	60	31	-15	24
		I	60	31	-20	12

NOTES:

4. Testing conducted at Brookhaven National Labs; sponsored by Naval Surface Warfare Center (NSWC), Crane, IN.
5. Fluence = 1E5 ions/cm² (typical), T = 25 $^\circ C$.
6. Does not exhibit Single Event Burnout (SEB) or Single Event Gate Rupture (SEGR).

Typical Performance Curves Unless Otherwise Specified

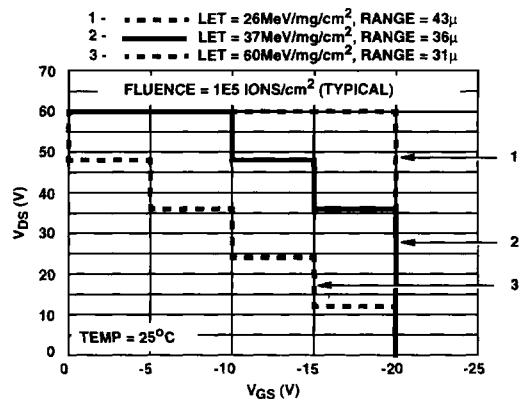


FIGURE 1. SINGLE EVENT EFFECTS SAFE OPERATING AREA

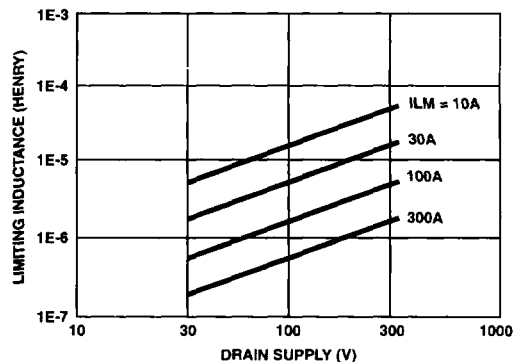


FIGURE 2. DRAIN INDUCTANCE REQUIRED TO LIMIT GAMMA DOT CURRENT TO I_{AS}

3

RAD HARD SEGR MOSFETS

Typical Performance Curves Unless Otherwise Specified (Continued)

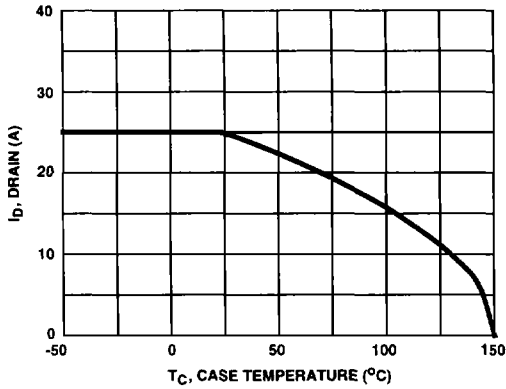


FIGURE 3. MAXIMUM CONTINUOUS DRAIN CURRENT vs TEMPERATURE

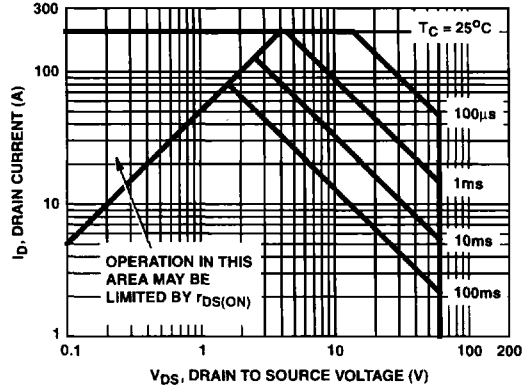


FIGURE 4. FORWARD BIAS SAFE OPERATING AREA

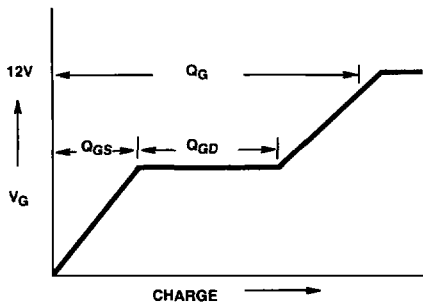


FIGURE 5. BASIC GATE CHARGE WAVEFORM

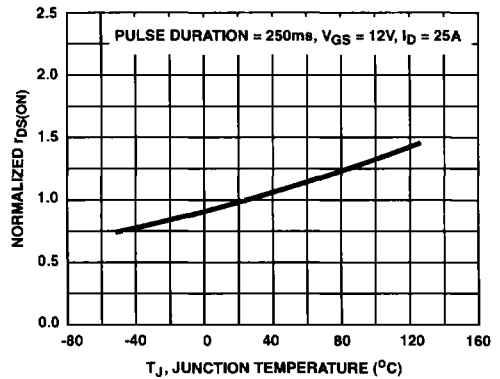


FIGURE 6. NORMALIZED $r_{DS(ON)}$ vs JUNCTION TEMPERATURE

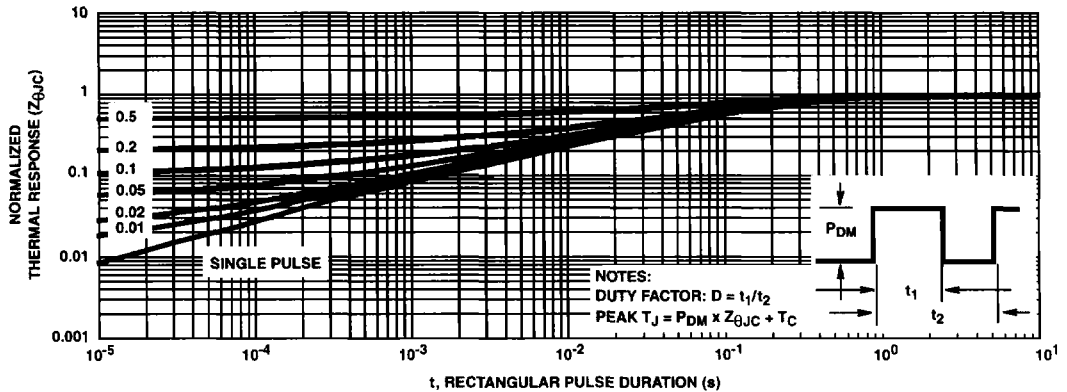


FIGURE 7. NORMALIZED MAXIMUM TRANSIENT THERMAL RESPONSE

Typical Performance Curves Unless Otherwise Specified (Continued)

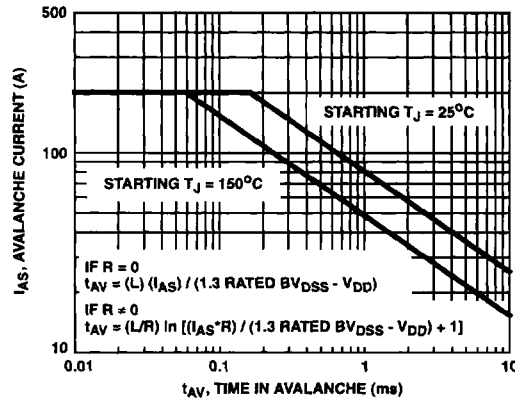


FIGURE 8. UNCLAMPED INDUCTIVE SWITCHING

Test Circuits and Waveforms

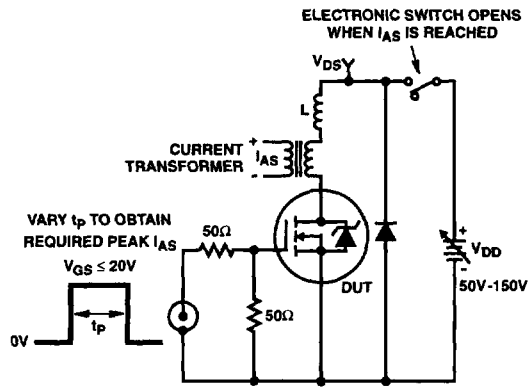


FIGURE 9. UNCLAMPED ENERGY TEST CIRCUIT

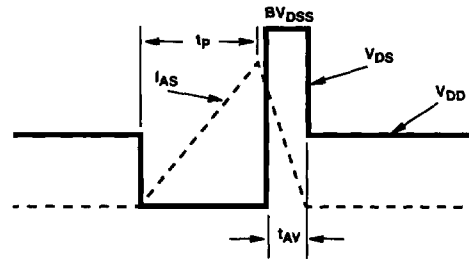


FIGURE 10. UNCLAMPED ENERGY WAVEFORMS

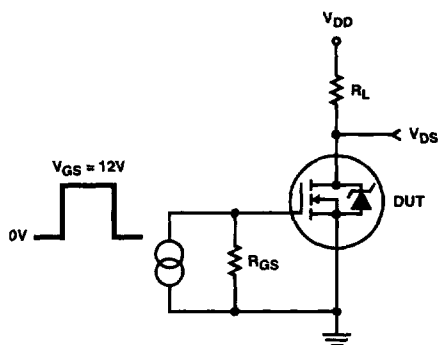


FIGURE 11. RESISTIVE SWITCHING TEST CIRCUIT

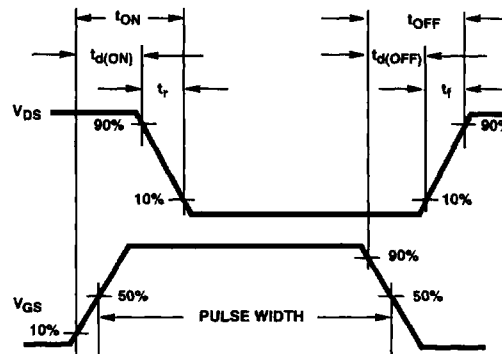


FIGURE 12. RESISTIVE SWITCHING WAVEFORMS

FSF055D, FSF055R

Screening Information

Screening is performed in accordance with the latest revision in effect of MIL-S-19500, (Screening Information Table).

Delta Tests and Limits (JANTXV Equivalent, JANS Equivalent) $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS	MAX	UNITS
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}$	± 20 (Note 7)	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\%$ Rated Value	± 25 (Note 7)	μA
Drain to Source On Resistance	$r_{DS(ON)}$	$T_C = 25^\circ\text{C}$ at Rated I_D	$\pm 20\%$ (Note 8)	Ω
Gate Threshold Voltage	$V_{GS(TH)}$	$I_D = 1.0\text{mA}$	$\pm 20\%$ (Note 8)	V

NOTES:

7. Or 100% of Initial Reading (whichever is greater).
8. Of Initial Reading.

Screening Information

TEST	JANTXV EQUIVALENT	JANS EQUIVALENT
Gate Stress	$V_{GS} = 30\text{V}$, $t = 250\mu\text{s}$	$V_{GS} = 30\text{V}$, $t = 250\mu\text{s}$
Pind	Optional	Required
Pre Burn-In Tests (Note 9)	MIL-S-19500 Group A, Subgroup 2 (All Static Tests at 25°C)	MIL-S-19500 Group A, Subgroup 2 (All Static Tests at 25°C)
Steady State Gate Bias (Gate Stress)	MIL-STD-750, Method 1042, Condition B $V_{GS} = 80\%$ of Rated Value, $T_A = 150^\circ\text{C}$, Time = 48 hours	MIL-STD-750, Method 1042, Condition B $V_{GS} = 80\%$ of Rated Value, $T_A = 150^\circ\text{C}$, Time = 48 hours
Interim Electrical Tests (Note 9)	All Delta Parameters Listed in the Delta Tests and Limits Table	All Delta Parameters Listed in the Delta Tests and Limits Table
Steady State Reverse Bias (Drain Stress)	MIL-STD-750, Method 1042, Condition A $V_{DS} = 80\%$ of Rated Value, $T_A = 150^\circ\text{C}$, Time = 160 hours	MIL-STD-750, Method 1042, Condition A $V_{DS} = 80\%$ of Rated Value, $T_A = 150^\circ\text{C}$, Time = 240 hours
PDA	10%	5%
Final Electrical Tests (Note 9)	MIL-S-19500, Group A, Subgroup 2	MIL-S-19500, Group A, Subgroups 2 and 3

NOTE:

9. Test limits are identical pre and post burn-in.

Additional Screening Tests

PARAMETER	SYMBOL	TEST CONDITIONS	MAX	UNITS
Safe Operating Area	SOA	$V_{DS} = 48\text{V}$, $t = 10\text{ms}$	6.8	A
Unclamped Inductive Switching	I_{AS}	$V_{GS(PEAK)} = 15\text{V}$, $L = 0.1\text{mH}$	200	A
Thermal Response	ΔV_{SD}	$t_H = 100\text{ms}$; $V_H = 25\text{V}$; $I_H = 4\text{A}$	130	mV
Thermal Impedance	ΔV_{SD}	$t_H = 500\text{ms}$; $V_H = 25\text{V}$; $I_H = 4\text{A}$	240	mV