

FDD9409_F085 N-Channel PowerTrench[®] MOSFET

40 V, 90 A, 3.2 mΩ

Features

- Typ $R_{DS(on)}$ = 2.3m Ω at V_{GS} = 10V, I_D = 80A
- Typ Q_{g(tot)} = 42nC at V_{GS} = 10V, I_D = 80A
- UIS Capability
- RoHS Compliant
- Qualified to AEC Q101

Applications

- Automotive Engine Control
- Powertrain Management
- Solenoid and Motor Drivers
- Electronic Steering
- Integrated Starter/Alternator
- Distributed Power Architectures and VRM
- Primary Switch for 12V Systems

MOSFET Maximum Ratings T_J = 25°C unless otherwise noted.

ROHS

Symbol	Parameter	Ratings	Units V		
V _{DSS}	Drain-to-Source Voltage	40			
V _{GS}	Gate-to-Source Voltage		±20	V	
ID	Drain Current - Continuous (V _{GS} =10) (Note 1)	T _C =25°C	90	Α	
	Pulsed Drain Current	T _C = 25°C	See Figure 4		
E _{AS}	Single-Pulse Avalanche Energy	(Note 2)	101	mJ	
P _D	Power Dissipation		150	W	
	Derate Above 25°C		1	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature		-55 to + 175	°C	
R _{0JC}	Thermal Resistance, Junction to Case		1	°C/W	
R _{0JA}	Maximum Thermal Resistance, Junction to Ambient	(Note 3)	52	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD9409	FDD9409_F085	D-PAK(TO-252)	13"	12mm	2500 units

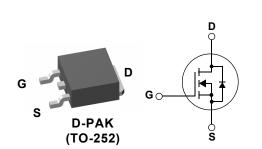
Notes:

1: Current is limited by bondwire configuration.

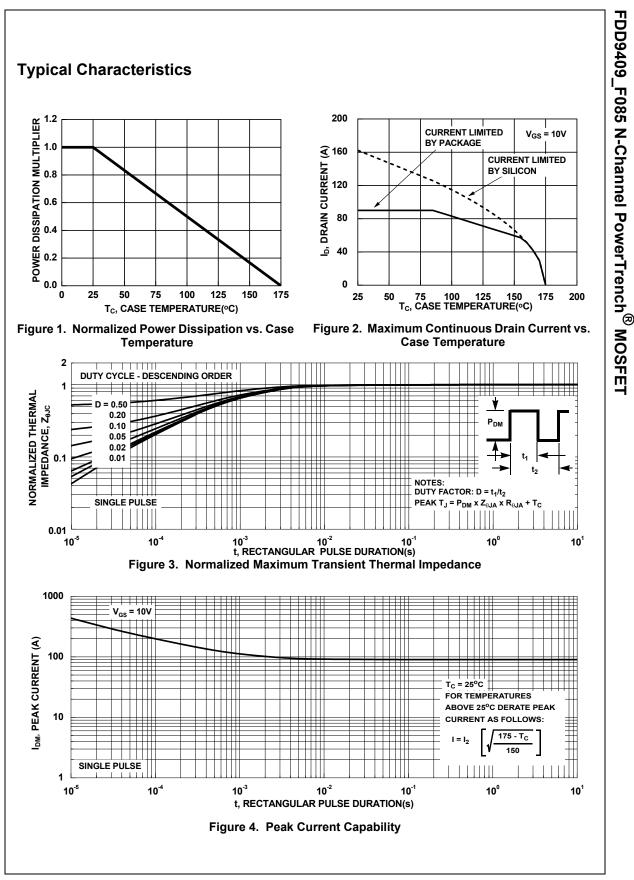
Current is infinite by boldwire configuration.
Starting T_J = 25°C, L = 0.1mH, I_{AS} = 44A, V_{DD} = 40V during inductor charging and V_{DD} = 0V during time in avalanche.
R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{0JC} is guaranteed by design while R_{0JA} is determined by the user's board design. The maximum rating presented here is based on mounting on a 1 in² pad of 2oz copper.

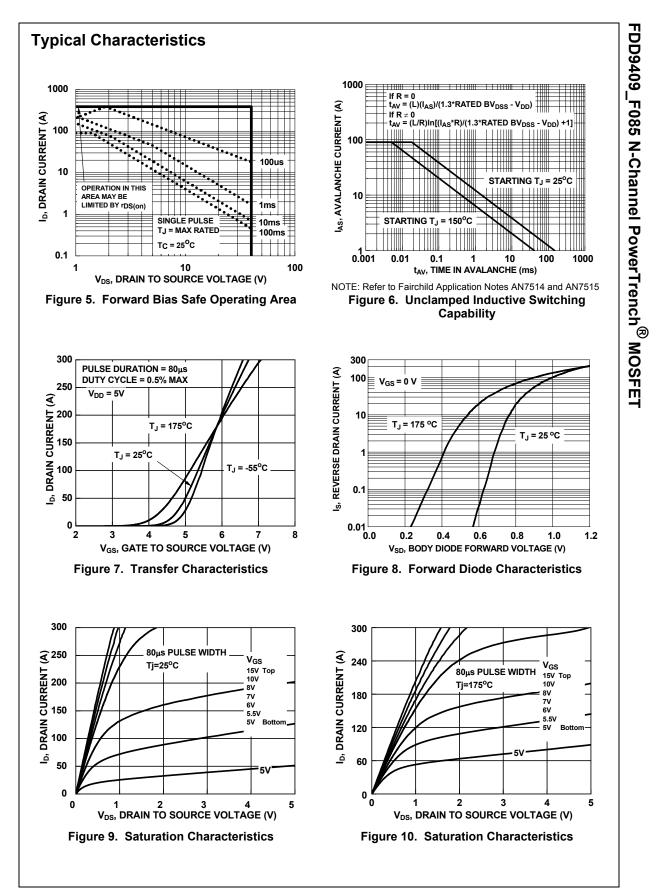
May 2014

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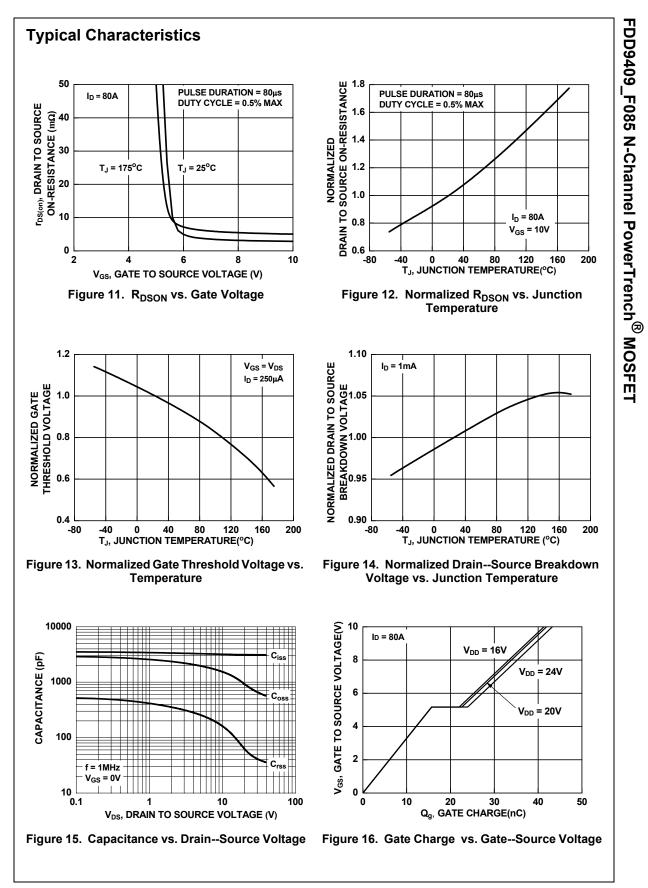


For current package drawing, please refer to the Fairchild website at www.fairchildsemi.com/packaging





FDD9409_F085 Rev. C4



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