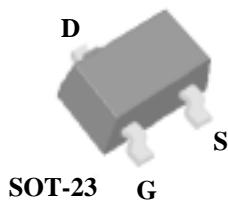


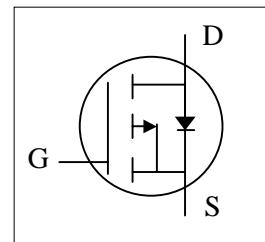
▼ Simple Drive Requirement
▼ Small Package Outline
▼ Surface Mount Device


BV_{DSS}	-30V
$R_{DS(ON)}$	80m Ω
I_D	- 3.2A

Description

The Advanced Power MOSFETs from TY provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-23 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	- 30	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_A=25^\circ\text{C}$	Continuous Drain Current ³	-3.2	A
$I_D @ T_A=70^\circ\text{C}$	Continuous Drain Current ³	-2.6	A
I_{DM}	Pulsed Drain Current ^{1,2}	-10	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation	1.38	W
	Linear Derating Factor	0.01	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
$R_{thj-amb}$	Thermal Resistance Junction-ambient ³	Max. 90	$^\circ\text{C}/\text{W}$

Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	-	-0.1	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-3.2\text{A}$	-	-	60	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-3.0\text{A}$	-	-	80	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$, $I_{\text{D}}=-2.0\text{A}$	-	-	150	$\text{m}\Omega$
		$V_{\text{GS}}=-1.8\text{V}$, $I_{\text{D}}=-1.0\text{A}$	-	-	250	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-0.5	-	-1.2	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-3.0\text{A}$	-	9	-	S
I_{DSS}	Drain-Source Leakage Current ($T_j=25^\circ\text{C}$)	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	-1	uA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	-25	uA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}= \pm 12\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_{\text{D}}=-3.2\text{A}$	-	10	18	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-24\text{V}$	-	1.8	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=-4.5\text{V}$	-	3.6	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time ²	$V_{\text{DS}}=-15\text{V}$	-	7	-	ns
t_r	Rise Time	$I_{\text{D}}=-3.2\text{A}$	-	15	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_{\text{G}}=3.3\Omega$, $V_{\text{GS}}=-10\text{V}$	-	21	-	ns
t_f	Fall Time	$R_{\text{D}}=4.6\Omega$	-	15	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	735	1325	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=-25\text{V}$	-	100	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	80	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_{\text{S}}=-1.2\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{S}}=-3.2\text{A}$, $V_{\text{GS}}=0\text{V}$,	-	24	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$	-	19	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 3.Surface mounted on 1 in² copper pad of FR4 board ; $270^\circ\text{C}/\text{W}$ when mounted on min. copper pad.