

# PHOTOCOUPLER **PS8101**

-NEPOC Series-

# 1 Mbps HIGH CMR ANALOG OUTPUT TYPE 5-PIN SOP (SO-5) PHOTOCOUPLER

# DESCRIPTION

The PS8101 is an optically coupled isolator containing a GaAlAs LED on the light emitting diode (input side) and a PIN photodiode and a high-speed amplifier transistor on the output side on one chip.

This is a plastic SOP (Small Out-line Package) type for high density applications.

#### **FEATURES**

- <R> • High common mode transient immunity (CMH, CML =  $\pm 15 \text{ kV}/\mu \text{s}$  MIN.)
  - Small package (SO-5)
  - High supply voltage (Vcc = 35 V)
  - High isolation voltage (BV = 3 750 Vr.m.s.)
  - High-speed response (tPHL = 0.8  $\mu$ s MAX., tPLH = 1.2  $\mu$ s MAX.)
  - Ordering number of taping product: PS8101-F3, F4: 2 500 pcs/reel •
  - Pb-Free product
  - Safety standards

**PIN CONNECTION** (Top View) 4 3 Π П 1. Anode 2. Cathode 3. GND 4. Vo 5. Vcc

• UL approved: File No. E72422

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- CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950) DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40008902 (Option)

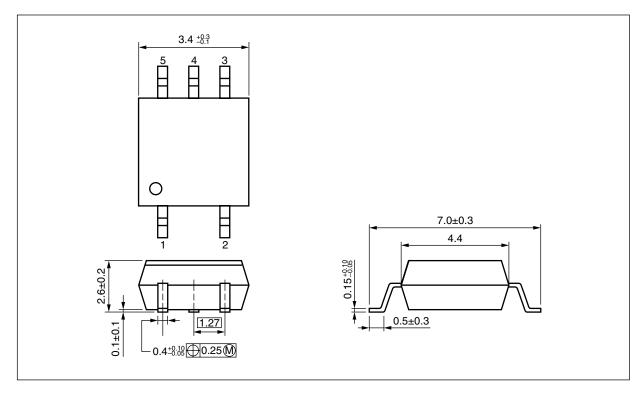
# **APPLICATIONS**

- Computer and peripheral manufactures
- General purpose inverter
- Substitutions for relays and pulse transformers
- Power supply

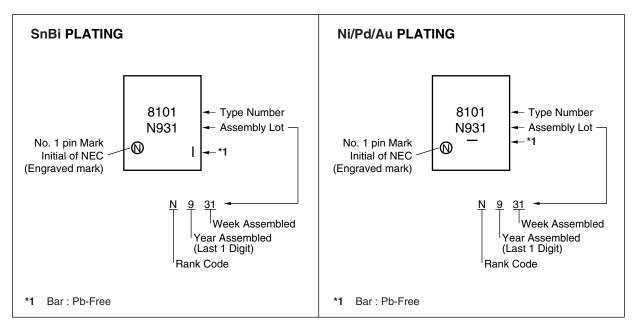
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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

# <R> PACKAGE DIMENSIONS (UNIT: mm)



# <R> MARKING EXAMPLE



# <R> PHOTOCOUPLER CONSTRUCTION

Parameter	PS8101
Air Distance (MIN.)	4.2 mm
Outer Creepage Distance (MIN.)	4.2 mm
Isolation Distance (MIN.)	0.2 mm

Data Sheet PN10260EJ06V0DS

#### <R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number <sup>*1</sup>
PS8101	PS8101-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS8101
PS8101-F3	PS8101-F3-A	(SnBi)	Embossed Tape 2 500 pcs/reel	(UL, CSA approved)	
PS8101-F4	PS8101-F4-A				
PS8101-V	PS8101-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS8101-V-F3	PS8101-V-F3-A		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS8101-V-F4	PS8101-V-F4-A			Approved (Option)	
PS8101	PS8101-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	
PS8101-F3	PS8101-F3-AX	(Ni/Pd/Au)	Embossed Tape 2 500 pcs/reel	(UL, CSA approved)	
PS8101-F4	PS8101-F4-AX				
PS8101-V	PS8101-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS8101-V-F3	PS8101-V-F3-AX		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS8101-V-F4	PS8101-V-F4-AX			Approved (Option)	

\*1 For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current	lf	25	mA
	Reverse Voltage	VR	5.0	v
	Power Dissipation <sup>™</sup>	PD	45	mW
Detector	Supply Voltage	Vcc	35	V
	Output Voltage	Vo	35	V
	Output Current	lo	8.0	mA
	Power Dissipation <sup>2</sup>	Pc	100	mW
Isolation Voltage <sup>*3</sup>		BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	–55 to +100	°C
Storage Temperature		Tstg	-55 to +125	°C

\*1 Reduced to 0.45 mW/°C at TA =  $25^{\circ}$ C or more.

\*2 Reduced to 1.00 mW/°C at TA = 25°C or more.

\*3 AC voltage for 1 minute at  $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	I⊧ = 16 mA		1.7	2.2	V
	Reverse Current	IR	V <sub>R</sub> = 3 V			10	μA
	Forward Voltage Temperature Coefficient	<i>∆</i> V <i>⊧/∆</i> Ta	I⊧ = 16 mA		-2.1		mV/°C
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Detector	High Level Output Current	Іон (1)	$I_F = 0 \text{ mA}, \text{ Vcc} = \text{Vo} = 5.5 \text{ V}$		3	500	nA
	High Level Output Current	Іон (2)	$I_F = 0 \text{ mA}, \text{ Vcc} = V_0 = 30 \text{ V}$			100	μA
	Low Level Output Voltage	Vol	$I_F = 16 \text{ mA}, \text{ V}_{CC} = 4.5 \text{ V}, \text{ I}_O = 1.2 \text{ mA}$		0.1	0.4	V
	Low Level Supply Current	Iccl	$I_F = 16 \text{ mA}, V_O = \text{open}, V_{CC} = 30 \text{ V}$		50		μA
	High Level Supply Current	Іссн	$I_F = 0 \text{ mA}, V_O = \text{open}, V_{CC} = 30 \text{ V}$		0.01	2	
Coupled	Current Transfer Ratio <sup>*1</sup>	CTR	$I_F = 16 \text{ mA}, \text{ Vcc} = 4.5 \text{ V}, \text{ Vo} = 0.4 \text{ V}$	15	20	35	%
	Isolation Resistance	R⊦o	$V_{I-O} = 1 \text{ kV}_{DC}, \text{ RH} = 40 \text{ to } 60\%$	10 <sup>11</sup>			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz		0.4		pF
	Propagation Delay Time $(H \rightarrow L)^2$	tph∟	$I_{\text{F}} = 16 \text{ mA}, \text{ V}_{\text{CC}} = 5 \text{ V}, \text{ R}_{\text{L}} = 2.2 \text{ k}\Omega, \\ C_{\text{L}} = 15 \text{ pF}$		0.5	0.8	μs
	Propagation Delay Time $(L \rightarrow H)^{2}$	tрін			0.6	1.2	
	Common Mode Transient Immunity at High Level Output <sup>3</sup>	Смн	$I_{\text{F}} = 0 \text{ mA}, \text{ V}_{\text{CC}} = 5 \text{ V}, \text{ R}_{\text{L}} = 4.1 \text{ k}\Omega,$ $V_{\text{CM}} = 1.5 \text{ kV}$	15			kV/ <i>µ</i> s
	Common Mode Transient Immunity at Low Level Output <sup>'3</sup>	Смг	$I_{\text{F}} = 16 \text{ mA}, \text{ V}_{\text{CC}} = 5 \text{ V}, \text{ R}_{\text{L}} = 4.1 \text{ k}\Omega,$ $V_{\text{CM}} = 1.5 \text{ kV}$	-15			

# ELECTRICAL CHARACTERISTICS (TA = 25°C)

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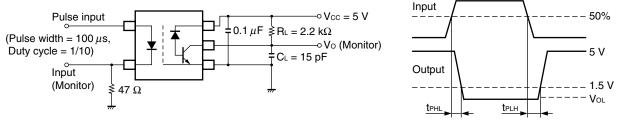
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\*1 CTR rank

K : 20 to 35 (%)

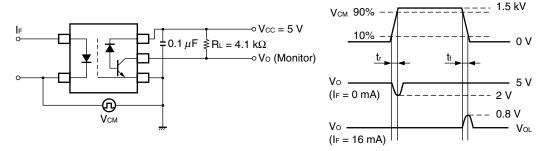
N: 15 to 35 (%)

\*2 Test circuit for propagation delay time



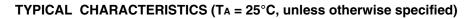
CL is approximately 15 pF which includes probe and stray wiring capacitance.

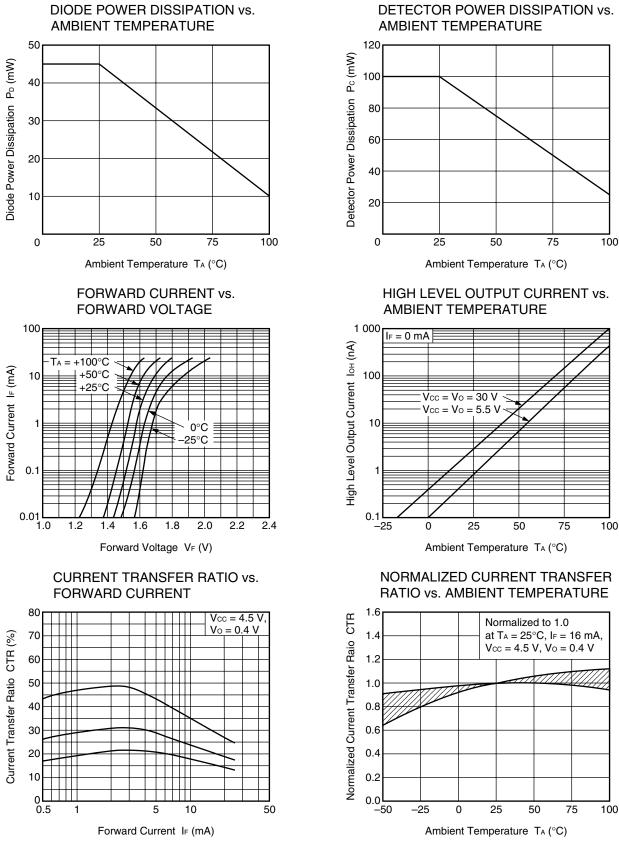
\*3 Test circuit for common mode transient immunity



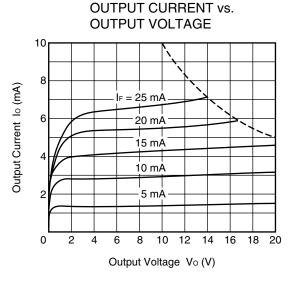
# **USAGE CAUTIONS**

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1  $\mu$ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.

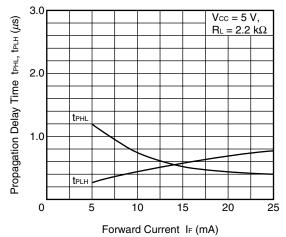




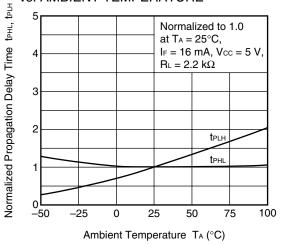
**Remark** The graphs indicate nominal characteristics.





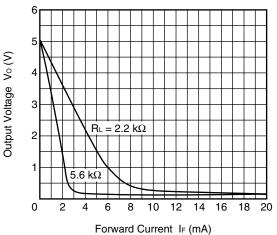




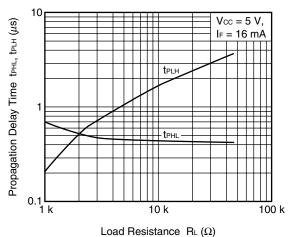


**Remark** The graphs indicate nominal characteristics.

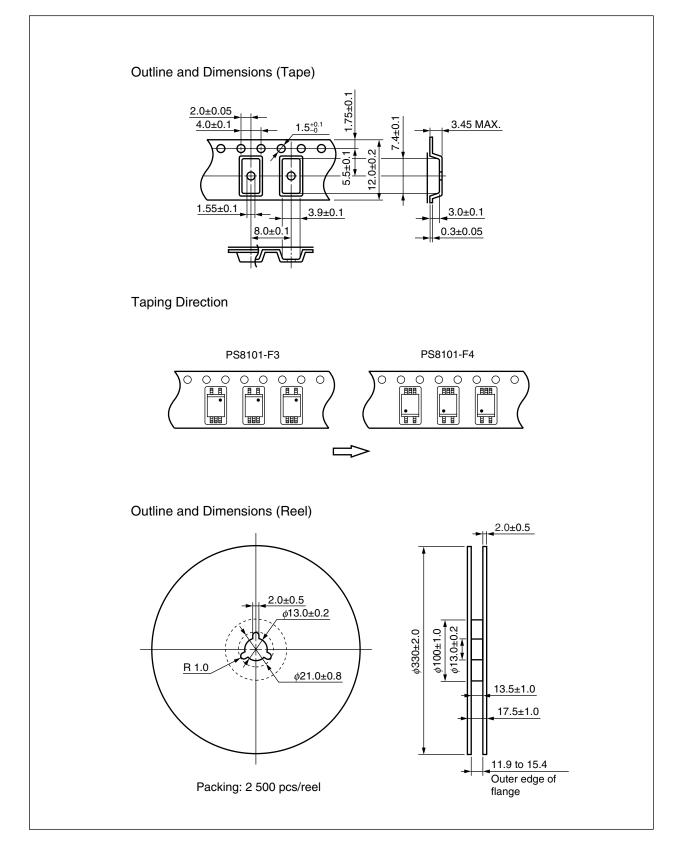
OUTPUT VOLTAGE vs. FORWARD CURRENT



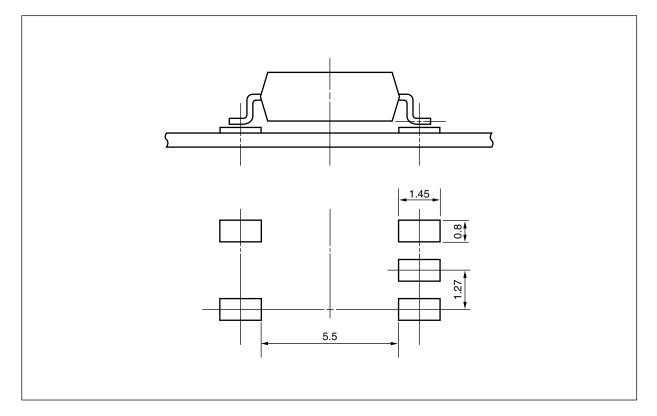
PROPAGATION DELAY TIME vs. LOAD RESISTANCE



# TAPING SPECIFICATIONS (UNIT: mm)



# RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



# NOTES ON HANDLING

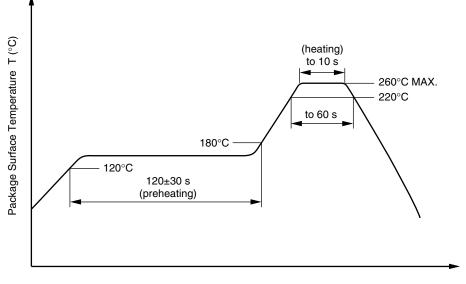
# 1. Recommended soldering conditions

# (1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



Time (s)

### (2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times
   One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature)	350°C or below
<ul> <li>Time (each pins)</li> </ul>	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over  $100^\circ\text{C}$

#### (4) Cautions

# Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

#### <R> 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between Vcc and GND at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

# <R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 pC$	Uiorm Upr	707 1 061	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{\text{pr}}$ = 1.875 $\times$ U_{IORM}, $P_{\text{d}}$ < 5 pC	Upr	1 326	V <sub>peak</sub>
Highest permissible overvoltage	Utr	6 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	–55 to +100	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A \text{ MAX. at least } 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	150 200 300	°C mA mW
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> = Tsi	Ris MIN.	10 <sup>°</sup>	Ω

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M8E0904E

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	<ol> <li>Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.</li> </ol>
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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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