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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR

2SK3430

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3430 is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

• Super low on-state resistance:

 $R_{DS(on)1} = 7.3 \, m\Omega \, MAX. \, (V_{GS} = 10 \, V, \, I_{D} = 40 \, A)$ $R_{DS(on)2} = 15 \, m\Omega \, MAX. \, (V_{GS} = 4 \, V, \, I_{D} = 40 \, A)$

- Low Ciss: Ciss = 2800 pF TYP.
- Built-in gate protection diode

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3430	TO-220AB
2SK3430-S	TO-262
2SK3430-ZJ	TO-263
2SK3430-Z	TO220SMD ^{Note}

Note TO-220SMD package is produced only in Japan.

(TO-220AB)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	40	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±80	Α
Drain Current (pulse) Note1	D(pulse)	±200	Α
Total Power Dissipation (Tc = 25°C)	PT	84	W
Total Power Dissipation (T _A = 25°C)	PT	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note2	las	37	Α
Single Avalanche Energy Note2	Eas	137	mJ



(TO-262)



(TO-263, TO-220SMD)

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V



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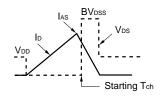


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

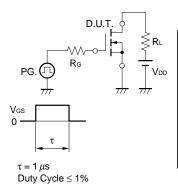
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	Vps = 40 V, Vgs = 0 V			10	μΑ
Gate Leakage Current	Igss	V _G S = ±20 V, V _D S = 0 V			±10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	٧
Forward Transfer Admittance	y fs	V _{DS} = 10 V, I _D = 40 A	20	40		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 40 A		5.9	7.3	mΩ
	RDS(on)2	V _G S = 4 V, I _D = 40 A		10.5	15	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V,		2800		pF
Output Capacitance	Coss	Vgs = 0 V,		730		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		320		pF
Turn-on Delay Time	t d(on)	V _{DD} = 20 V, I _D = 40 A		110		ns
Rise Time	t r	Vgs = 10 V		1800		ns
Turn-off Delay Time	td(off)	$R_G = 10 \Omega$		170		ns
Fall Time	t f			350		ns
Total Gate Charge	Q _G	V _{DD} = 32 V		50		nC
Gate to Source Charge	Qgs	V _G S = 10 V		10		nC
Gate to Drain Charge	Q _{GD}	ID = 80 A		14		nC
Body Diode Forward Voltage	V _{F(S-D)}	IF = 80 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 80 A, VGS = 0 V		50		ns
Reverse Recovery Charge	Qrr	$di/dt = 100 A/\mu s$		77		nC

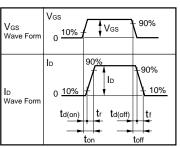
TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{Rg} = 25 \ \Omega \\ \text{PG.} \\ \text{>} 50 \ \Omega \\ \text{V}_{\text{GS}} = 20 \rightarrow 0 \ \text{V} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME



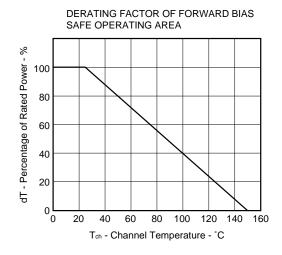


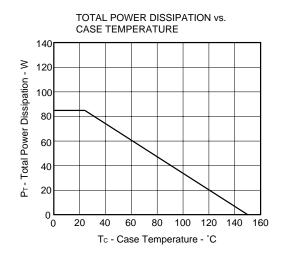
TEST CIRCUIT 3 GATE CHARGE

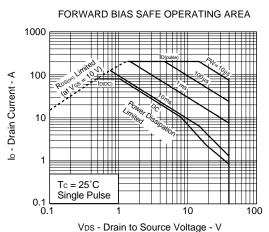
$$\begin{array}{c|c} D.U.T. & \\ \hline \\ I_G = 2 \text{ mA} \\ \hline \\ PG. & \\ \hline \\ \end{array} \begin{array}{c} RL \\ \hline \\ \\ \end{array}$$



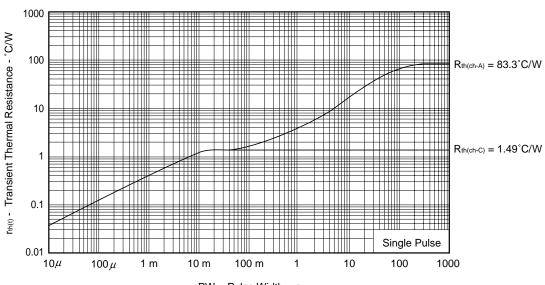
TYPICAL CHARACTERISTICS (TA = 25 °C)







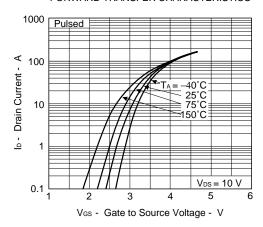
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



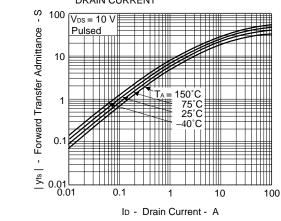
PW - Pulse Width - s

Data Sheet D14599EJ3V0DS

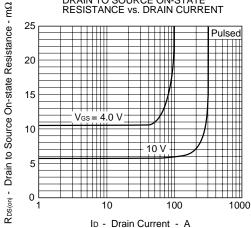
FORWARD TRANSFER CHARACTERISTICS



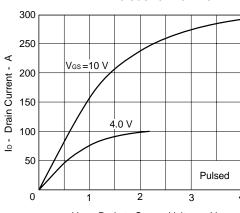
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

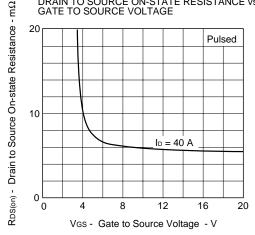


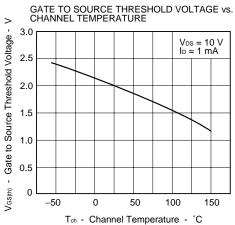
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

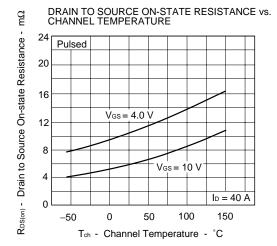


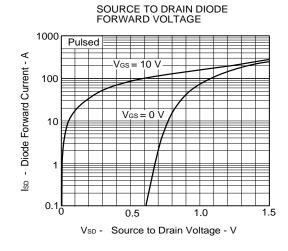
VDS - Drain to Source Voltage - V

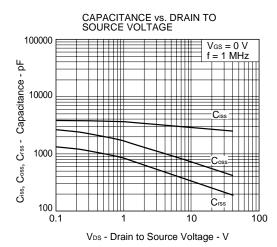
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

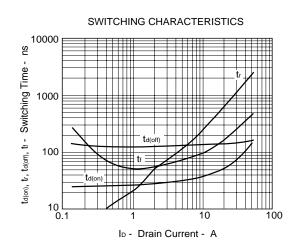


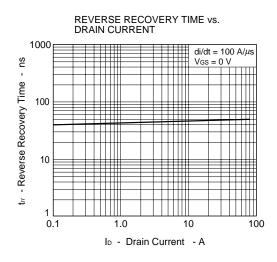


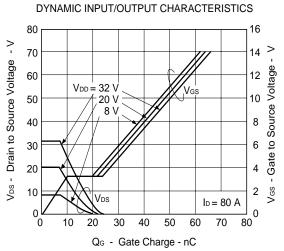


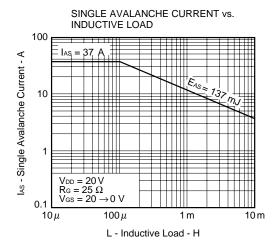


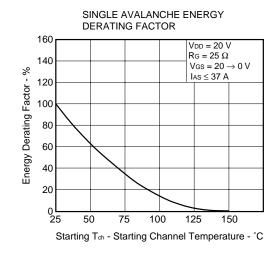








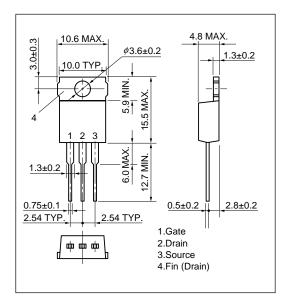




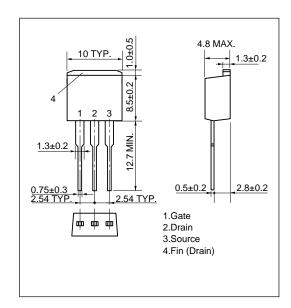


★ PACKAGE DRAWINGS (Unit: mm)

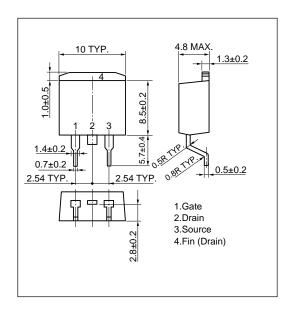
1) TO-220AB(MP-25)



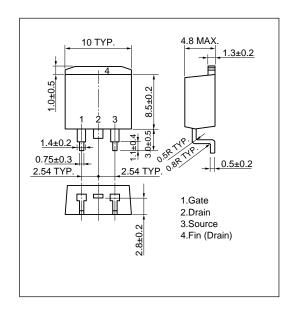
2) TO-262(MP-25 Fin Cut)



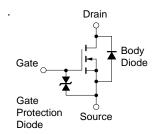
TO-263 (MP-25ZJ)



4) TO-220SMD (MP-25Z)^{Note}



EQUIVALENT CIRCUIT



Note This package is produced only in Japan.

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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