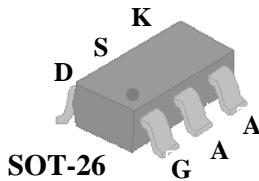




▼ Low On-Resistance

▼ Fast Switching Characteristic

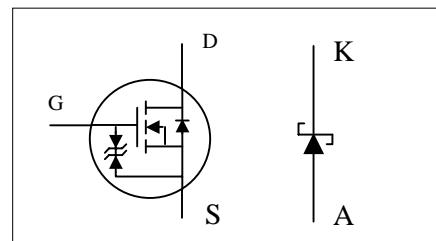
▼ Included Schottky Diode



BV_{DSS}	20V
$R_{DS(ON)}$	600mΩ
I_D	1A

Description

The Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage (MOSFET)	20	V
V_{KA}	Reverse Voltage (Schottky)	20	V
V_{GS}	Gate-Source Voltage (MOSFET)	± 6	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current ³ (MOSFET)	1	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current ³ (MOSFET)	0.8	A
I_{DM}	Pulsed Drain Current ¹ (MOSFET)	8	A
I_F	Average Forward Current (Schottky)	0.5	A
I_{FM}	Pulsed Forward Current ¹ (Schottky)	2	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation (MOSFET)	0.9	W
	Total Power Dissipation (Schottky)	0.9	W
T_{STG}	Storage Temperature Range	-55 to 125	°C
T_J	Operating Junction Temperature Range	-55 to 125	°C

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³ (MOSFET)	110	°C/W
	Maximum Thermal Resistance, Junction-ambient ³ (Schottky)	110	°C/W



Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	20	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.02	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=1\text{A}$	-	-	600	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_{\text{D}}=0.3\text{A}$	-	-	2	Ω
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	0.5	-	1.2	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=600\text{mA}$	-	1	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	1	uA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	10	uA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 6\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	± 10	uA
Q_g	Total Gate Charge ²	$I_{\text{D}}=600\text{mA}$	-	1.3	2	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=16\text{V}$	-	0.3	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=4.5\text{V}$	-	0.5	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time ²	$V_{\text{DS}}=10\text{V}$	-	21	-	ns
t_r	Rise Time	$I_{\text{D}}=600\text{mA}$	-	53	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_G=3.3\Omega$, $V_{\text{GS}}=5\text{V}$	-	100	-	ns
t_f	Fall Time	$R_D=16.7\Omega$	-	125	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	38	60	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=10\text{V}$	-	17	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	12	-	pF

Source-Drain Diode

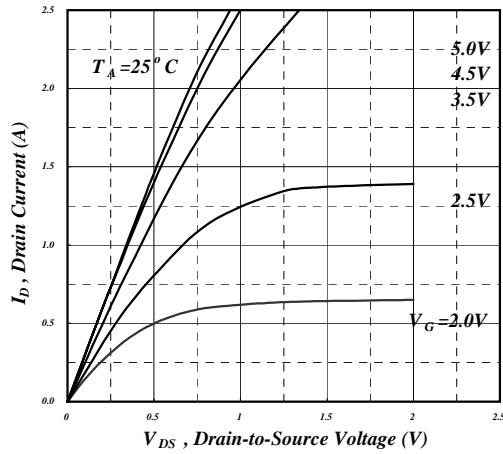
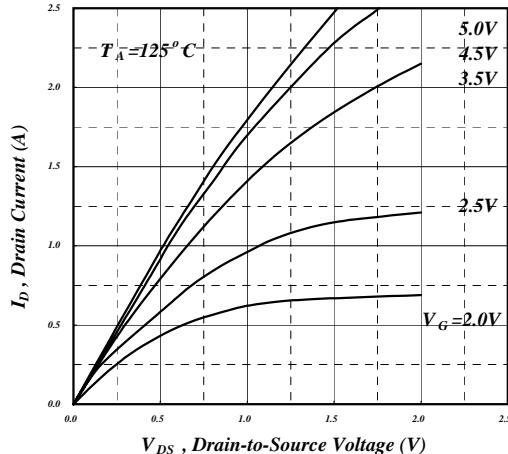
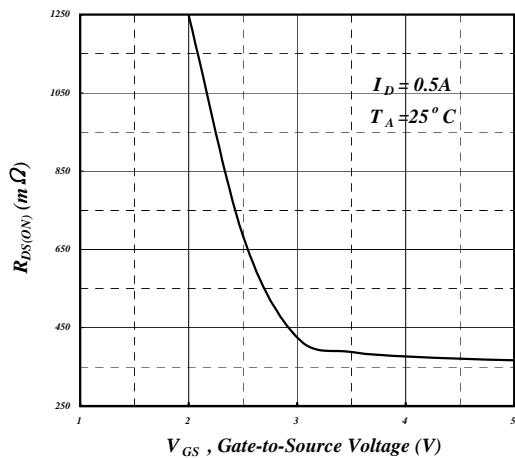
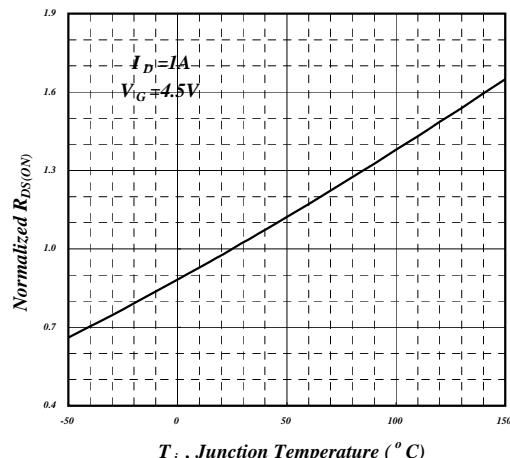
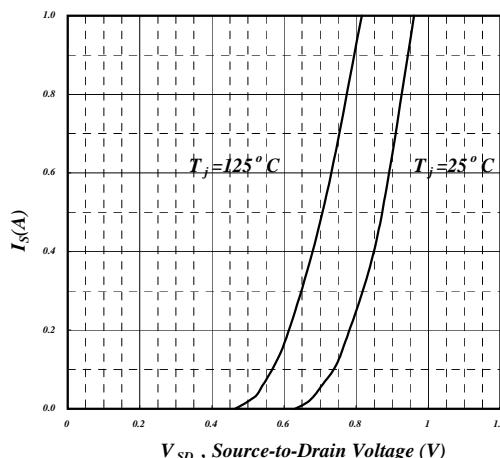
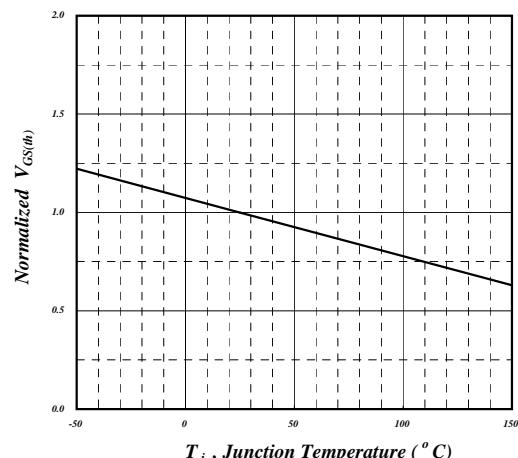
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Forward On Voltage ²	$I_{\text{S}}=750\text{mA}$, $V_{\text{GS}}=0\text{V}$	-	-	1.2	V

Schottky Characteristics@ $T_j=25^\circ\text{C}$

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_F	Forward Voltage Drop	$I_F=500\text{mA}$	-	-	0.5	V
I_{rm}	Maximum Reverse Leakage Current	$V_r=20\text{V}$	-	-	100	uA
C_T	Junction Capacitance	$V_r=10\text{V}$	-	21	-	pF

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- 3.Surface mounted on 1 in² copper pad of FR4 board, $t \leq 5\text{sec}$; $180^\circ\text{C}/\text{W}$ when mounted on min. copper pad.

**MOSFET****Fig 1. Typical Output Characteristics****Fig 2. Typical Output Characteristics****Fig 3. On-Resistance v.s. Gate Voltage****Fig 4. Normalized On-Resistance v.s. Junction Temperature****Fig 5. Forward Characteristic of Reverse Diode****Fig 6. Gate Threshold Voltage v.s. Junction Temperature**

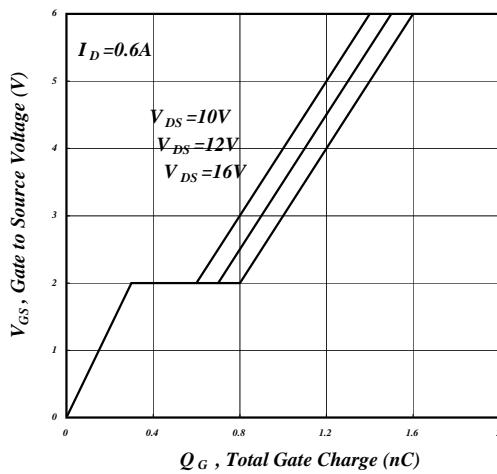


Fig 7. Gate Charge Characteristics

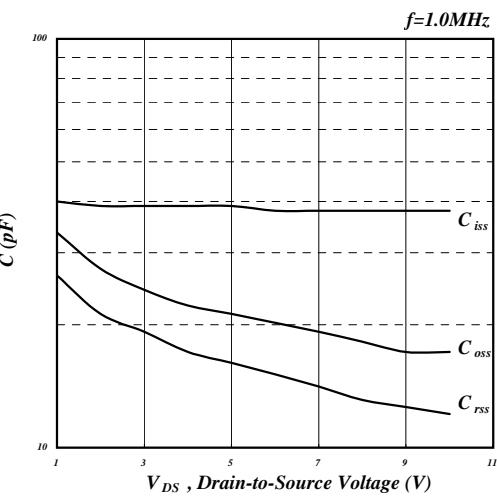


Fig 8. Typical Capacitance Characteristics

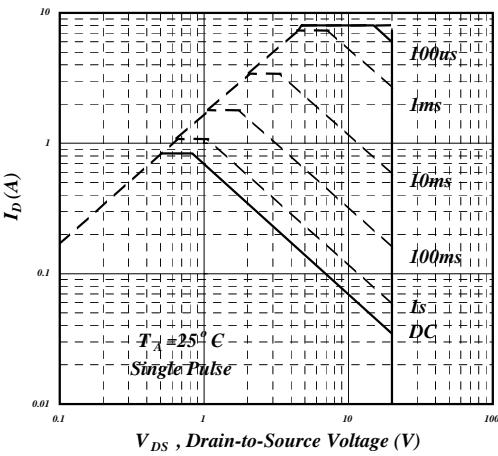


Fig 9. Maximum Safe Operating Area

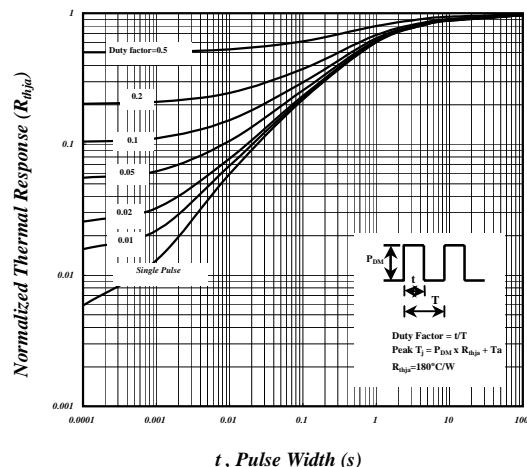


Fig 10. Effective Transient Thermal Impedance

SCHOTTKY DIODE

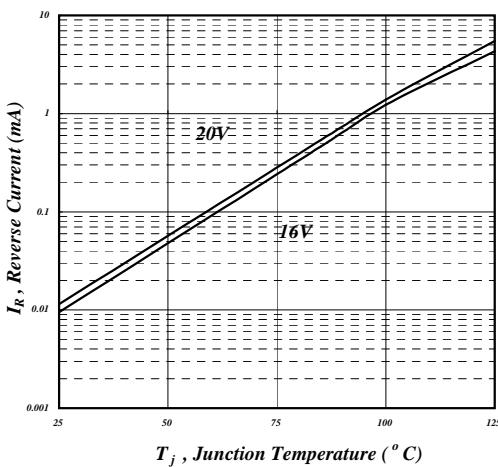


Fig 1. Reverse Leakage Current v.s. Junction Temperature

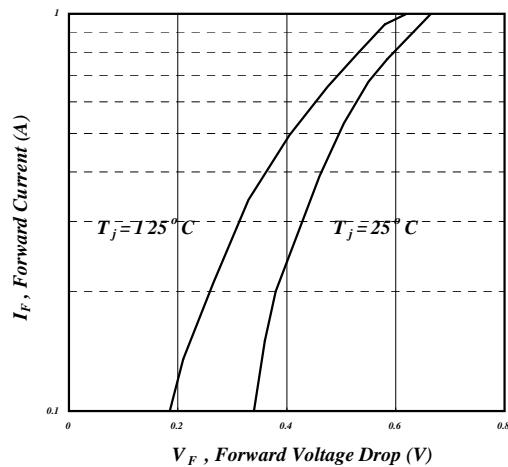


Fig 2. Forward Voltage Drop