

BM20 Bluetooth[®] Evaluation Board User's Guide

Index

Preface	4
1 OVERVIEW	4
1.1 INTRODUCTION 1.2 BM20 EVALUATION BOARD FEATURES 1.3 BM20 EVALUATION BOARD CONTENTS	4 4 5
2. Getting Started	8
 2.1 INTRODUCTION	
APPENDIX A. BM20 AUDIO EVALUATION BOARD SCHEMATICS	24
APPENDIX B. CLASS D AMPLIFIER DAUGHTER BOARD	29

Abbreviations List: HFP: Hands-free Profile AVRCP: Audio Video Remote Control Profile A2DP: Advanced Audio Distribution Profile HSP: Headset Profile NFC: Near Field Communication

1 OVERVIEW

1.1 INTRODUCTION

This user's guide describes the hardware and software setup for the BM20 Bluetooth[®] Evaluation Board. This board contains the hardware needed to evaluate the BM20 Bluetooth module. The BM20 module is mounted to an evaluation board that demonstrates the module's key features. The evaluation board contains:

- Stereo audio output
- Easy access to IO pins
- 6 push buttons to control audio playback
- Status LEDs
- The BM20 supports the following Bluetooth profiles: A2DP, AVRCP, and HFP/HSP
- A2DP stereo audio (Sink mode wi support for Sub-Band Coding (SBC),
- AVRCP media player remote control
- HFP/HSP for accepting a phone call support.

For data sheet and other details related to BM20 module, refer to the Microchip web

site at http://www.microchip.com.

This chapter discusses the following topics:

- BM20 Evaluation Board Features
- BM20 Evaluation Board Contents and Part Details

1.2 BM20 EVALUATION BOARD FEATURES

The BM20 Evaluation Board has the following features:

- Fully qualified Bluetooth version 4.1 module, fully compatible with Bluetooth version 3.0, 2.0, 1.2 system.
- Embedded BM20 module with postage-stamp size form factor of 15 x 29 x 2.5 mm (include shielding case)
- Embedded Bluetooth stack profiles: A2DP, AVRCP, and HFP/HSP
- Stereo audio output for highest quality audio
- Castellated SMT pads for easy and reliable PCB mounting
- Environmentally friendly, RoHS compliant
- Bluetooth SIG certified

1.3 BM20 EVALUATION BOARD CONTENTS

BM20 Evaluation Board contains the following components as shown in Figure 1-1 which describes the

evaluation board's interfaces and connectors. Table 1-1 describes the various components of the evaluation board.

FIGURE 1-1: BM20 EVALUATION BOARD



Audio Output (JP23)

	0	Ο
1	2	S

Pin	Description	
1	AOHPL	
2	AOHPM	
3	AOHPR	

MIC (JP22)

Pin	Description	
1	MIC_P1	
2	AGND	
3	MIC_N1	

NFC tag connector (JP3)



Pin	Description
1	NFC_P
2	NFC_N

IO Connector (J1)

	11	9	7	5	3	1
ſ	Ο	0	0	0	0	
	Ο	Ο	Ο	Ο	Ο	Ο
	12	10	8	6	4	2

Pin	Description		
1	P0_0		
2	P3_0		
3	P2_0		
4	P1_5		
5	P0_4		
6	P0_1		
7	P2_4		
8	P0_2		
9	P0_3		
10	P0_5		
11	P2_7		
12	P2_4		

External AMP. Connector (JP26)



Pin	Description		
1	EXT_AMP_EN		
2	SPKR		
3	AGND		
4	SPKL		
5	AMP_POWER		

Battery connector (JP20) and Jack (JP19) (Alternative for BAT_IN)





Pin	Description
1	BAT_IN
2	GND

Hardware Component	Description	
BM20	Bluetooth [®] 4.1 Module	
USB Connector	DC 5V input, USB to UART for EEPROM R/W	
BAT connector & Jack	JP20 and JP19, alternative for BAT_IN	
Audio Out	Audio 3.5 mm jack for audio playback(P6).	
	External AMP Connector JP26	
Audio In	Audio 3.5 mm jack for Mono microphone input(P5) and AUX input(P7)	
Status LEDs	Red and Blue LEDs show the pairing/connection status	
MFB Button	Switch to turn on/off BM20 module (SW7; Button 0 in UI)	
Play/Pause Button	Button to play or pause the audio playback (SW8; Button1 in UI)	
Previous Track Button	Button to skip track backwards (SW4; Button 5 in UI)	
Next Track Button	Button to skip track forwards(SW5; Button 4 in UI)	
Volume Up Button	Button to increase volume (SW9; Button 2 in UI)	
Volume Down Button	Button to decrease volume (SW10; Button 3 in UI)	
Reset Button	Reset system (SW11)	
NFC Tag Connector	NFC Tag connector.(For NFC tag without Rectifier Circuit)(JP3)	
MP Tool Interface	Interfaces connect to VICTORIA board when use MP Tool	
MCU Interface	Interfaces connect to PIC32 Platform.	

2. Getting Started 2.1 INTRODUCTION

This chapter describes how the BM20 Evaluation Board works. Certain hardware and utilities are essential to

support the evaluation/development of demo applications. This chapter discusses the following topics:

- Hardware Requirements
- Software/Utility Requirements
- Module Configuration

2.2 HARDWARE REQUIREMENTS

2.2.1 HARDWARE SETUP

To setup the evaluation hardware, perform the following steps:

1. Make sure pin 1 / 2 / 3 of "SW12" in "Off / Off / On" state make .system in application mode.



2. Connect the portable mini-speaker 3.5 mm to the stereo audio out connector (P6).

2.2.2 USING THE EVALUATION BOARD

- 1. Connect Li-Ion batteries to JP20 or JP19.
- 2. Click MFB button to turn-on and enter pairing mode. The status LEDs will blink. Now the BM20 Evaluation board should be discoverable.
- 3. Turn on Bluetooth device manager on a host device (PC or smartphone), the host device will display a list of discoverable Bluetooth devices. The board display as "EDDY SHS".
- 4. If the pairing with the device is successful, BM20 evaluation board can connect to the host device. Once connected, BM20 evaluation board enables Advanced Audio Distribution Profile (A2DP) for audio playback and Audio Video Remote Control Profile (AVRCP) for player control.

2.3 APPLICATION DEMONSTRATION

2.3.1 AUDIO DEMONSTRATION

In this demonstration, user can play an audio stream on both BM20 evaluation boards using a computer or smartphone. The following are the steps to perform the demonstration.

- 1. Connect BM20 evaluation board to a host device (PC or smartphone) that has an audio source.
- 2. Connect headphones (or mini-speakers) to BM20 evaluation board P6.
- Open the audio source on the host device. Microchip recommends using media player.(e.g. Microsoft Media Player, iTunes, and Android).
- 4. Start the audio stream on the media player.

When BM20 evaluation board is connected to an audio source compatible with Bluetooth AVRCP, the audio control buttons are use to:

- Control the volume of audio output.
- Go to the previous track.

- Go to the next track.
- Start / stop playing the current track.

FIGURE 1-2: BM20 EVALUATION BOARD AUDIO CONTROL BUTTONS



FIGURE 1-3: BM20 EVALUATION BOARD WITH EXTERNAL CLASS-D AMPLIFIER (It need modify the setting in UI to support external amplifier)



HSP/HFP DEMONSTRATION

2.3.2

In this demonstration, user can explore the hands-free profile setting to receive an incoming voice call from a

paired smartphone. This demonstration requires a microphone. It would be good to use a PC headset/microphone (with two-plugs). The following are the steps to perform the demonstration.

- 1. Connect the headset/microphone to BM20 evaluation board's audio out connector (P6) and MIC input (P5) respectively.
- 2. Connect BM20 evaluation board to a smartphone that supports the A2DP and HFP/HSP Bluetooth profiles.
- 3. From another one phone, initiate a call to the smartphone that is paired with BM20 evaluation board. The A2DP stream pauses and the ringtone plays on the headset/microphone.
- 4. Click button "MFB" on BM20 evaluation board to accept the incoming call.

2.4 SOFTWARE/UTILITY REQUIREMENTS 2.4.1 UI SETTING

Step1. Open UI tool

IS20XXS_002UI v00.01.0	00.00	No. of Contraction	x
Version & Devid	ce		
IC Package:		v	
Module Name:		Ţ	
Customer Versic	on:		
		DICS	
Save	Export	Generator	?
Load	Edit	Exit	

Step2. Firstly, you can load default UI setting or previous setting file.

IC Package:	開啟舊檔		
Module Name:	UI tool 🕨 IS20XX_002UI	v00.01.00.04	201.000.01.0 🔎
	組合管理 ▼ 新増資料夾	8==	• 🗊 🕐
Sustomer Version:	🎳 ISRT 🔷	名稱	修改日期
	🐌 MP tool		2015 (2/12 15-2
	J MPAF		2015/2/12 15:2
Save	🌗 NFC tag writer		2015/2/12 15:2
	퉬 Patch_Release 🔤	IS2013SUI DEFAULT TABLE.txt	2015/2/12 15:2
	퉬 UART Tool 🗏	S2015SUI DEFAULT TABLE.txt	2015/2/12 15:2
Load	🍌 UI tool	IS2020 EVB_UART.txt	2015/3/26 09:0
	🍌 20140626 charging test	IS2020SUL_DEFAULT_TABLE.txt	2015/2/12 15:2
	🁪 backup	IS2021SUI_DEFAULT_TABLE.txt	2015/2/12 15:2
	IS20XX_002UI v00.01.00.0	IS2023SUI_DEFAULT_TABLE.txt	2015/2/12 15:2
	IS20XX_002UI v00.01.00.0	IS2025SUI_DEFAULT_TABLE.txt	2015/2/12 15:2
	JS20XX_002UI v00.01.00.0		
	IS20XX_002UI v00.01.00.0		
	JS20XX_203UI v00.00.00.0		
	JS20XXS_203UI v00.00.0C ▼	•	,
	檔案2番(NI)- IS2020S		

Step3. Click "Edit" to modify the settings meet your needs.

IC Package:	IS2020S_002_5	SHS 🗾
Module Name:		*
Customer Versio	on:	
Save	Export.	PICS Generator
	Edd	Evit

Step4. In the main settings, it can enable or disable supported profile or function which system need. Click "Next" for other setting.

✓ Bth I(PU2) IV Bth2	(P27) IV Bth3(P05)
 Btn5(P03) 	
T AUX In	T Buzzer
External Amp.	UART Command
High Active	🗖 Rx IND
C Low Active	Tx IND
🗖 Internal Amplifier	
	■ AUX In ■ External Amp.

Step5. You can do system and functional setting in these pages.

LED Setup2 Tone Setup	PMU Setup CODEC Setup
Sys. Setup1 Sys. Setup2 S	Sys. Setup3 Button Setup LED Setu
Power Switch Setting	
Power Switch Type	Power ON Directly
Buzzer Setting	
Buzzer Output Enable	Disable - Help
Buzzer Output Type	Pulse _
Buzzer Default On/Off	Off
- Power On Buzzer Mode	0x03 Triple 50ms 🕑
Power Off Buzzer Mode	0x02 Dual 50ms -
Ring Buzzer Mode	0x05 Dual 100ms +
Enter Pairing Buzzer Mode	0x0A Single 500ms +
Pairing Complete Buzzer Mode	0x04 Single 100ms -
Battery Low Buzzer Mode	0x06 Triple 100ms -
NFC Buzzer Mode	0x0B Dual 500ms +
Link Loss Buzzer Mode	0x0C Triple 500ms -
Link Weak Buzzer Mode	0x08 Dual 200ms =
Disas DMMA Examinant	2700 (co 2000)

Step6. After finish parameter set up, click "Finish" button and a message will remind you check EEPROM size on your system.

	, I Tone Se	etup	PMU	Setup	CO	DEC Setup
Sys. Setup1	Sys. Setup2	Sys. S	etup3	Button Setu	ıp	LED Setup1
-Power Switch Power Switc	Setting ch Type		MFB Powe	er ON/OF		Help
-Buzzer Setting	1					
Buzzer Put	out Enable		Dicablo	×		Help
No BUZZAT	otification					
Duzzer						
Buzzer	Current E2Prom size is	4096 bytes!!				
Power 0	The Suggested EEPRC	OM must be e	qual to or g	reater than 240	.32	
Power C	The Suggested EEPRC	OM must be e	qual to or g	reater than 240	.32 2	
Power (1 Power (The Suggested EEPRC	0M must be e	qual to or g	reater than 240	2	
Power (Power (Ring Bu Enter P,	The Suggested EEPRC	OM must be e	qual to or g	reater than 240	2 12	
Power C Power C Ring Bu Enter P Pairing Corr	The Suggested EEPRC	DM must be e	qual to or g	e 100ms 🚽	32 2 定	
Power C Power C Ring Bu Enter P Pairing Com Battery Low	The Suggested EEPRC nplete Buzzer Mode r Buzzer Mode	DM must be e	qual to or g	e 100ms 💌	32 2 定	
Power C Power C Ring Bu Enter P Pairing Corr Battery Low NFC Buzzer	The Suggested EEPRC Iplete Buzzer Mode r Buzzer Mode r Mode	DM must be e	ox04 Singl 0x06 Triple 0x06 Dual	e 100ms ♥ 500ms ♥	32 2 定	
Power C Power C Ring Bu Enter P Pairing Com Battery Low NFC Buzzee Link Loss B	The Suggested EEPRC nplete Buzzer Mode r Buzzer Mode r Mode iuzzer Mode	DM must be e	0x04 Singl 0x06 Triple 0x06 Dual 0x0C Triple	e 100ms v 500ms v 2 500ms v	32 2 定	
Power C Power C Ring Bu Enter P Pairing Corr Battery Low NFC Buzzer Link Loss B Link Weak I	The Suggested EEPRC Iplete Buzzer Mode I Buzzer Mode Iuzzer Mode Buzzer Mode	DM must be e	Qual to or g 0x04 Singl 0x06 Triple 0x08 Dual 0x08 Dual	e 100ms v 500ms v 200ms v	32 2 定	
Power C Power C Ring Bu Enter P Pairing Corr Battery Low NFC Buzzer Link Loss B Link Weak I Diago DMM	The Suggested EEPRC iplete Buzzer Mode r Buzzer Mode iuzzer Mode Buzzer Mode Buzzer Mode	DM must be e	0x04 Singi 0x06 Triple 0x06 Triple 0x0C Triple 0x0C Triple 0x08 Dual 12700	e 100ms 500ms 200ms 200ms (50 20000)	.32 2 定	

Step7. Click "Save"	button to save these UI	parameter as a ".txt" file
-		

io i ackage.	IS2020S_002_9	shs 🔄
Module Name:		-
Customer Versio	n:	
	1	
	Export.	PICS Generator
Save		Generator

Step8. We will use MPET tool to merge it with EEPROM table and use EEPROM tool load these parameter to system.

2.4.2 DSP TOOL SETTING

Step1. Open DSP tool

Step2. Select IC version ""IS2020_XXX_SHS" (XXX is the version of chip, e.g. IS2020S-203)

Main Function Voice Function Audio	Function I2S/PCM
IS2020S_002_SHS DSP Configu	ation Tool 0.2.9.B
	 Speaker Phone Speaker
Load Default txt	DSP Parameter DSP Default

Step3. You can setup all voice and audio function in these pages.

CVSD Digital Equalizer Noise AEC/	
Encoder Gain/Comfort (EQ) Reduction (NR)	Filter (Codec Gain)
$\begin{array}{c} \textbf{CVSD} \\ \textbf{Decoder} \end{array} \longrightarrow \begin{array}{c} \textbf{Noise} \\ \textbf{Reduction} \\ \textbf{(NR)} \end{array} \longrightarrow \begin{array}{c} \textbf{Equalizer} \\ \textbf{(EQ)} \end{array} \longrightarrow \begin{array}{c} \textbf{Filter} \end{array}$	→ DAC (Speaker Gain) →
Iter NR EQ SpkGain MIC Gain/ComfortNoise AEC/AES	
Speaker – HighPass Filter 🛛 Cutoff Freq: 120Hz 🔹 👻	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	DSP Parameter
Speaker – HighPass Filter Cutoff Freq: 120Hz • MIC – HighPass Filter Cutoff Freq: 120Hz •	DSP Parameter DSP Default

Step4. Click "Save" button to save these DSP parameter as a ".txt" file after finish all DSP setting.

Then use MPET tool to merge it with EEPROM table and use EEPROM tool load these parameter to system.

ain Function Voice Function	Audio Function I2S/PCM	
SBC/AAC Decoder	$\begin{array}{c} \text{Audio} \\ \text{Effect} \end{array} \rightarrow \begin{array}{c} \text{Equalizer} \\ \text{(EQ)} \end{array} \rightarrow \begin{array}{c} \text{DAC} \\ \text{(Speaker} \\ \text{Gain)} \end{array} \rightarrow \begin{array}{c} \end{array}$	
	LineIn - Audio Input	
neln SPK Gain Sound Effect	EQ	
Silence Detection Threshold	0×1A:-84dB0∨ ▼	
nitial Lineln SPK Gain	0×0A •	
ineln MaxLevel	F	
ineln MinLevel	0 -	
ineln ADC Gain	-6dB, 0×00 🔹	
Load		DSP Parameter
Ludu		DSP Default
Save Default.txt		Exit

2.4.3 MERGE TOOL SETTING

Step1. Open MPET tool, click "Next" to set up.



Step3. Select the bin file (full EEPROM table) and click "Next"

Select latest ISSC Default Browse ISSC default as a base	iss
Please choose the default bin file	1
D:95 BT5502\Tool\MP tool\MP_V2.1.26.4332\issc_default_bin\IS2020S_002_SH	Browse
BIN file description: Format Version : 4 Solution Name : IS2020S_002_SHS_V2.1 EEPROM Version : 1.0.3.1 Company Name : ISSC Project Name : IS2020S_002_SHS_V2.1 TXT Files : IPF Files :	
÷	
2	

Step4. If the bin file had included UI, DSP or patch code parameter, you can see them in block 1 as the figure below. If you want keep the in your new system, you can select the item which you want to keep and use "pull down" button to add them to merge list. If you want to add new parameter (e.g. UI and DSP parameter), click "+" button to add these files into tool for merge with EEPROM table.

Customized settings in selected BIN			
FileName	Version	Brief	1
bin://BM20 EVB_DSP_20140805.txt:00 bin://BM20 EVB_UI.txt:01	0.2.1.1	Speech/A	udio Qualit
bin://IS2020S_002_8051_patch_20140725.txt:02	0.0.0.1	Function	/Feature En
Merge List FileName 4		0	00
D:95 BT5502\Tool\DSP tool\002 version\BM2020-0 D:95 BT5502\Tool\UI tool\2020_test.txt	02_DSP_201404	14.txt	

Step5. Select an output path and create a name for the merged EEPROM table.

Select Destination to Save Output Assign output name and path	issc
Please select output file name and path D:95 BT5502\Tool\MP tool\MP_V2.1.26.4332\IS2020-002 test.ipf	1 Output File
	2
Back (B)	Next (N) Cancel

Step6. Click "Generate" button to generate the new EEPROM table.

Double check the selections	ISSC
Click Generate to continue, or click Back if you want to review or change setting.	
Merge Type: UI Patch Only (Customized UI Update, *.ipf) Solution (IC): IS2020S_002_SHS_V2.1 Output File: D:\95 BT5502\Tool\MP tool\MP_V2.1.26.4332\IS2020-002 test.ipf Merge Files: D:\95 BT5502\Tool\UI tool\IS20XX_002UI v00.01.00.04\IS2020SUI_DEFAUL T_T	ABLE.tx
	÷

Step7. Select if you want use new setting of these parts.

Click the check box, the parameters will be decided by following the UI Patch file.	
CSYS:RUN-TIME] Device List 1	
[SYS:RUN-TIME] Device List 2	
SYS:RUN-TIME] Device List 3	
SYS:RUN-HMEJ Device List 4	
SVS-PINLTIMET Device List 6	
SYS'RIIN-TIME] Device List 7	
SYS:RUN-TIME] Device List 8	
SYS:RUN-TIME] Device Link priority	
[SYS RUN-TIME] Device A2DP Index	
V ₁	
1	
2	
Peok (P) Nut (T)	Canaal

Step8. Now you have a merged patch file (*.ipf file).



2.4.4 Procedure to Update EEPROM Parameters



Step 1. Make sure SW12 in "ROM TEST" mode.



Step 2. Connect EVB "P1" port and PC by USB cable. **LED1** & **LED2** on EVB will keep lighting. **Step 3.** Run the **E2PROM_tool.exe** program and a window will be come up as below

		MICROCHIP
Access Config	juration	
COM Port	COM40 -	
Vrite EEPRO	1	
input Path	D:\95 BT5502\Tool\EEPROM tool\IS2020_203 test.ipf	Browse
		Write
		Exit
		5

Step 4. Specify the COM Port.

Access Config	juration	
COM Port	COM40	
Write EEPROM	И	
Input Path	D:\95 BT5502\Tool\EEPROM tool\IS2020_203 test.ipf	Browse
		Write
		Exit

Step5. Press "Browse" to choose the file where you want to write EEPROM table or patch file. **Step6.** Press" **Write**" to write these setting to EEPROM

Access Config	juration	
COM Port	COM40 •	
Write EEPROI	4	
Input Path	D:\95 BT5502\Tool\EEPROM tool\IS2020_203 test.ipf	Browse
		Write
		Exit
		[

Step7. After finish data update, remove USB cable and make SW12 to "ROM APP" mode and reboot.



Now EVB can use the new setting after updated EEPROM parameter.

2.5 MODULE CONFIGURATION

2.5.1 Mode Settings

Setting in Mode Switch:

For ROM chip application

Mode	SW12 Setting	Switch 12 PIN Definition
Test Mode	ON 1 2 3	1: ON (P2_0: LOW) 2: OFF (P2_4: HIGH) 3: ON (EAN: HIGH)
Application Mode	ON 1 2 3	1: OFF (P2_0: HIGH) 2: OFF (P2_4: HIGH) 3: ON (EAN: HIGH)

APPENDIX A. BM20 AUDIO EVALUATION BOARD SCHEMATICS

EVB block diagram





P0.05 P3.05 P3.05 P2.05 P2.05 P0.45 AOHPM

N N

10u/16V MIC_BIAS

C54 4.7u/10V

-{**|**---||∙

MIC_BIAS





AD AP_IN

302

£

DC POWER SOURCE

















POWER / UART HCI

BM20 EVB





APPENDIX B. CLASS D AMPLIFIER DAUGHTER BOARD

BM20 EVB reserves an interface (JP26) to connect with an external class D amplifier daughter board for speaker application demonstration.

Here is a daughter board example of NAU8223 3.1W class D amplifier.



On the daughter board, we reserve pin header for amplifier gain setting, L/R channel speaker connector and BM20 EVB connector.

It can be connected with BM20 EVB to emulate a speaker application.

The schematics of the daughter board as below:

