# BD676, BD676A, BD678, BD678A, BD680, BD680A, BD682

# Plastic Medium-Power Silicon PNP Darlingtons

This series of plastic, medium-power silicon PNP Darlington transistors can be used as output devices in complementary general-purpose amplifier applications.

#### **Features**

- Pb-Free Package is Available\*
- High DC Current Gain h<sub>FE</sub> = 750 (Min) @ I<sub>C</sub> = 1.5 and 2.0 Adc
- Monolithic Construction
- BD676, 676A, 678, 678A, 680, 680A, 682 are complementary with BD675, 675A, 677, 677A, 679, 679A, 681
- BD678, 678A, 680, 680A are equivalent to MJE 700, 701, 702, 703

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$		Vdc
BD676, BD676A	020	45	
BD678, BD678A		60	
BD680, BD680A		80	
BD682		100	
Collector-Base Voltage	V <sub>CB</sub>		Vdc
BD676, BD676A		45	
BD678, BD678A		60	
BD680, BD680A		80	
BD682		100	
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current	I <sub>C</sub>	4.0	Adc
Base Current	Ι <sub>Β</sub>	0.1	Adc
Total Device Dissipation	$P_{D}$		
@ T <sub>C</sub> = 25°C		40	W
Derate above 25°C		0.32	W/°C
Operating and Storage Junction	T <sub>J</sub> , T <sub>stg</sub>	-55 to	°C
Temperature Range		+150	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance,	$R_{\theta JC}$	3.13	°C/W
Junction-to-Case			

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

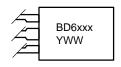


http://onsemi.com

4.0 A DARLINGTON POWER TRANSISTORS PNP SILICON 45, 60, 80, 100 V, 40 W



#### MARKING DIAGRAM



xxx = 76, 76A, 78, 78A, 80, 80A or 82

Y = Year WW = Work Week

#### ORDERING INFORMATION

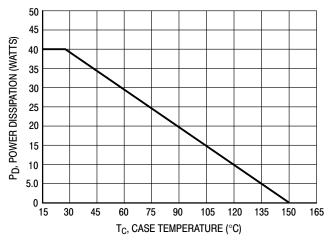
See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

## BD676, BD676A, BD678, BD678A, BD680, BD680A, BD682

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					1
Collector–Emitter Breakdown Voltage (Note 1) $(I_C = 50 \text{ mAdc}, I_B = 0)$	BD676, 676A BD678, 678A BD680, 680A BD682	BV <sub>CEO</sub>	45 60 80 100	- - -	Vdc
Collector Cutoff Current ( $V_{CE}$ = Half Rated $V_{CEO}$ , $I_B$ = 0)		I <sub>CEO</sub>	-	500	μAdc
Collector Cutoff Current $(V_{CB} = Rated \ BV_{CEO}, \ I_E = 0)$ $(V_{CB} = Rated \ BV_{CEO}. \ I_E = 0, \ T_C = 100^{\circ}C)$		I <sub>CBO</sub>	- -	0.2 2.0	mAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0)		I <sub>EBO</sub>	-	2.0	mAdc
ON CHARACTERISTICS				•	
DC Current Gain (Note 1) ( $I_C = 1.5 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ ) ( $I_C = 2.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ )	BD676, 678, 680, 682 BD676A, 678A, 680A	h <sub>FE</sub>	750 750	- -	
Collector–Emitter Saturation Voltage (Note 1) ( $I_C = 1.5 \text{ Adc}$ , $I_B = 30 \text{ mAdc}$ ) ( $I_C = 2.0 \text{ Adc}$ , $I_B = 40 \text{ mAdc}$ )	BD678, 680, 682 BD676A, 678A, 680A	V <sub>CE(sat)</sub>	- -	2.5 2.8	Vdc
Base–Emitter On Voltage (Note 1) $ (I_C = 1.5 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}) $ $ (I_C = 2.0 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}) $	BD678, 680, 682 BD676A, 678A, 680A	V <sub>BE(on)</sub>	- -	2.5 2.5	Vdc
DYNAMIC CHARACTERISTICS					
Small–Signal Current Gain (I <sub>C</sub> = 1.5 Adc, V <sub>CE</sub> = 3.0 Vdc, f = 1.0 MHz)		h <sub>fe</sub>	1.0	_	_

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.



**Figure 1. Power Temperature Derating** 

## BD676, BD676A, BD678, BD678A, BD680, BD680A, BD682

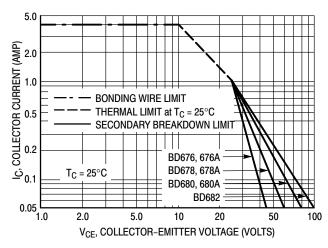


Figure 2. DC Safe Operating Area

There are two limitations on the power handling ability of a transistor average junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C-V_{CE}$  limits of the transistor that must be observed for reliable operation; e.g., the transistor must not be subjected to greater dissipation than the curves indicate.

At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by secondary breakdown.

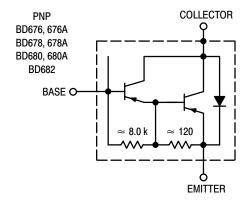


Figure 3. Darlington Circuit Schematic

#### **ORDERING INFORMATION**

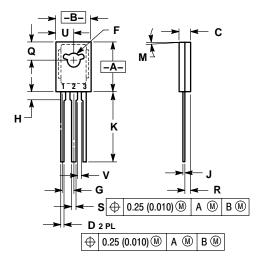
Device	Package	Shipping <sup>†</sup>
BD676	TO-225AA	
BD676G	TO-225AA (Pb-Free)	500 Units / Box
BD676A	TO-225AA	500 Units / Box
BD678	TO-225AA	500 Units / Box
BD678A	TO-225AA	500 Units / Box
BD680	TO-225AA	500 Units / Box
BD680A	TO-225AA	500 Units / Box
BD682	TO-225AA	
BD682G	TO-225AA (Pb-Free)	500 Units / Box
BD682T	TO-225AA	50 Units / Rail

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### BD676, BD676A, BD678, BD678A, BD680, BD680A, BD682

#### PACKAGE DIMENSIONS

TO-225AA CASE 77-09 ISSUE Z



- DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: INCH.
- 3. 077-01 THRU -08 OBSOLETE, NEW STANDARD

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
С	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094 BSC		2.39 BSC	
Н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
M	5° TYP		5°	TYP
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
٧	0.040		1.02	

STYLF 1:

**EMITTER** 

COLLECTOR

2. BASE 3.

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