

### 256Kx4 Monolithic CMOS Static RAM Commercial and Military

### ADVANCE INFORMATION

The ED184258CS/LPS/PS is a high speed, high performance, megabit density monolithic Static RAM with for both commercial and military applications. The device features separate input and output lines which provide fast read and write access to all memory locations.

It is intended for use in any application where large quantities of memory are required and /or board space is of prime concern.

Fully asynchronous, the ED184258CS/LPS/PS requires no clocks or refreshing for operation. Inputs and outputs are TTL compatible and allow for direct interfacing with common system bus architecture.

Two low power versions are available, PS and LPS, for military products. The LPS version includes a 2V Data Retention Function for battery back-up operation.

Military product compliant to MIL-STD-883, Paragraph 1.2.1 is available.

### Features

256Kx4 bit CMOS Static  
Random Access Memory

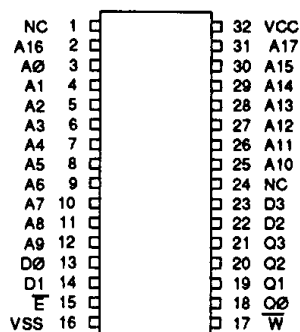
- Fast Access Times  
Commercial: 20, 25, and 35ns  
Military: 25, 35, 45, and 55ns
- $\bar{E}$  and  $\bar{G}$  Functions for Bus Control
- TTL Compatible I/O
- Separate Data Inputs and Outputs
- Fully Static, No Clocks

Thru-hole and Surface Mount Package Options

- 32 Pin Dual-in-line Packages  
Ceramic DIP, 400 mils Wide, No. 102  
Plastic DIP, 400 mils Wide, No. 5
- 32 Lead Ceramic SOJ, No. 140
- 32 Lead Plastic SOJ, No. 7

Single +5V ( $\pm 10\%$ ) Supply Operation

### Pin Configuration and Block Diagram



### Pin Names

A0-A17	Address Inputs
$\bar{E}$	Chip Enable
$\bar{W}$	Write Enable
$\bar{G}$	Output Enable
DQ0-DQ3	Common Data Input/Output
VCC	Power (+5V $\pm 10\%$ )
VSS	Ground
NC	No Connection

