TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SZ04AFS

: ±24mA (min) at V<sub>CC</sub> = 3V

at V<sub>CC</sub> = 5V, 50pF

#### INVERTER

#### Features

- High output current
- Super high speed operation : t<sub>pd</sub> = 2.4ns (typ.)
- Operation voltage range
- 5.5-V tolerant input

# on voltage range : $V_{CC (opr.)} = 1.65$ to 5.5V

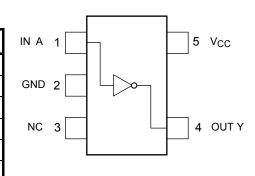
# Marking

# Product name

# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 6	V
DC input voltage	VIN	–0.5 to 6	V
DC output voltage	V <sub>OUT</sub>	–0.5 to V <sub>CC</sub> +0.5	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	±20 (Note1)	mA
DC output current	IOUT	±50	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

# Pin Assignment (top view)

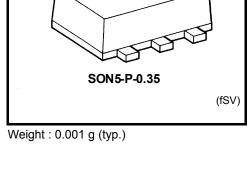


Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

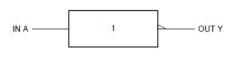
Note 1:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

Start of commercial production 2008-03



# <u>TOSHIBA</u>

# IEC Logic Symbol



Truth Ta	ble
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А	Y
II.	Н
Н	L

# **Operating Ranges**

Characteristics	Symbol	Rating		
Supply voltage	V <sub>CC</sub>	1.65 to 5.5	V	
Supply vollage		1.5 to 5.5 (Note 2)		
Input voltage	VIN	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
	dt/dv	0 to 20 (V_{CC} = 1.80 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)	ns/V	
Input rise and fall time		0 to 10 (V_{CC} = 3.3 V $\pm$ 0.3 V)		
		0 to 5 (V_{CC} = 5.0 V $\pm$ 0.5 V)		

Note 2: Data retention only

# **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol Test Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit		
		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit		
High-level input			1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	v	
voltage			_	2.3 to 5.5	$V_{CC} \times 0.7$	_	_	V <sub>CC</sub> × 0.7	_	v
Low-level input	VIL					_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	v
voltage	VIL			2.3 to 5.5		_	$V_{CC} \times 0.3$	_	V <sub>CC</sub> × 0.3	v
				1.65	1.55	1.65	_	1.55	_	
			I <sub>OH</sub> = -100 μA	2.3	2.2	2.3	_	2.2	_	
			10H = -100 μA	3.0	2.9	3.0	_	2.9	_	
				4.5	4.4	4.5	_	4.4	_	
High-level V <sub>OH</sub> V	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -4 mA	1.65	1.29	1.52	_	1.29	_	V	
		I <sub>OH</sub> = -8 mA	2.3	1.9	2.15	_	1.9	_		
			I <sub>OH</sub> = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I <sub>OH</sub> = -24 mA	3.0	2.3	2.68	_	2.3	_	
		I <sub>OH</sub> = -32 mA	4.5	3.8	4.2	_	3.8	_		
			100.0	1.65	_	0	0.1		0.1	-
				2.3	_	0	0.1		0.1	
Low-level output V <sub>OL</sub> V <sub>IN</sub> =		I <sub>OL</sub> = 100 μA	3.0	_	0	0.1		0.1	V	
			4.5	_	0	0.1		0.1		
	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 4 mA	1.65	_	0.08	0.24		0.24		
		I <sub>OL</sub> = 8 mA	2.3		0.1	0.3		0.3		
		I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4		0.4		
		I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55	_	0.55		
			I <sub>OL</sub> = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I <sub>IN</sub>	$V_{IN} = 5.5 V \text{ or GND}$		0 to 5.5		_	±1	_	±10	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND		_	_	2		20	μA

#### AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics S	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	$1.8\pm0.15$	1.1	5.3	9.6	1.1	9.8	ns
			$2.5\pm0.2$	1.0	3.2	5.3	1.0	5.7	
			$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.4	3.7	0.8	4.0	
			$5.0\pm0.5$	0.5	1.9	2.9	0.5	3.2	
		$\begin{array}{l} C_L = 50 \ pF, \\ R_L = 500 \ \Omega \end{array}$	$\textbf{3.3}\pm\textbf{0.3}$	1.0	3.0	4.6	1.0	4.9	
			$5.0\pm0.5$	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C <sub>IN</sub>		0 to 5.5		4			_	pF
Power dissipation capacitance	C <sub>PD</sub> (Note 3)	(Note 3)	3.3		10	_	—	—	ъЕ
		5.5	_	15	_	_	_	pF	

Note3: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

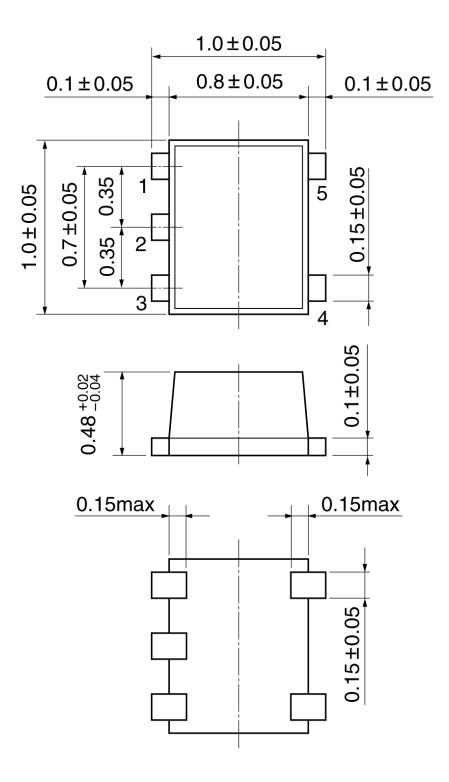
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# <u>TOSHIBA</u>

# **Package Dimensions**

SON5-P-0.35

Unit: mm



Weight: 0.001 g (typ.)

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