Low forward voltage drop, low power losses · High efficiency operation

Trench MOS Schottky technology

FEATURES

- RoHS Solder dip 275 °C max. 10 s, per JESD 22-B106 COMPLIANT
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

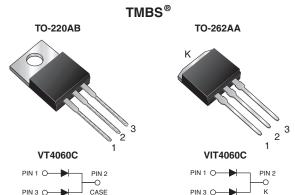
Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	6	0	V	
Maximum average forward rectified current	per device		40		^	
(fig. 1)	per diode	IF(AV)	20	0	A	
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I _{FSM}	2	40	А	
Voltage rate of change (rated V_R)		dV/dt	10	000	V/µs	
Operating junction and storage temperature ra	nge	T _J , T _{STG}	-40 to	o +150	°C	

Dual Trench MOS Barrier Schottky Rectifier

Ultra Low VF = 0.32 V at IF = 5.0 A



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PRIMARY CHARACTERISTICS			
I _{F(AV)}	2 x 20 A		
V _{RRM}	60 V		
I _{FSM}	240 A		
V_F at $I_F = 20$ A	0.48 V		
T _J max.	150 °C		
Package	TO-220AB, TO-262AA		
Diode variation	Common cathode		



VT4060C-E3, VIT4060C-E3

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5.0 A	T _A = 25 °C	V _F (1)	0.43	-	V	
	I _F = 10 A			0.48	-		
	I _F = 20 A			0.53	0.62		
	I _F = 5.0 A	T _A = 125 °C		0.32	-		
	I _F = 10 A			0.39	-		
	I _F = 20 A			0.48	0.57		
Reverse current per diode	V _B = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	6.0	mA	
	$v_{\rm R} = 60 v$ $T_{\rm A} = 12$	T _A = 125 °C		34	190		

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	1.5		°C/W	
	per device		0.8			

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	VT4060C-E3/4W	1.85	4W	50/tube	Tube	
TO-262AA	VIT4060C-E3/4W	1.46	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

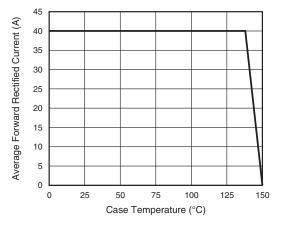


Fig. 1 - Maximum Forward Current Derating Curve

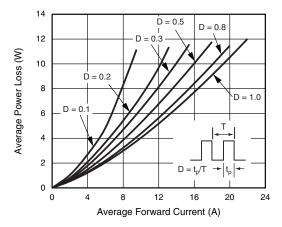


Fig. 2 - Forward Power Dissipation Characteristics Per Diode



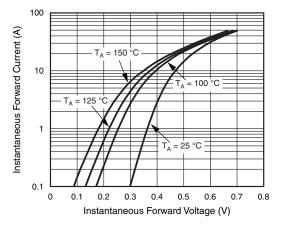


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

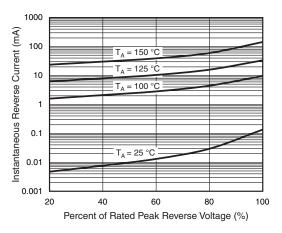


Fig. 4 - Typical Reverse Characteristics Per Diode

VT4060C-E3, VIT4060C-E3

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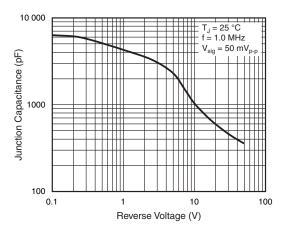


Fig. 5 - Typical Junction Capacitance Per Diode

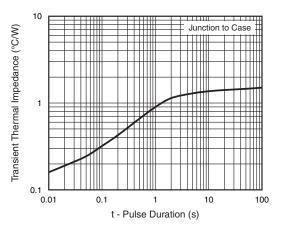


Fig. 6 - Typical Transient Thermal Impedance Per Diode



VT4060C-E3, VIT4060C-E3

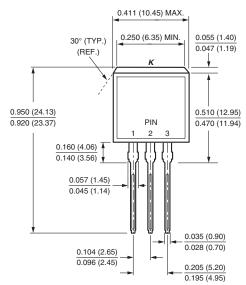
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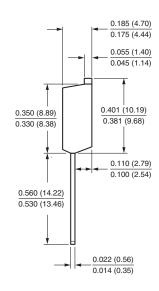
PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

0.415 (10.54) MAX 0.185 (4.70) 0.370 (9.40) 0.154 (3.91) 0.175 (4.44) 0.360 (9.14) 0.148 (3.74) 0.055 (1.39) 0.113 (2.87) 0.045 (1.14) 0.103 (2.62) 0.145 (3.68) 0.135 (3.43) 0.603 (15.32) 0.635 (16.13) 0.625 (15.87) 0.573 (14.55) PIN 0.350 (8.89) 2 0.330 (8.38) 0.160 (4.06) Π 1.148 (29.16) 0.140 (3.56) 1.118 (28.40) 0.110 (2.79) 0.100 (2.54) 0.057 (1.45) 0.045 (1.14) 0.560 (14.22) 0.530 (13.46) 0.105 (2.67) 0.095 (2.41) 0.035 (0.90) 0.104 (2.65) 0.028 (0.70) 0.022 (0.56) 0.205 (5.20) 0.096 (2.45) 0.014 (0.36) 0.195 (4.95)

TO-220AB

TO-262AA





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