

# **SAW Components**

SAW Duplexer LTE / E-UTRA Band 3

Series/type: B8533

Ordering code: B39182B8533P810

Date: December 10, 2014

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SAW Components

B8533

# **SAW Duplexer**

1747.5 / 1842.5 MHz

#### Data sheet



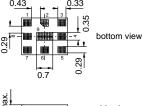
## Application

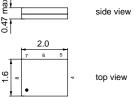
- Low-loss SAW duplexer for mobile telephone LTE / E-UTRA Band 3 systems
- Low insertion attenuation
- Low amplitude ripple
- Usable passband 75 MHz
- high Tx Rx isolation



#### **Features**

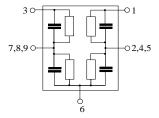
- Package size 2.0 x 1.6 mm<sup>2</sup>
- Package height 0.47mm max.
- RoHS compatible
- Approximate weight 4.2mg
- Package for Surface Mount Technology (SMT)
- Ni, gold-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitive Level 3





### Pin configuration

■ 3	Tx input
■ 1	Rx output
<b>6</b>	Antenna
<b>2</b> , 4, 5	To be grounded
<b>7</b> , 8, 9	To be grounded





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#### Characteristics

Temperature range for specification: T = -30 °C to +85 °C ANT terminating impedance: Z<sub>ANT</sub>=  $50 \Omega \parallel 3.5 \text{ nH}$ 

RX terminating impedance:  $Z_{RX} = 50 \Omega$ TX terminating impedance:  $Z_{TX} = 50 \Omega$ 

Characteristics TX-ANT 1)	min.	typ. @ 25°C	max.	
Center frequency f <sub>C</sub>	_	1747.5	_	MHz
1712.5 1782.5 MHz $\alpha_{LTE}^{2)3}$	_	2.0	2.8	dB
1712.5 1782.5 MHz $\alpha_{LTE}^{(2)}$	_	2.0	3.2	dB
<b>Amplitude ripple</b> per 5MHz channel $\Delta\alpha$				
1710.24 1784.76 MHz	_	0.5	_	dB
Input VSWR (Tx port)				
1710.24 1784.76 MHz 3)	_	1.7	2.2	
1710.24 1784.76 MHz	_	1.7	2.3	
Output VSWR (Ant Port) 1710.24 1784.76 MHz <sup>3)</sup>		1.5	2.0	
1710.24 1784.76 MHz	_	1.5	2.3	
Attenuation α				
10.0 1565.42 MHz	32	36	_	dB
703.0 748.0 MHz	40	46	_	dB
716.0 756.0 MHz	40	46	_	dB
814.0 849.0 MHz	38	43	_	dB
824.0 849.0 MHz	38	43	_	dB
830.0 845.0 MHz	38	43	_	dB
832.0 862.0 MHz	38	43	_	dB
880.0 915.0 MHz	36	42	_	dB
925.0 960.0 MHz	36	41	_	dB
1226.0 1250.0 MHz	32	37	_	dB
1496.0 1511.0 MHz	35	39	_	dB
1559.0 1563.0 MHz	38	43	_	dB
1565.42 1573.374MHz	38	44	_	dB
1573.374 1577.466MHz	40	46	_	dB
1577.466 1585.42 MHz	40	46	_	dB dB
1597.55151605.886MHz 1605.886 1680.0 MHz	40 22	52	_	dВ
	44	37		dВ
1807.5 1877.5 MHz $\alpha_{LTE}^{2)}$ 1920.0 1980.0 MHz	25	50 37	_	dB dB
2110.0 2170.0 MHz	27	36	_	dB



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Characteristics TX-A	NT <sup>1)</sup>		min.	typ. @ 25°C	max.	
2400.0	2500.0	MHz	24	38	_	dB
2440.0	2494.0	MHz	24	38	_	dB
2500.0	2570.0	MHz	23	37	_	dB
2620.0	2690.0	MHz	21	35	_	dB
3420.0	3570.0	MHz	18	28	_	dB
4900.0	5950.0	MHz	8	23	_	dB
5100.0	5385.0	MHz	8	28	_	dB
5130.0	5355.0	MHz	8	28	_	dB
6840.0	7140.0	MHz	-	24	_	dB
8550.0	8925.0	MHz	-	28	_	dB
10260.0	10710.0	MHz	-	34	_	dB
11970.0	12495.0	MHz	_	25	_	dB

<sup>1)</sup> Specified values are valid for a testing power of +10dBm

<sup>2)</sup> Averaged value of linear s-parameter over 5 MHz

<sup>3)</sup> Valid in the temperature range from 0°C to 85°C



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 $\equiv$ MD

#### Characteristics

Temperature range for specification: T = -30 °C to +85 °C ANT terminating impedance: Z<sub>ANT</sub>=  $50 \Omega$  || 3.5 nH

RX terminating impedance:  $Z_{RX} = 50 \Omega$ TX terminating impedance:  $Z_{TX} = 50 \Omega$ 

Characteristics ANT-RX <sup>1)</sup>	min.	typ. @ 25°C	max.	
Center frequency f <sub>C</sub>	_	1842.5	_	MHz
$\textbf{Maximum insertion attenuation} \qquad \qquad \alpha_{\text{max}}$				
1807.5 1877.5 MHz <sub>QLTE</sub> <sup>2)3)</sup>	_	2.6	3.8	dB
		2.6	4.2	dB
1807.5 1877.5 MHz $\alpha_{LTE}^{2}$	_	2.0	4.2	uБ
<b>Amplitude ripple</b> per 5MHz channel $\Delta\alpha$				
1805.24 1879.76 MHz	_	0.7	_	dB
Input VSWR (Ant port)				
1805.24 1879.76 MHz	_	1.5	2.2	
Output VSWR (Rx Port)				
1805.24 1879.76 MHz	_	1.9	2.5	
Attenuation α				
10.0 1710.0 MHz 95.0 MHz	40	48	_	dB dB
95.0 MHz 718.0 748.0 MHz	50 40	80 59	_	dВ
814.0 849.0 MHz	40	57	_	dB
832.0 862.0 MHz	40	57	_	dB
880.0 915.0 MHz	40	56	_	dB
1447.0 1463.0 MHz	40	50	_	dB
1615.0 1690.0 MHz	46	54	_	dB
1712.5 1782.5 MHz $\alpha_{LTE}^{2)}$	45	57	_	dB
1785.0 1790.0 MHz	10	49	_	dB
1920.0 2000.0 MHz	30	55	_	dB
2000.0 2400.0 MHz	30	44	_	dB
2400.0 2500.0 MHz	37	54	_	dB
2500.0 2570.0 MHz 2570.0 3515.0 MHz	46 40	58 49	_	dB dB
2570.0 3515.0 MHz 3515.0 3665.0 MHz	40	53		dВ
3665.0 3760.0 MHz	40	52	_	dB
3760.0 6000.0 MHz	34	42	_	dB
4900.0 5950.0 MHz	34	42	_	dB
5205.0 5660.0 MHz	34	46	_	dB
6000.013025.0 MHz	_	34	_	dB



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Characteristics ANT-RX <sup>1)</sup>	min.	typ. @ 25°C	max.	
7220.0 7520.0 MHz	_	45	_	dB
9025.0 9400.0 MHz	_	43	_	dB
10830.0 11280.0 MHz	_	36	_	dB
12635.0 13160.0 MHz	_	36	_	dB

<sup>1)</sup> Specified values are valid for a testing power of +10dBm

<sup>2)</sup> Averaged value of linear s-parameter over 5 MHz

<sup>3)</sup> Valid in the temperature range from 0°C to 85°C



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#### Characteristics

Temperature range for specification: T = -30 °C to +85 °C ANT terminating impedance:  $Z_{ANT} = 50 \Omega \parallel 3.5 \text{ nH}$ 

RX terminating impedance:  $Z_{RX} = 50 \Omega$ TX terminating impedance:  $Z_{TX} = 50 \Omega$ 

Characteristics TX-RX <sup>1)</sup>			min.	typ. @ 25°C	max.		
Isolation			α				
	1712.5 .	1782.5	MHz $\alpha_{LTE}^{2)3}$	52	59	_	dB
	1712.5 .	1782.5	MHz $\alpha_{LTE}^{2}$	50	59	_	dB
	1807.5 .	1877.5	MHz $\alpha_{LTE}^{2)4)}$	50	54	_	dB
	1807.5 .	1877.5	MHz α <sub>LTE</sub> 2)	48	54	_	dB

<sup>1)</sup> Specified values are valid for a testing power of +10dBm

#### **Maximum ratings**

Storage temperature range	T <sub>stg</sub>	-40/+90	°C	
DC voltage	$V_{DC}$	0 1)	V	
ESD voltage	V <sub>ESD</sub>	50 <sup>2)</sup>	V	Machine Model
	$V_{ESD}$	300 3)	V	Human Body Model
	$V_{ESD}$	500 <sup>4)</sup>	V	Charge Device Model
Input Power 1712.5 1782.5 MHz	$P_{IN}$	29	dBm	5 MHz LTE uplink @ 50°C, 5000h

<sup>&</sup>lt;sup>1)</sup> DC resistance at RX output might be less than 100 M $\Omega$  at elevated temperatures. Hence, we recommend usage of blocking capacitors.

<sup>2)</sup> Averaged value of linear s-parameter over 5 MHz

<sup>3)</sup> Valid in the temperature range from -30°C to 55°C

<sup>4)</sup> Valid in the temperature range from 0°C to 85°C

<sup>2)</sup> Acc. to JESD22-A115B (machine model), 10 negative & 10 positive pulses.

<sup>3)</sup> Acc. to JESD22-A114F (human body model), 1 negative & 1 positive pulses.

<sup>4)</sup> Acc. to JESD22-C101C (charge device model), 3 negative & 3 positive pulses.



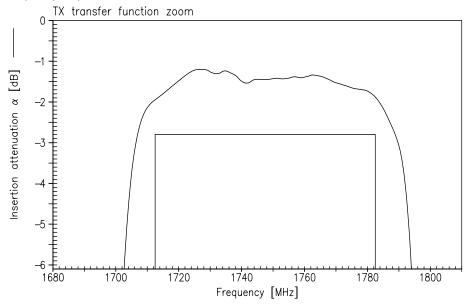
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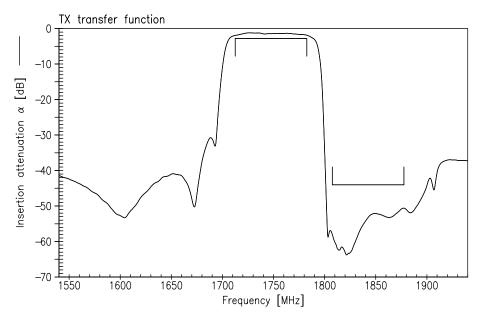
 SAW Duplexer
 1747.5 / 1842.5 MHz

Data sheet

#### $\equiv$ MD

# Frequency response TX - ANT

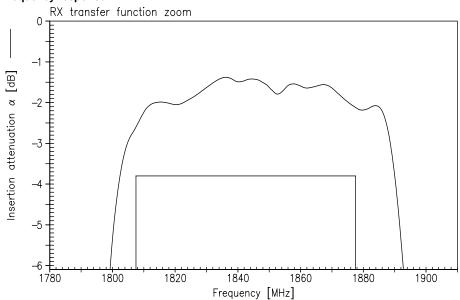


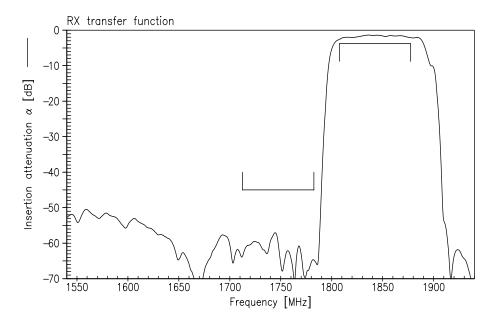






# Frequency response ANT - RX







1747.5 / 1842.5 MHz

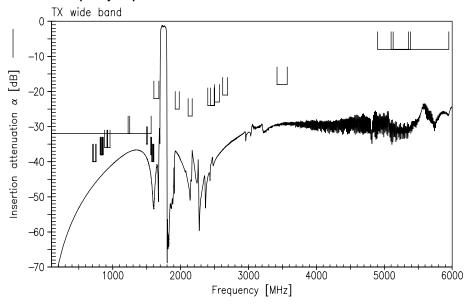
SAW Components B8533

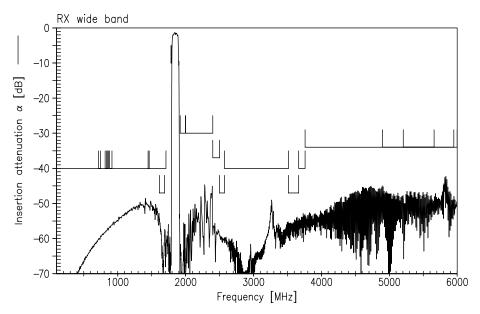
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Data sheet

 $\equiv$ M $\square$ 

# Wide band frequency response TX - ANT and ANT - RX







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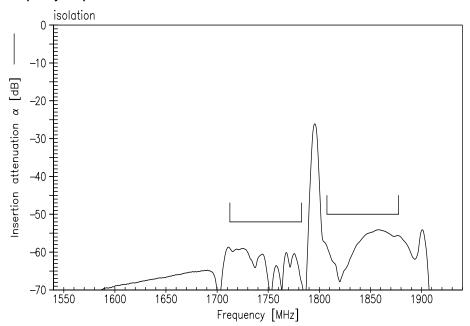
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Data sheet

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1747.5 / 1842.5 MHz

# Frequency Response TX - RX





**SAW Components** B8533 **SAW Duplexer** 1747.5 / 1842.5 MHz Data sheet  $\equiv$ MD VSWR of TX-port, RX-port and ANT-port TX port matching 3.0 3.0 2.5 2.5 VSWR 2.0 2.0 1.5 1.5 1.0 1700 1800 1750 normal impedance: 50.0 Ohm Frequency [MHz] RX port matching 3.0 3.0 2.5 2. 5 VSWR 2. 0 2.0 1.5 1.5 1.0 1.0 1850 1900 1800 normal impedance: 50.0 Ohm Frequency [MHz] ANT port matching 3.0 3.0 2.5 2.5 VSWR 2.0 2.0 1.5 1.5 1900<sup>1</sup>.0 1.0 1700 1750 1800 1850 normal impedance: 50.0 Ohm Frequency [MHz]



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Data sheet	=MD	

#### References

Туре	B8533
Ordering code	B39182B8529P810
Marking and Package	C61157-A8-A153
Packaging	F61074-V8247-Z000
Date Codes	L_1126
S-Parameters	B8529_NB_UN.s4p (narrow band, unmatched), B8529_WB_UN.s4p (wide band, unmatched), B8529_HD_WB_UN.s4p (HD wide band, unmatched) See file header for port/pin assignment table
Soldering profile	S_6001
RoHS compatible	RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8 <sup>th</sup> , 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.
Moldability	Before using in overmolding environment, please contact your EPCOS sales office.
Matching coils	See Inductor pdf-catalog     http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation     http://www.tdk.co.jp/etvcl/index.htm

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