

# FDPF085N10A N-Channel PowerTrench<sup>®</sup> MOSFET 100 V, 40 A, 8.5 mΩ

# Features

- $R_{DS(on)} = 6.5 \text{ m}\Omega \text{ (Typ.)} \otimes V_{GS} = 10 \text{V}, I_D = 40 \text{A}$
- Fast Switching Speed
- Low Gate Charge, Q<sub>G</sub> = 31 nC(Typ.)
- High Performance Trench Technology for Extremely Low  $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

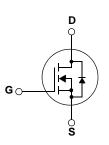
# **General Description**

This N-Channel MOSFET is produced using Fairchild Semiconductor<sup>®</sup>'s advance PowerTrench<sup>®</sup> process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

# Applications

- Consumer Appliances
- LED TV
- Synchronous Rectification for ATX / Sever / Telecom PSU
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter





# MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

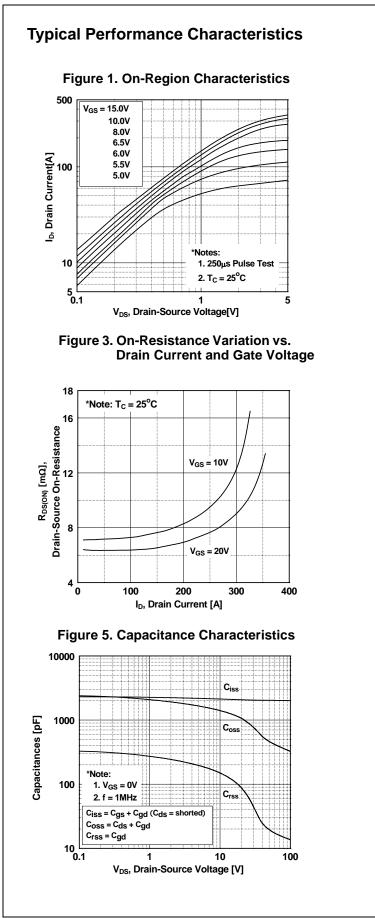
Symbol		Parameter	FDPF085N10A	Unit		
V <sub>DSS</sub>	Drain to Source Voltage		100	V		
V <sub>GSS</sub>	Gate to Source Voltage			±20	V	
I <sub>D</sub>	Drain Current	-Continuous (T <sub>C</sub> = 25°C)		40	Α	
	Drain Current	-Continuous ( $T_C = 100^{\circ}C$ )		28	- A	
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)		160	A	
E <sub>AS</sub>	Single Pulsed Avalanche Ene	269	mJ			
dv/dt	Peak Diode Recovery dv/dt (Note		(Note 3)	6.0	V/ns	
P <sub>D</sub>	Dower Discipation	$(T_{C} = 25^{\circ}C)$		33.3	W	
	Power Dissipation	- Derate above 25°C		0.22	W/ºC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +175	°C	
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C	

# **Thermal Characteristics**

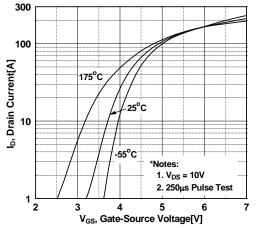
Symbol	Parameter	FDPF085N10A	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	4.5	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	C/W

March 2013

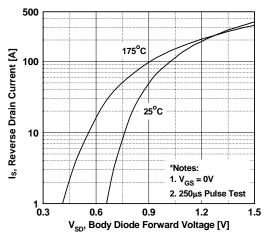
Device Marking Device Pac		Package	e	Reel Size	Та	pe Width		Quant	ity	
			TO-220	-	-		-		50	
Electrica	I Char	acteristics T <sub>C</sub> =2	25°C unless ot	herwise n	oted					
Symbol		Parameter		Test Conditions			Min.	Тур.	Max.	Unit
Off Charac	teristic	S								
BV <sub>DSS</sub>	Drain to	o Source Breakdown Vo	Itage I	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V		100	-	-	V	
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakd Coeffic	own Voltage Temperatu	re I <sub>I</sub>	$I_D = 250\mu A$ , Referenced to 2		25°C	-	0.07	-	V/ºC
1	Zoro C	Zana Cata Valtana Dusia Cumant			$V_{DS} = 80V, V_{GS} = 0V$			-	1	
DSS	Zelo G	Zero Gate Voltage Drain Current		$V_{\rm DS} = 80V, T_{\rm C} = 150^{\rm o}{\rm C}$			-	-	500	μΑ
I <sub>GSS</sub>	Gate to	Gate to Body Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$					-	-	±100	nA
On Charac	teristic	S								
V <sub>GS(th)</sub>	Gate Threshold Voltage			$V_{GS} = V_{DS}$	<sub>S</sub> , I <sub>D</sub> = 250μA		2.0	-	4.0	V
R <sub>DS(on)</sub>	Static E	Drain to Source On Resist	stance \	$V_{GS} = 10V, I_D = 96A$		-	6.5	8.5	mΩ	
9 <sub>FS</sub>	Forwar	d Transconductance	١	$V_{DS} = 10V, I_D = 96A$			-	76	-	S
C <sub>oss</sub> C <sub>rss</sub> C <sub>oss</sub> (er)	Revers Engry F	Capacitance e Transfer Capacitance Related Output Capacita ate Charge at 10V	f	$V_{DS} = 50V, V_{GS} = 0V$ $f = 1MHz$ $V_{DS} = 50V, V_{GS} = 0V$			468 20 752 31	620 - - 40	pF pF pF	
Q <sub>g(tot)</sub>		ate Charge at 10V		V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V I <sub>D</sub> = 96A (Note 4) f = 1MHz		-	31	40	nC	
Q <sub>gs</sub>		Source Gate Charge				-	9.7	-	nC	
Q <sub>gs2</sub>		harge Threshoid to Plate	eau i			-	5.0	-	nC	
		Drain "Miller" Charge	<u> </u>			-	7.5	-	nC	
ESR		ent Series Resistance (	J-J)				-	0.97	-	Ω
Switching								40	40	
t <sub>d(on)</sub>		n Delay Time	,	$V_{DD} = 50V, I_D = 96A$ $V_{GS} = 10V, R_{GEN} = 4.7\Omega$		-	18	46	ns	
t <sub>r</sub>		n Rise Time				-	22 29	54 68	ns	
t <sub>d(off)</sub>		ff Delay Time ff Fall Time				-	29	26	ns	
t <sub>f</sub>						(Note 4)	-	0	20	ns
	1	de Characteristics				1			40	
S	Maximum Continuous Drain to Source Dio						-	-	40	A
SM	Maximum Pulsed Drain to Source Diode F Drain to Source Diode Forward Voltage						-	-	160 1.3	A V
V <sub>SD</sub>		e Recovery Time		$V_{GS} = 0V, I_{SD} = 96A$		-	- 59	1.3		
t <sub>rr</sub>		e Recovery Charge		$V_{DD} = 50V, V_{GS} = 0V, I_{SD} = 96A$ dI <sub>E</sub> /dt = 100A/us			-	80	-	ns nC
Q <sub>rr</sub>	IVEAE126	Charge			, ο, ν μο		-	00	-	no
lotes:	a: Duloo widt	h limited by maximum junction to								



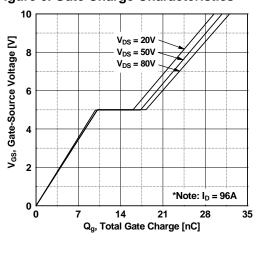
## Figure 2. Transfer Characteristics

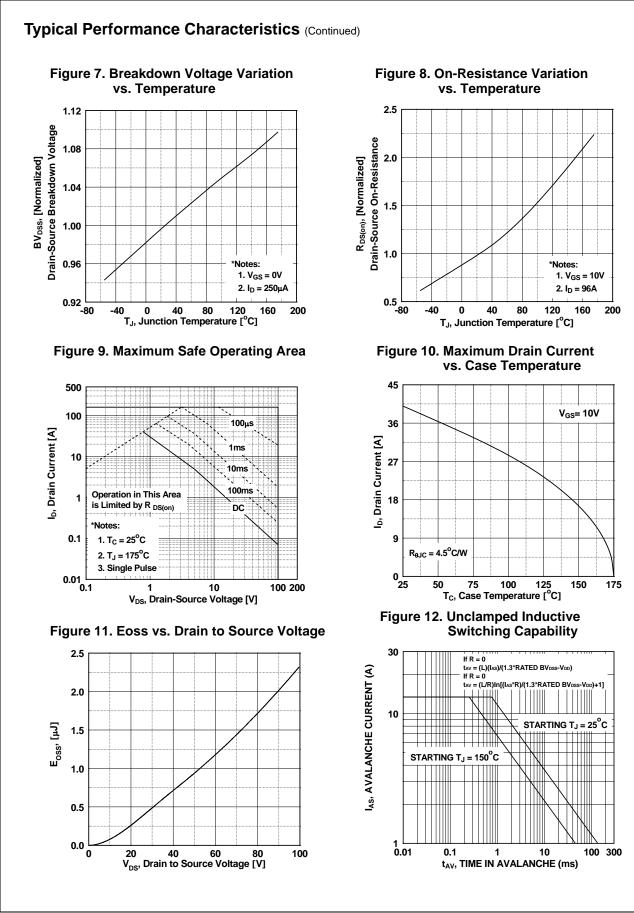


### Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

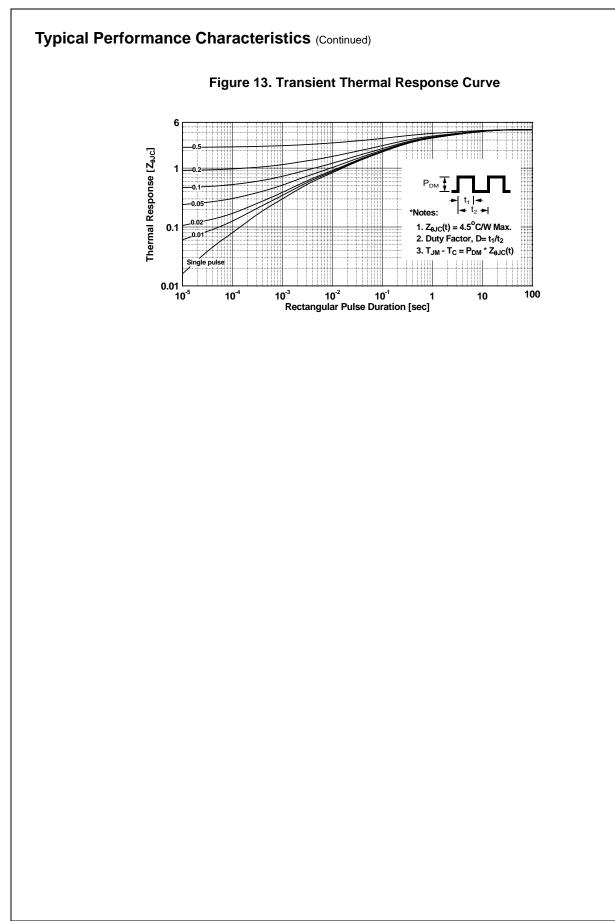


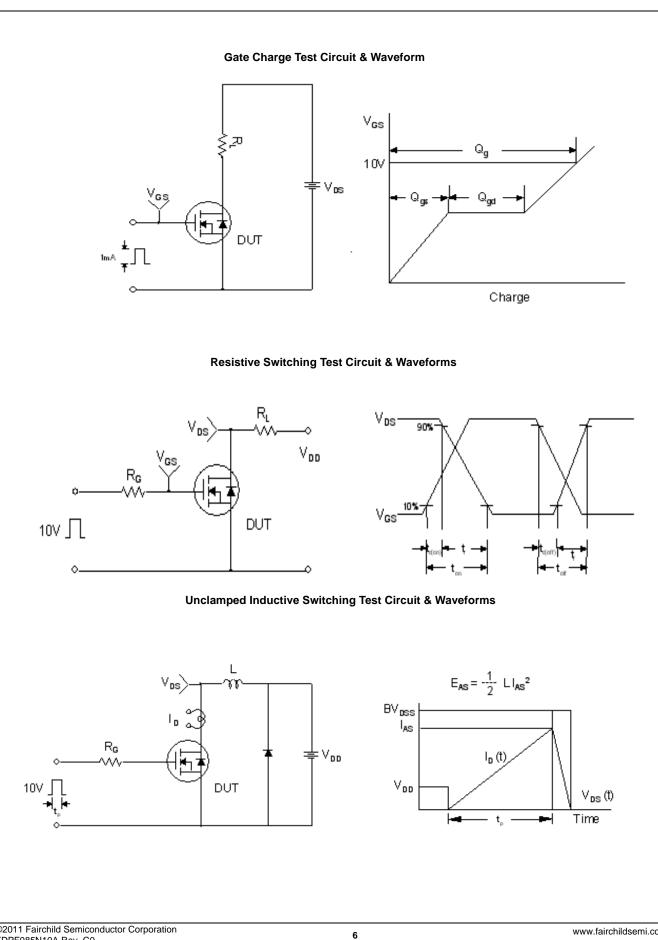






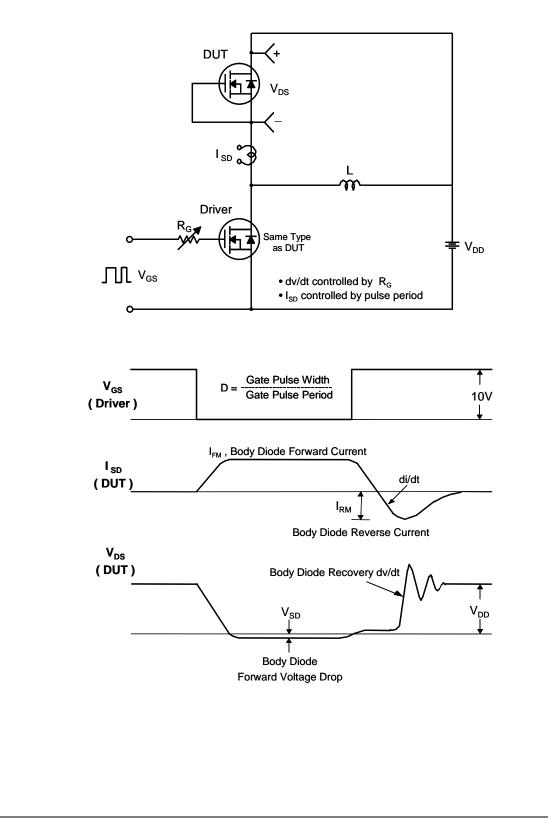
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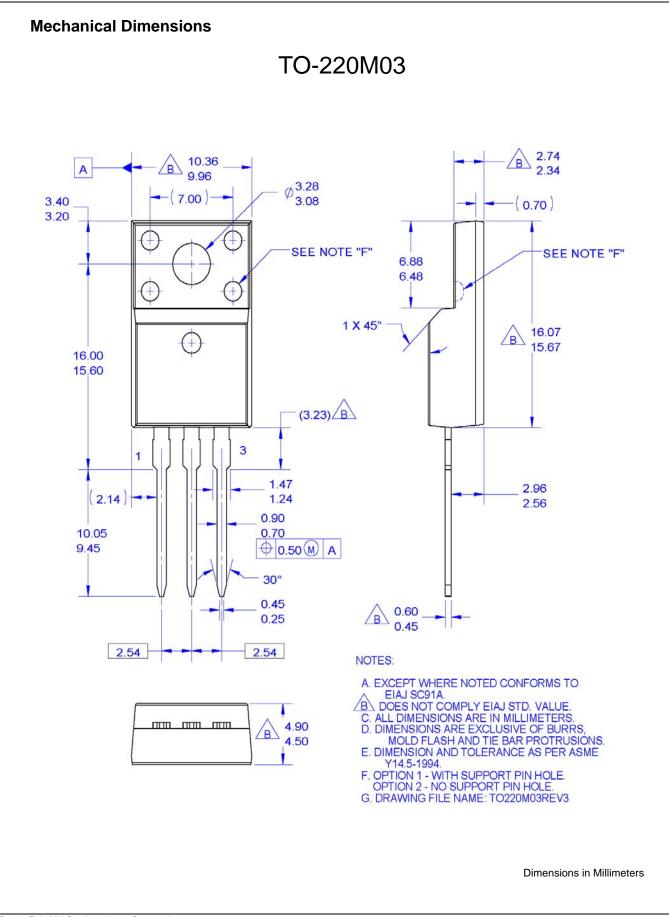




FDPF085N10A N-Channel PowerTrench<sup>®</sup> MOSFET









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