

2CL15KV

Φ 7.5±0.5

Φ 1.2±0.03

Technical Specifications

FEATURES

- Avalanche Breakdown Protection
- Low Forward Voltage Drop
- Typical IR less than 0.1 μA
- High Overload Surge Capacity



To order with quick disconnect terminals, use the partnum-TERM

Cathode Mark

Code Lot No.

32.5±2.5

ABSOLUTE MAXIMUM RATINGS

 $\begin{array}{lll} V_{RRM} & Repeating \ Peak \ Reverse \ Voltage \ (kV): & 15 \\ T_{JMAX} & Max. \ junction \ temp.(^{\circ}C): & 120 \\ T_{STG} & Storage \ temp.(^{\circ}C): & -40 \ to +120 \\ I_{O} & Avg. \ Forward \ Current \ (mA): & 550 \\ I_{FSM} & Forward \ Surge \ Current \ (A): & 44 \end{array}$

ELECTRICAL CHARACTERISTICS

 $\begin{array}{ll} I_{R1} & Normal \ temp. \ Reverse \ Current \ (\mu A): \\ I_{R2} & High \ temp. \ Reverse \ Current \ (\mu A): \\ V_F & Forward \ Voltage \ (V): \end{array} \qquad \begin{array}{ll} 5.0 \ max \\ 12 \ max \end{array}$

Cathode Mark \$\phi 7.5\pm 0.5\$ Code Lot No. \$\phi 1.2\pm 0.03\$ \$\phi 1.2\pm 0.03\$ 22 minim. 22 minim.

TEST CONDITIONS

High temp. Reverse Voltage @ 1000 hrs.: $V_{RM}=V_{RRM}$, f=50Hz, $T_{AMB}=100^{\circ}C$ Half sine

voltage with f=50Hz applied, T_{AMB}=100°C

High temp. storage @ 1000 Hrs.: $T_{AMB}=130\pm2^{\circ}C$

Soldering Resistance Heat Test: Solder trough temp.: 350±10°C,

Dip Time: $3.5s \pm 0.5s$

High pressure smoke test @ 10 hrs.: 120°C , $2 \times 10^{5}\text{pa}$

Insulation Resistance Test (1000M Ω): Between the center of the body and terminal (See Fig. 1) 1 min. between center of the body and terminal. (Fig.1)

Lead bend test: Force 10 N to the lead, bent it to pos. and neg. 90°

Lead pull test: Force 70 N of axial to the lead for 1 min.

Insulation resistance test condition: Measure between A and B by using a DC 500V Insulation resistance tester

Insulation strength test condition: Apply half sine wave voltage with 10kV wave height between A and B in insulation liquid



