### Clock OSC

## SG5032CAN

Product name SG5032CAN Product Number / Ordering code

6.144000 MHz TJGA X1G0044510057xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS

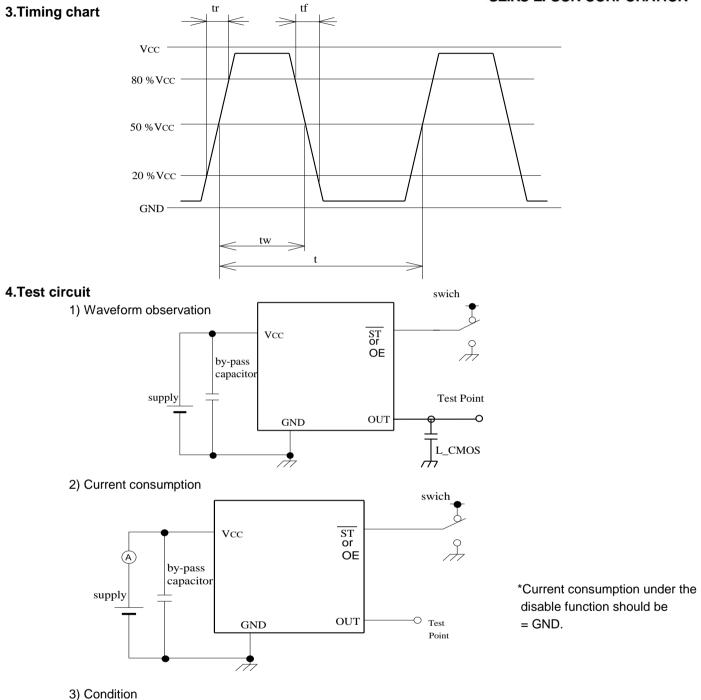
Pb free / Complies with EU RoHS directive

Reference weight Typ. 52 mg

1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	4	V	-
Storage temperature	T_stg	-40	-	+125	°C	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

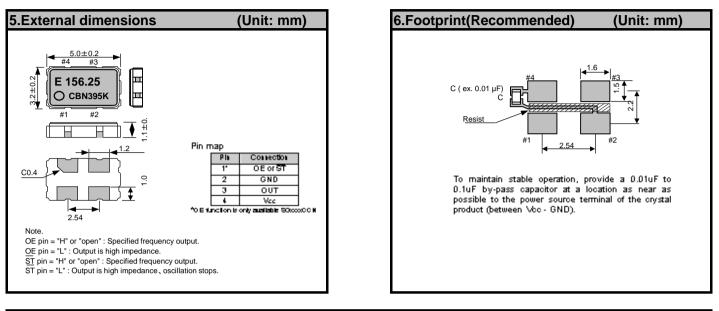
2.Specifications(characteri	stics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO		6.1440		MHz	
Supply voltage	Vcc	1.6	-	3.6	V	-
Operating temperature	T_use	-40	-	+85	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	lcc	-	-	3	mA	No load condition
Stand-by current	I_std	-	-	2.7	μA	ST = GND
Disable current	I_dis	-	-	-	mA	-
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V <sub>OH</sub>	Vcc-0.4	-	-		-
	V <sub>OL</sub>	-	-	0.4		-
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V <sub>IH</sub>	0.8Vcc	-	-		ST terminal
	V <sub>IL</sub>	-	-	0.2Vcc		ST terminal
Rise time	t <sub>r</sub>	-	-	4	ns	Vcc1.6V : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	4	ns	Vcc1.6V : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	3	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	-	0	-	ps	Deterministic Jitter Vcc=3.3V
	t <sub>RJ</sub>	-	2.4	-	ps	Random Jitter Vcc=3.3V
	t <sub>RMS</sub>	-	2.3	-	ps	δ(RMS of total distribution) Vcc=3.3V
	t <sub>p-p</sub>	-	20	-	ps	Peak to Peak Vcc=3.3V
	t <sub>acc</sub>	-	2.5	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles, Vcc=3.3V
Phase jitter	t <sub>PJ</sub>	-	-	-	ps	-
Phase noise	L(f)	-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
Frequency aging	f_age	-3	-	3	x10 <sup>-6</sup>	@+25°C first year
		-	-	-		

### SEIKO EPSON CORPORATION

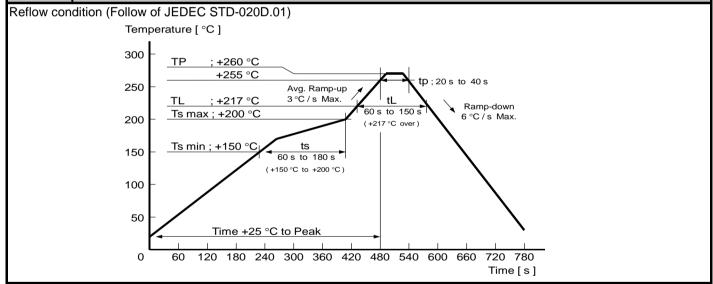


- (1) Oscilloscope
- Band width should be minimum 5 times higher (wider) than measurement frequency.
- · Probe earth should be placed closely from test point and lead length should be as short as possible
- \* Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L\_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01  $\mu$ F to 0.1  $\mu$ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- $\cdot$  Start up time (0 %VCC to 90 %VCC) of power source should be more than 150  $\mu s.$
- $\cdot$  Impedance of power supply should be as lowest as possible.

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#### 7.Reflow profile

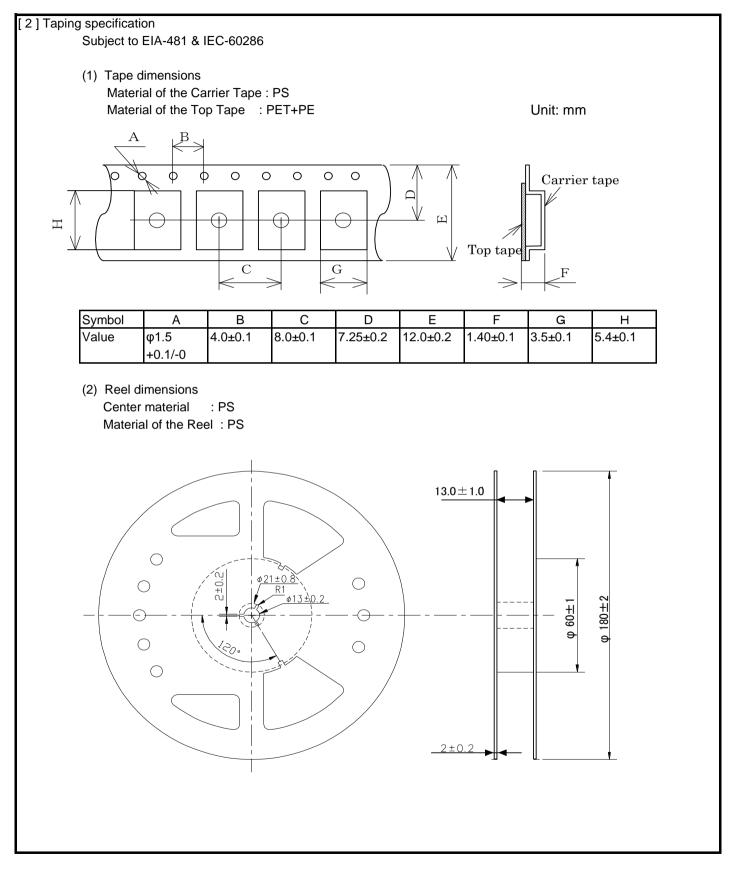


#### 8.Packing information

[1	]Product number	last 2	digits	code(xx)	description
	X1G004	45100	)57vv	<i>,</i>	

The recommended code is "00"

 100044	J100J7XX		
Code	Condition	Code	Condition
01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
11	Any Q'ty / Reel	00	1000pcs / Reel
12	250pcs / Reel		



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