

## **AUTOMOTIVE GRADE**

# **AUIRGDC0250**

### **Features**

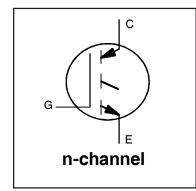
- Low V<sub>CE (on)</sub> Planar IGBT Technology
   Low Switching Losses
- Square RBSOA
- 100% of The Parts Tested for I<sub>LM</sub>①
- Positive  $V_{CE\,(on)}$  Temperature Coefficient. Lead-Free, RoHS Compliant
- Automotive Qualified '

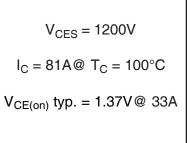


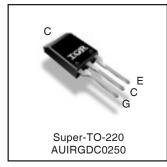
- Device optimized for soft switching applications
- $\bullet\,$  High Efficiency due to Low  $V_{\text{CE(on)}},$  low switching
- · Rugged transient performance for increased reliability
- · Excellent current sharing in parallel operation
- Low EMI



- PTC Heater
- Relay Replacement







G	С	E
Gate	Collector	Emitter

Base part number	Package Type	Standard	Pack	Complete Part Number	
Base part number	rackage Type	Form	Quantity	Complete Part Number	
AUIRGDC0250	Super-TO-220	Tube	50	AUIRGDC0250	

## Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolutemaximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (TA) is 25°C, unless otherwise specified.

	Parameter	Max.	Units	
V <sub>CES</sub>	Collector-to-Emitter Voltage	1200	V	
I <sub>C</sub> @ T <sub>C</sub> = 25°C	Continuous Collector Current	141 ⑤		
I <sub>C</sub> @ T <sub>C</sub> = 100°C Continuous Collector Current		81	A	
I <sub>см</sub>	Pulse Collector Current, V <sub>GE</sub> = 15V ⊘	99	^	
I <sub>LM</sub>	Clamped Inductive Load Current, V <sub>GE</sub> = 20V ①	99		
V <sub>GE</sub>	Continuous Gate-to-Emitter Voltage	±20	V	
	Transient Gate-to-Emitter Voltage	±30	v	
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Maximum Power Dissipation	543	w	
P <sub>D</sub> @ T <sub>C</sub> = 100°C	Maximum Power Dissipation	217	VV	
Operating Junction and		554-1450		
Storage Temperature Range  Soldering Temperature, for 10 sec. (1.6mm from case)		-55 to +150	°C	
		300		

## **Thermal Resistance**

	Parameter	Min.	Тур.	Max.	Units
R <sub>qJC</sub> (IGBT)	Thermal Resistance Junction-to-Case (IGBT) @			0.23	
R <sub>qCS</sub>	Thermal Resistance, Case-to-Sink (flat, greased surface)		0.50		°C/W
$R_{qJA}$	Thermal Resistance, Junction-to-Ambient		62		

<sup>\*</sup>Qualification standards can be found at <a href="http://www.irf.com/">http://www.irf.com/</a>



## Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter		Тур.	Max.	Units	Conditions
V <sub>(BR)CES</sub>	Collector-to-Emitter Breakdown Voltage	1200	_	_	V	$V_{GE} = 0V, I_{C} = 250\mu A$ ③
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	_	1.2	_	V/°C	V <sub>GE</sub> = 0V, I <sub>C</sub> = 1mA (25°C-150°C)③
		_	1.37	1.57	V	I <sub>C</sub> = 33A, V <sub>GE</sub> = 15V, T <sub>J</sub> = 25°C
V <sub>CE(on)</sub>	Collector-to-Emitter Saturation Voltage	_	1.45	_	v	I <sub>C</sub> = 33A, V <sub>GE</sub> = 15V, T <sub>J</sub> = 150°C
$V_{GE(th)}$	Gate Threshold Voltage	3.0	-	6.0	V	$V_{CE} = V_{GE}, I_{C} = 250 \mu A$
$\Delta V_{GE(th)}/\Delta TJ$	Threshold Voltage temp. coefficient	_	-15	_	mV/°C	$V_{CE} = V_{GE}, I_{C} = 250 \mu A (25^{\circ}C - 150^{\circ}C)$
gfe	Forward Transconductance	_	30	_	S	$V_{CE} = 50V, I_{C} = 33A, PW = 20\mu s$
		_	_	250		V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V
I <sub>CES</sub>	Collector-to-Emitter Leakage Current	_	-	2.0	μA	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 10V, T <sub>J</sub> = 25°C
		_	_	1000		V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V, T <sub>J</sub> = 150°C
I <sub>GES</sub>	Gate-to-Emitter Leakage Current	_	-	±100	nA	$V_{GE} = \pm 20V$

## Switching Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
$Q_g$	Total Gate Charge (turn-on)	_	151	227		$I_C = 33A$
$Q_{ge}$	Gate-to-Emitter Charge (turn-on)	_	26	39	nC	$V_{GE} = 15V$
$Q_{gc}$	Gate-to-Collector Charge (turn-on)		62	93		V <sub>CC</sub> = 600V
E <sub>off</sub>	Turn-Off Switching Loss	_	15	16	mJ	$I_C=33A,\ V_{CC}=600V,\ V_{GE}=15V$ $R_G=5\Omega,\ L=400\mu H,\ T_J=25^{\circ}C$ Energy losses include tail
t <sub>d(off)</sub>	Turn-Off delay time	_	485	616		$I_C = 33A$ , $V_{CC} = 600V$ , $V_{GE} = 15V$
t <sub>f</sub>	Fall time		1193	1371	ns	$R_G = 5\Omega$ , L = 400 $\mu$ H, $T_J = 25$ °C
E <sub>off</sub>	Turn-Off Switching Loss	_	29	_	mJ	$I_C$ = 33A, $V_{CC}$ = 600V, $V_{GE}$ = 15V $R_G$ = 5Ω, L = 400μH, $T_J$ = 150°C Energy losses include tail
t <sub>d(off)</sub>	Turn-Off delay time	_	689	_		$I_C = 33A$ , $V_{CC} = 600V$ , $V_{GE} = 15V$
t <sub>f</sub>	Fall time		2462	_	ns	$R_G = 5\Omega$ , L = 400 $\mu$ H, $T_J = 150$ °C
C <sub>ies</sub>	Input Capacitance		3804	_		$V_{GE} = 0V$
C <sub>oes</sub>	Output Capacitance		161	_	pF	$V_{CC} = 30V$
C <sub>res</sub>	Reverse Transfer Capacitance	_	31	_		f = 1.0Mhz
RBSOA	Reverse Bias Safe Operating Area	FULL SC	QUARE			$T_J$ = 150°C, $I_C$ = 99A $V_{CC}$ = 960V, $V_D$ ≤ 1200V $Rg$ = 5Ω, $V_{GE}$ = +20V to 0V

### Notes

- ①  $V_{CC}$  = 80% ( $V_{CES}$ ),  $V_{GE}$  = 20V, L = 400 $\mu H$ ,  $R_G$  = 50 $\Omega$ .
- $\ensuremath{{\mathbb Q}}$  Pulse width limited by max. junction temperature.
- $\ \, \mbox{\for guidelines}$  for measuring  $\mbox{\Volume{V}}_{\mbox{\formalfont BR)CES}}$  safely.
- 4 R<sub> $\theta$ </sub> is measured at T<sub>J</sub> of approximately 90°C.
- © Calculated continuous current based on maximum allowable junction temperature. Bond wire current limit is 78A. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.



# Qualification Information<sup>†</sup>

			Automotive (per AEC-Q101) ††		
Qualification	on Level	Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
Moisture Sensitivity Level		3L-Super-TO-220	N/A		
	Machine Model	Class M4 (+/- 800 V )			
	Wachine Woder	(per AEC-Q101-002)			
F0D	Liver on Dody Model	Class H3A (+/- 6000V)			
ESD	Human Body Model	(per AEC-Q101-001)			
	0	Class C5 (+/- 2000 V )			
	Charged Device Model		(per AEC-Q101-005)		
RoHS Compliant			Yes		

<sup>†</sup> Qualification standards can be found at International Rectifier's web site: <a href="http://www.irf.com/">http://www.irf.com/</a>

<sup>††</sup> Highest passing voltage.



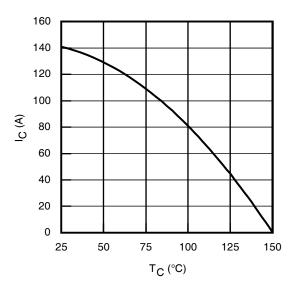


Fig. 1 - Maximum DC Collector Current vs.

Case Temperature

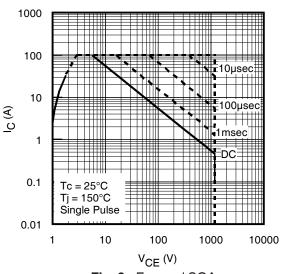


Fig. 3 - Forward SOA  $T_C = 25^{\circ}C$ ,  $T_J \le 150^{\circ}C$ ;  $V_{GE} = 15V$ 

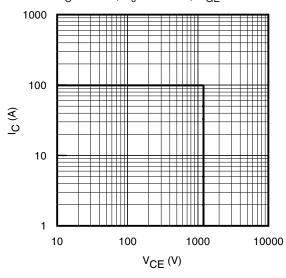
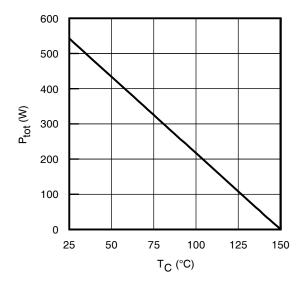
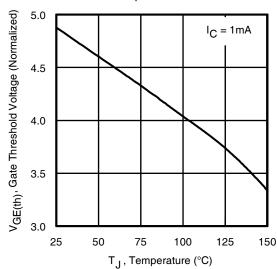


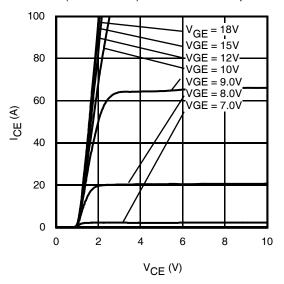
Fig. 5- Reverse Bias SOA  $T_J = 150$ °C;  $V_{GE} = 20$ V



**Fig. 2** - Power Dissipation vs. Case Temperature



**Fig. 4** - Typical Gate Threshold Voltage (Normalized) vs. Junction Temperature



**Fig. 6** - Typ. IGBT Output Characteristics  $T_J = -40$ °C; tp = 20 $\mu$ s

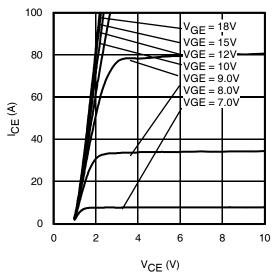


Fig. 7 - Typ. IGBT Output Characteristics  $T_{.1} = 25$ °C; tp = 20 $\mu$ s

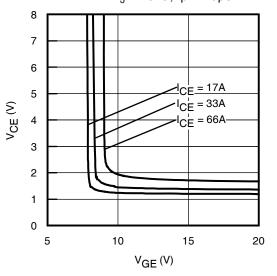


Fig. 9 - Typical  $V_{CE}$  vs.  $V_{GE}$  $T_{J} = -40$ °C

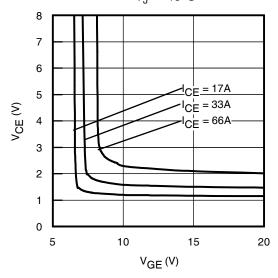
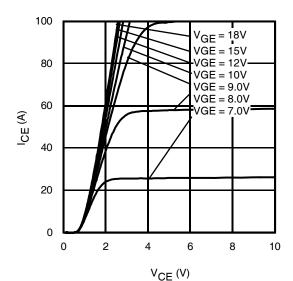


Fig. 11 - Typical  $V_{CE}$  vs.  $V_{GE}$   $T_J = 150^{\circ}C$ 



**Fig. 8** - Typ. IGBT Output Characteristics  $T_J = 150$ °C; tp = 20 $\mu$ s

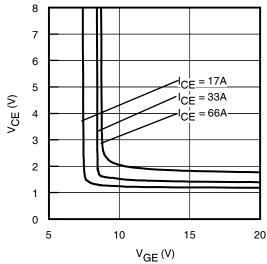


Fig. 10 - Typical  $V_{CE}$  vs.  $V_{GE}$  $T_J = 25^{\circ}C$ 

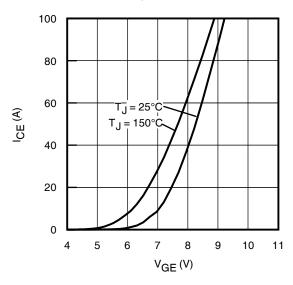


Fig. 12- Typ. Transfer Characteristics  $V_{CF} = 50V$ ; tp =  $20\mu s$ 

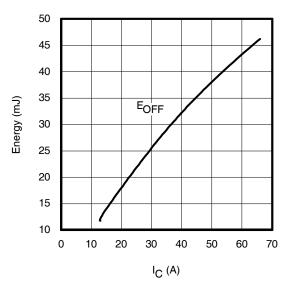
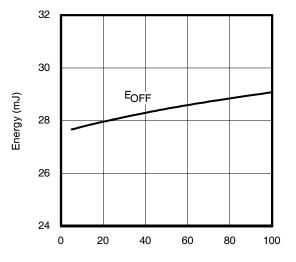


Fig. 13 - Typ. Energy Loss vs. I<sub>C</sub>  $T_J = 150$ °C;  $L = 400\mu H$ ;  $V_{CE} = 600V$ ,  $R_G = 5\Omega$ ;  $V_{GE} = 15V$ 



 $\operatorname{Rg}\left(\Omega\right)$ Fig. 15 - Typ. Energy Loss vs. R<sub>G</sub>  $T_J = 150^{\circ}C; L = 400 \mu H; V_{CE} = 600 V, I_{CE} = 33 A; V_{GE} = 15 V$ 

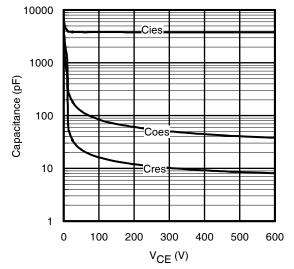


Fig. 17 - Typ. Capacitance vs. V<sub>CE</sub>  $V_{GE}$ = 0V; f = 1MHz

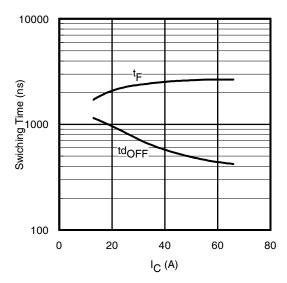


Fig. 14 - Typ. Switching Time vs. I<sub>C</sub>  $T_J = 150$ °C;  $L = 400\mu H$ ;  $V_{CE} = 600V$ ,  $R_G = 5\Omega$ ;  $V_{GE} = 15V$ 

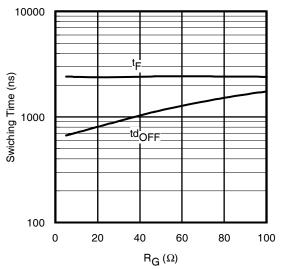


Fig. 16- Typ. Switching Time vs. R<sub>G</sub>  $T_J = 150^{\circ}C; L = 400 \mu H; V_{CE} = 600 V, I_{CE} = 33 A; V_{GE} = 15 V$ 

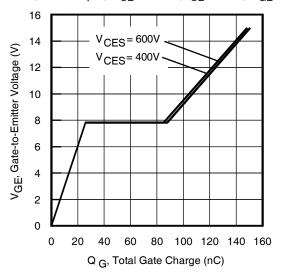


Fig. 18 - Typical Gate Charge vs.  $V_{\text{GE}}$  $I_{CE} = 33A; L = 2.0mH$ 

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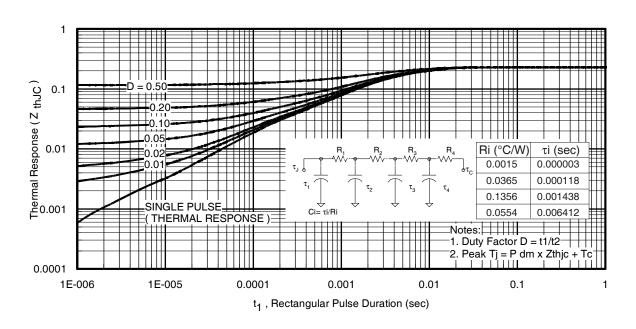


Fig 19. Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)



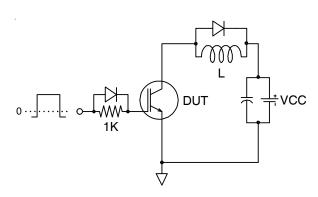


Fig.C.T.1 - Gate Charge Circuit (turn-off)

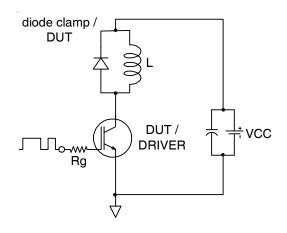


Fig.C.T.3 - Switching Loss Circuit

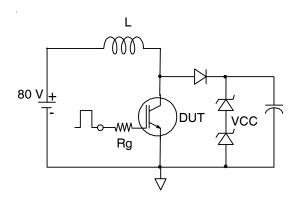


Fig.C.T.2 - RBSOA Circuit

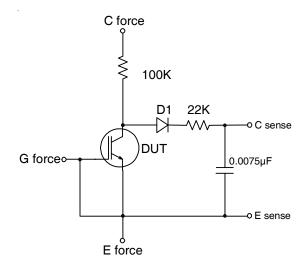


Fig.C.T.4 - BVCES Filter Circuit

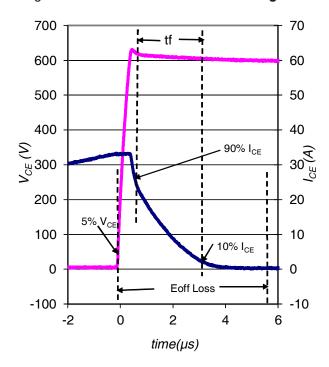
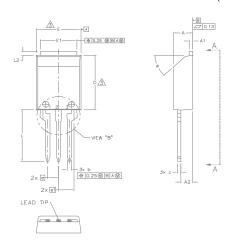


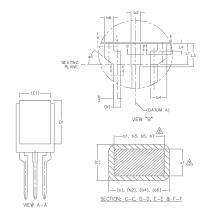
Fig. WF1 - Typ. Turn-off Loss Waveform @ T<sub>J</sub> = 150°C using Fig. CT.3



## Super-TO-220 Package Outline

Dimensions are shown in millimeters (inches)





#### NOTES:

- 1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
- 2. DIMENSIONS 61, 63, 65 & c1 APPLY TO BASE METAL ONLY.
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE, THESE DIMENSIONS ARE MEASURED AT THE OUTER EXTREMES OF THE PLASTIC BODY.
- 4.- ALL DIMENSIONS SHOWN IN MILLIMETERS.
- 5.- CONTROLLING DIMENSION: MILLIMETER.
- 6.- OUTLINE CONFORMS TO JEDEC OUTLINE TO-273AA.

S		DIMEN	SIONS		N.
M			1		N O T
B 0	MILLIM	ETERS	INC	HES	T
L	MIN.	MAX.	MIN.	MAX.	E S
А	4.34	4.74	.171	.187	
A1	0.50	1.00	.020	.039	
A2	2.50	3.00	,098	.118	
b	0.90	1.30	.035	.051	
ь1	0.80	1.10	.031	.043	2
b2	1.25	1.65	.049	.065	
b3	1.10	1.55	.043	.061	2
b4	2.35	2.55	.093	.100	
b5	2.30	2.50	.091	.098	2
b6	1.25	1.65	.049	.065	
ь7	1.10	1.55	.043	.061	2
С	0.70	1.00	.028	.039	
c1	0.60	0.90	.024	.035	2
D	14.00	15.00	.0551	.591	3
D1	12.50	13.50	.492	.531	
E	10.00	11.00	.394	.433	3
E1	8.00	9.00	.315	.354	
е	2.55	BSC	.100	BSC	
e1	3.66	BSC	.144	BSC	
L	13.00	14.50	.512	.571	
L1	3.00	3.50	.118	.138	
L2	0.50	1.50	.020	.059	
L3	3.50	4.00	.138	.157	
L4	_	1.50	_	.059	
ø	42.5°	47.5°	42.5°	47.5°	
ø1	_	42.5*	_	42.5°	

### LEAD ASSIGNMENTS

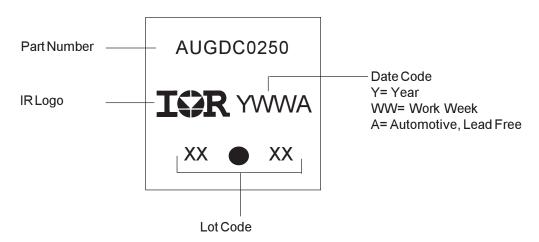
### **MOSFET**

- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE
- 4.- DRAIN

## <u>IGBT</u>

- 1.- GATE
- 2.- COLLECTOR
- 3.- EMITTER 4.- COLLECTOR

# Super-TO-220 Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/



## **IMPORTANT NOTICE**

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http://www.irf.com/technical-info/

## **WORLD HEADQUARTERS:**

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Tel: (310) 252-7105



## **Revision History**

Date	Comments
	Updated datasheet with IR corporate template.
9/2/2014	Removed Ic Nominal current on page 1.
9/2/2014	Updated package outline on page 9.
	• Updated typo on switch time test condition from "25C" to "150C" on page 2.
	Updated Bvdss test condition from "100uA" to "250uA" on page 2.
12/1/2014	Updated Vgeth test condition from "1mA" to "250uA" on page 2.
	• Updated Vgeth temp coefficient test condition from "1mA" to "250uA" and spec from "-12mV/C" to "-15mV/C"on page 2.