# UNISONIC TECHNOLOGIES CO., LTD

### 13003DF

#### **Preliminary**

#### NPN SILICON TRANSISTOR

## NPN SILICON BIPOLAR TRANSISTORS FOR LOW FREQUENCY AMPLIFICATION

#### DESCRIPTION

The UTC 13003DF is a silicon NPN power switching transistor; it uses UTC's advanced technology to provide customers high collector-base breakdown voltage, low reverse leakage current and high reliability, etc.

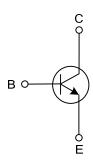
The UTC 13003DF is suitable for electronic ballast power switch circuit and the compact electronic energy-saving light.

# TO-92 TO-126

#### **FEATURES**

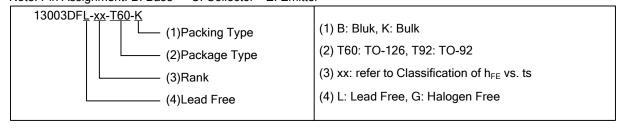
- \* High collector-base breakdown voltage
- \* Low reverse leakage current
- \* High reliability

#### **EQUIVALENT CIRCUIT**



#### ORDERING INFORMATION

Ordering	Daekasa	Pin Assignment			Dookina		
Lead Free	Halogen Free	Package	1	2	3	Packing	
13003DFL-xx-T60-K	13003DFG-xx-T60-K	TO-126	В	O	Е	Bulk	
13003DFL-xx-T92-B	13003DFG-xx-T92-B	TO-92	Е	O	В	Tape Box	
13003DFL-xx-T92-K	13003DFG-xx-T92-K	TO-92	Е	С	В	Bulk	



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#### ■ MARKING INFORMATION

PACKAGE	MARKING		
TO-126	UTC DDDD Data Code  13003DFD L: Lead Free  1 P: Halogen Free		
TO-92	UTC 13003DF  L: Lead Free P: Halogen Free Data Code		

#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		$V_{CBO}$	600	V
Collector-Emitter Voltage		$V_{CEO}$	400	V
Emitter-Base Voltage		$V_{EBO}$	9	V
Continuous Collector Current		I <sub>C</sub>	1.5	Α
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	1.25	W
	T <sub>C</sub> =25°C		50	W
Junction Temperature		TJ	150	°C
Storage Temperature Range		T <sub>STG</sub>	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> =25°C, unless otherwise noted)

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	$BV_CBO$	I <sub>C</sub> =0.1mA	600			V
Collector-Emitter Breakdown Voltage	$BV_CEO$	I <sub>C</sub> =1mA	400			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =0.1mA	9			V
Collector Cut-Off Current	I <sub>CBO</sub>	$V_{CB}$ =600V, $I_{E}$ =0			0.1	mA
Collector-Emitter Cut-Off Current	I <sub>CEO</sub>	$V_{CE}$ =400V, $I_{B}$ =0			0.1	mA
Emitter-Base Cut-Off Current	I <sub>EBO</sub>	$V_{EB}=9V$ , $I_{C}=0$			0.1	mA
DC Current Gain (Note 1)	$h_{FE}$	$V_{CE}$ =5V, $I_{C}$ =0.2A	15		30	
	h <sub>FE1</sub> / h <sub>FE2</sub>	h <sub>FE1</sub> : V <sub>CE</sub> =5V, I <sub>C</sub> =5mA	0.75	0.9		
Low current and high current h <sub>FE2</sub> h <sub>FE1</sub> ratio		h <sub>FE2</sub> : V <sub>CE</sub> =5V, I <sub>C</sub> =0.2A				
Collector-Emitter Saturation Voltage (Note)	$V_{CE(SAT)}$	I <sub>C</sub> =1A, I <sub>B</sub> =0.25A		0.3	0.9	V
Base-Emitter Saturation Voltage (Note)	$V_{BE(SAT)}$	I <sub>C</sub> =1A, I <sub>B</sub> =0.25A		0.9	1.2	V
Storage Time	t <sub>S</sub>		3		5	μs
Rise Time	$t_R$	UI9600, I <sub>C</sub> =0.1A			1	μs
Fall Time	$t_{F}$				1	μs
Transition Frequency	$f_T$	I <sub>C</sub> =0.1A, V <sub>CE</sub> =10V, f=1MHz	5			MHz

Note: Pulse test, pulse width tp≤300µs, Duty cycle≤2%

#### ■ CLASSIFICATION OF h<sub>FE</sub>

RANK	Α	В	С	
RANGE	15 ~ 20	20 ~ 25	25 ~ 30	

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