Nch 20V 2A Middle Power MOSFET

| V _{DSS} | 20V |
|----------------------------|-------|
| R _{DS(on)} (Max.) | 105mΩ |
| I _D | ±2A |
| P_D | 0.8W |

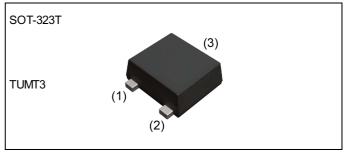
Features

- 1) Low on resistance.
- 2) 1.5V drive
- 3) Built-in G-S Protection Diode.
- 4) Small Surface Mount Package (TUMT3).
- 5) Pb-free lead plating; RoHS compliant

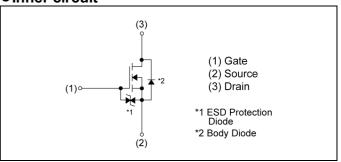
Application

Switching

Outline



●Inner circuit



Packaging specifications

| | Packing | Embossed Tape |
|------|---------------------------|------------------|
| | Reel size (mm) | 180 |
| Туре | Tape width (mm) | 8 |
| | Basic ordering unit (pcs) | 3000 |
| | Taping code | TL |
| | Marking | XK |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| Parameter | Symbol | Value | Unit |
|--|--------------------|-------------|------|
| Drain - Source voltage | V _{DSS} | 20 | V |
| Continuous drain current | I _D | ±2 | Α |
| Pulsed drain current | I _{DP} *1 | ±6 | Α |
| Gate - Source voltage | V_{GSS} | ±10 | V |
| Dawer discinction | P _D *2 | 0.8 | W |
| Power dissipation | P _D *3 | 0.75 | W |
| Junction temperature | T _j | 150 | °C |
| Operating junction and storage temperature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Doramatar | Symbol | Values | | | Linit |
|--|----------------------|--------|------|------|-------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal registeres innetion, ambient | R _{thJA} *2 | - | - | 156 | °C/W |
| Thermal resistance, junction - ambient | R _{thJA} *3 | - | - | 167 | °C/W |

● Electrical characteristics (T_a = 25°C)

| Parameter | Symbol | Conditions | | Values | 'alues | | |
|--|---|---|------|--------|--------|-------|--|
| Parameter | Cymbol | | Min. | Тур. | Max. | Unit | |
| Drain - Source breakdown voltage | V _{(BR)DSS} | $V_{GS} = 0V$, $I_D = 1mA$ | 20 | - | - | V | |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | 29 | - | mV/°C | |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 20V, V _{GS} = 0V | - | - | 1 | μA | |
| Gate - Source leakage current | I _{GSS} | $V_{GS} = \pm 10V, V_{DS} = 0V$ | - | - | ±10 | μΑ | |
| Gate threshold voltage | $V_{GS(th)}$ | V _{DS} = 10V, I _D = 1mA | 0.3 | - | 1.0 | V | |
| Gate threshold voltage temperature coefficient | $\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$ | I _D = 1mA referenced to 25°C | - | -1.6 | - | mV/°C | |
| | | V _{GS} = 4.5V, I _D = 2A | - | 75 | 105 | | |
| Static drain - source | b *4 | V _{GS} = 2.5V, I _D = 2A | - | 95 | 135 | 0 | |
| on - state resistance | R _{DS(on)} *4 | V _{GS} = 1.8V, I _D = 1A | - | 130 | 185 | mΩ | |
| | | V _{GS} = 1.5V, I _D = 0.4A | - | 170 | 240 | | |
| Gate resistance | R_G | | | 24 | - | Ω | |
| Forward Transfer Admittance | Y _{fs} *4 | V _{DS} = 10V, I _D = 2A | 1.8 | - | - | S | |

^{*1} Pw≦10µs , Duty cycle≦1%

Downloaded from: http://www.datasheetcatalog.com/

^{*2} Mounted on a ceramic board (30x30x0.8mm)

^{*3} Mounted on a FR4 (25x25x0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

| Darameter | Symbol | Conditions | Values | | | Unit |
|------------------------------|------------------------|------------------------------------|--------|------|------|------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 180 | - | |
| Output capacitance | C _{oss} | V _{DS} = 10V | - | 45 | - | pF |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 25 | - | |
| Turn - on delay time | t _{d(on)} *4 | $V_{DD} \simeq 10V, V_{GS} = 4.5V$ | - | 6 | - | |
| Rise time | t _r *4 | I _D = 1A | - | 17 | - | no |
| Turn - off delay time | t _{d(off)} *4 | $R_L \simeq 10\Omega$ | - | 30 | - | ns |
| Fall time | t _f *4 | $R_G = 10\Omega$ | - | 30 | - | |

● Gate charge characteristics (T_a = 25°C)

| (a) | | | | | | | |
|----------------------|--------------------|--|--------|------|------|-------|--|
| Darameter | Cumbal | Conditions | Values | | | 1.1:4 | |
| Parameter | Symbol Conditions | | Min. | Тур. | Max. | Unit | |
| Total gate charge | Qg*4 | V _{DD} ≃ 10V, | - | 2.0 | - | | |
| Gate - Source charge | Q _{gs} *4 | I _D = 2A, V _{GS} = 4.5V | - | 0.6 | - | nC | |
| Gate - Drain charge | Q _{gd} *4 | $V_{GS} = 4.5V$ | - | 0.4 | - | | |

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Darameter | Symbol | Conditions | Values | | | Unit |
|----------------------------|--------------------|---|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic |
| Continuous forward current | I _S | T - 25°0 | - | - | 0.6 | Α |
| Pulse forward current | I _{SP} *1 | T _a = 25°C | - | - | 6 | Α |
| Forward voltage | V _{SD} *4 | V _{GS} = 0V, I _S = 2A | - | - | 1.2 | V |

Drain Current : I_D [A]

Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

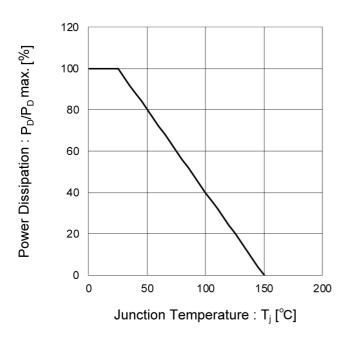
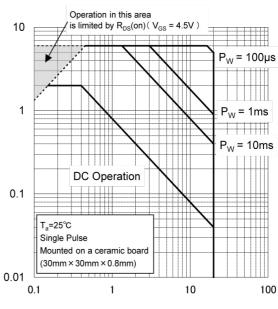


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage: V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

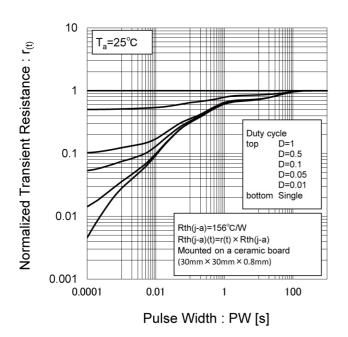
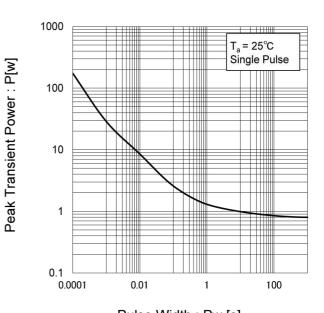


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw [s]

• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

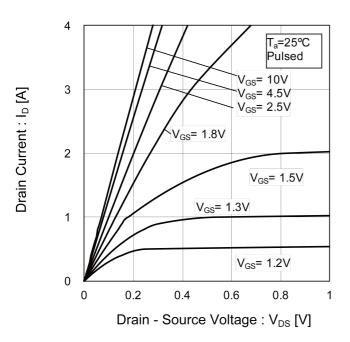


Fig.6 Typical Output Characteristics(II)

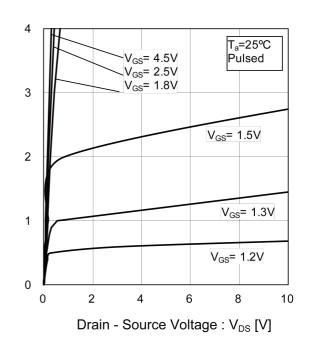


Fig.7 Breakdown Voltage vs.
Junction Temperature

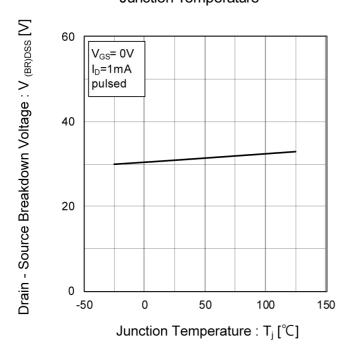
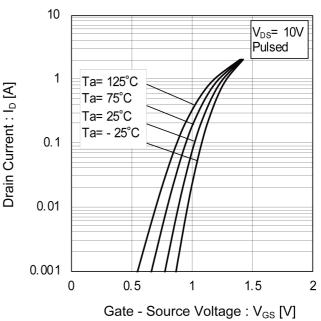


Fig.8 Typical Transfer Characteristics



Drain Current : I_D [A]

• Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

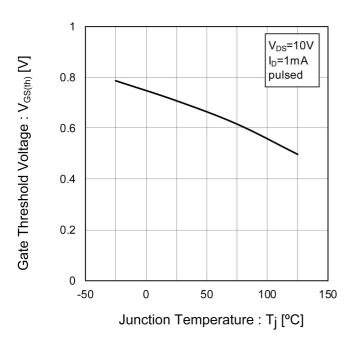


Fig.10 Forward Transfer Admittance vs.
Drain Current

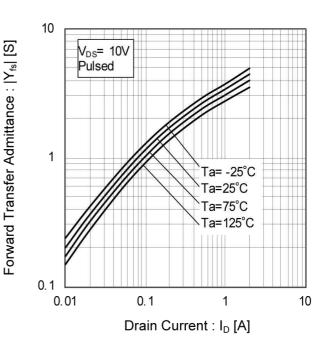


Fig.11 Drain Current Derating Curve

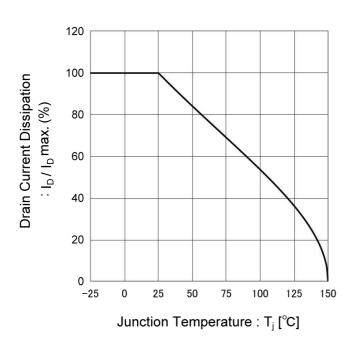
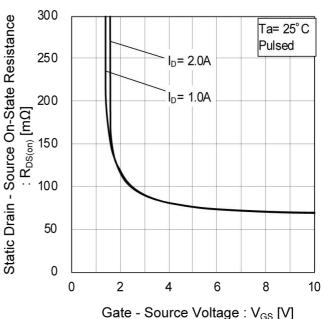


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

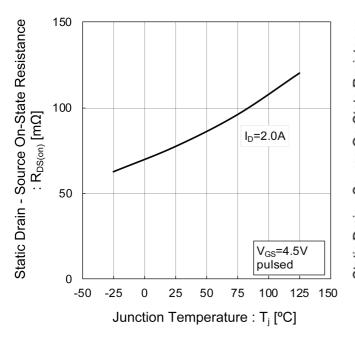


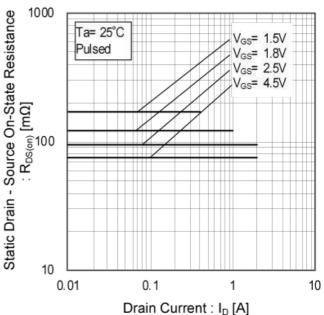
RUF020N02 Datasheet

• Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)





ROHM

RUF020N02 Datasheet

• Electrical characteristic curves

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

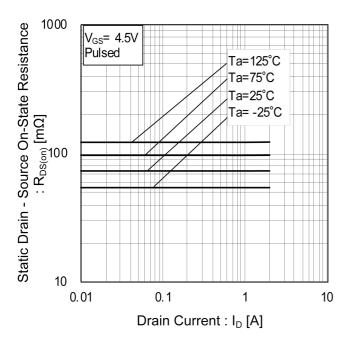


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current (III)

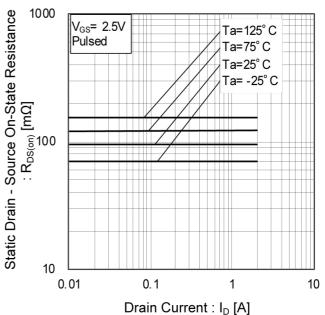


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

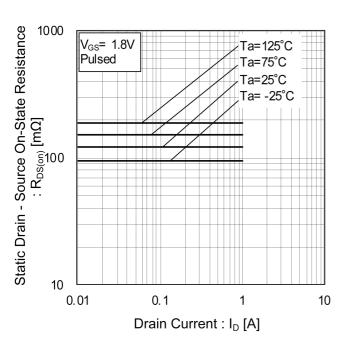
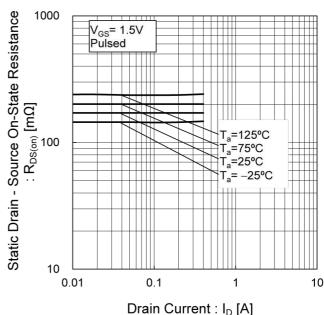


Fig.18 Static Drain - Source On - State Resistance vs. Drain Current (V)



• Electrical characteristic curves

Fig.19 Typical Capacitance vs.

Drain - Source Voltage

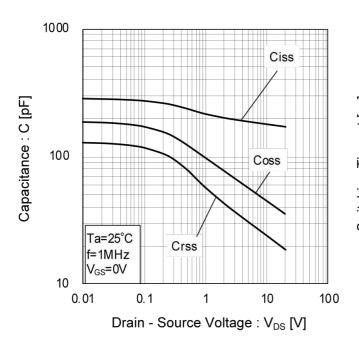


Fig.20 Switching Characteristics

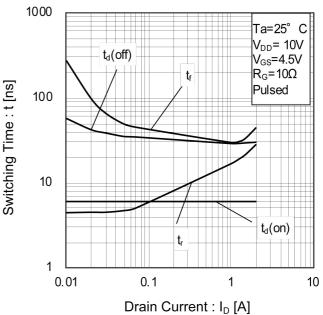


Fig.21 Dynamic Input Characteristics

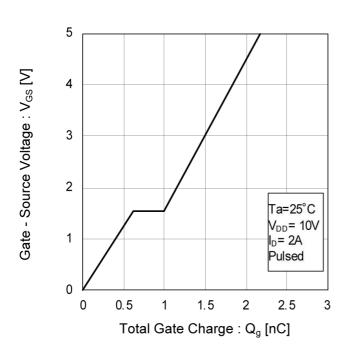
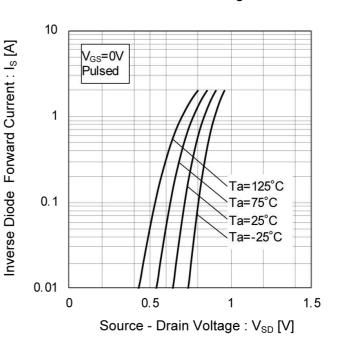


Fig.22 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

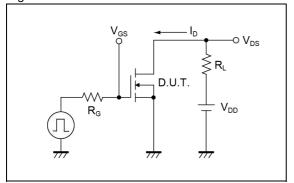


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

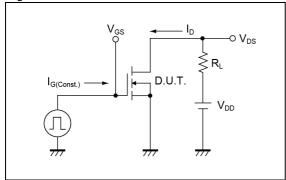


Fig. 1-2 SWITCHING WAVEFORMS

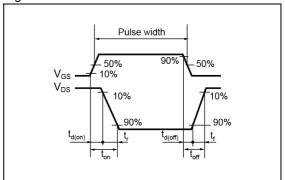
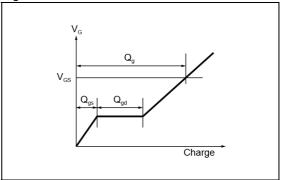


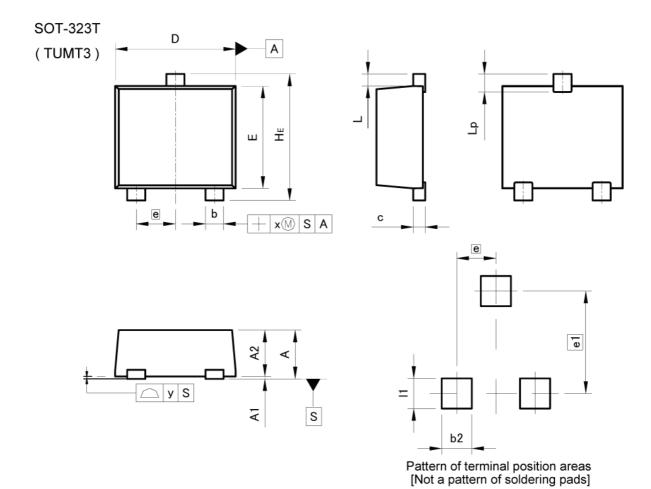
Fig. 2-2 GATE CHARGE WAVEFORM



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



| DIM | MILIM | ETERS | INC | HES |
|-------|-------|-------|-------|-------|
| DIM [| MIN | MAX | MIN | MAX |
| Α | =: | 0.85 | - | 0.033 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| A2 | 0.72 | 0.82 | 0.028 | 0.032 |
| b | 0.25 | 0.40 | 0.010 | 0.016 |
| С | 0.12 | 0.22 | 0.005 | 0.009 |
| D | 1.90 | 2.10 | 0.075 | 0.083 |
| E | 1.60 | 1.80 | 0.063 | 0.071 |
| е | 0. | 65 | 0.0 | 26 |
| HE | 2.00 | 2.20 | 0.079 | 0.087 |
| L | 0.3 | 20 | 0.0 | 80 |
| Lp | = | 0.40 | 12 | 0.016 |
| х | = | 0.10 | - | 0.004 |
| у | (EE) | 0.10 | = | 0.004 |

| DIM | MILIME | TERS | INC | HES |
|-----|----------|------|-----|-------|
| DIM | MIN | MAX | MIN | MAX |
| b2 | 보인 | 0.50 | 122 | 0.020 |
| e1 | 1.7 | 70 | 0.0 | 67 |
| 11 | <u> </u> | 0.50 | 100 | 0.020 |

Dimension in mm/inches



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| JAPAN | USA | EU | CHINA |
|---------|-----------|------------|------------|
| CLASSⅢ | CI ACCIII | CLASS II b | CI 700 III |
| CLASSIV | CLASSII | CLASSⅢ | CLASSⅢ |

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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