## AKM

### = Preliminary =

# AK5574

120dB 768kHz 32-bit 4ch  $\Delta\Sigma$  ADC with 4Filters

#### 1. General Description

The AK5574 is a 32bit, 768kHz sampling, differential input A/D converter for digital audio systems. It integrates an 4-channel A/D converter, suitable for mixers and automotive amplifier units. The AK5574 achieves 120dB dynamic range, 110dB S/(N+D) and 123dB dynamic range in Mono mode while keeping low power consumption performance. Four types of digital filters are selectable according to the application. The AK5574 can be easily connected to a DSP by supporting TDM audio formats.

#### 2. Features

- Sampling Rate: 8kHz ~ 768kHz
- Full Differential Inputs
- 32Bit Digital Filter
  - Sharp Roll-Off Filter
  - Slow Roll-Off Filter
  - Short Delay Sharp Roll-Off Filter
  - Short Delay Slow Roll-Off Filter
- S/(N+D): 110dB
- DR, S/N: 120dB (Mono Mode: 123dB)
- Digital HPF
- Power Supply: 4.75~ 5.25V(Analog), 1.7~1.98V or 3.0 ~ 3.6V(Digital)
- Output Format: 24/32bit MSB justified, I<sup>2</sup>S or TDM
- Cascade TDM I/F: 16ch/48kHz, 8ch/96kHz, 4ch/192kHz
- DSD (64fs, 128fs, 256fs) Output Data
- Master & Slave Modes
- Overflow Flag
- 3-wire Serial and I<sup>2</sup>C µP I/F
- Power Consumption: 302.2mW (@AVDD=5.0V, TVDD=3.3V, fs=48kHz)
- Package: 48-pin QFN



#### 3. Block Diagram

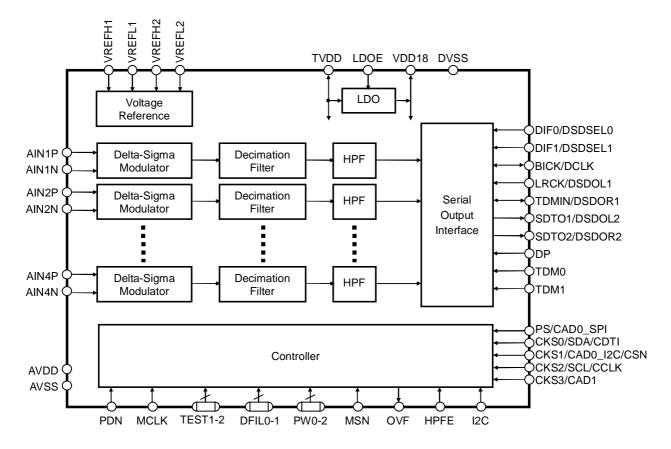


Figure 1. Block Diagram

#### 4. Absolute Maximum Ratings

(VSS=0V; Note 1)							
Parameter		Symbol	Min.	Max.	Unit		
Power	Analog (AVDD pin)	AVDD	-0.3	6.0	V		
Supplies	Digital (TVDD pin)	TVDD	-0.3	4.0	V		
	Digital (VDD18 pin) (Note 2)	VDD18	-0.3	2.5	V		
Input Current (Any Pin Except Supplies)		IIN	-	±10	mA		
Analog Input Voltage (AIN1~8P, AIN1-8N pins)		VINA	-0.3	AVDD+0.3	V		
Digital Input Voltage		VIND	-0.3	TVDD+0.3	V		
Ambient Temperature (Power applied)							
When the back tab is connected to VSS		Та	-40	105	°C		
When th	e back tab is open	Та	-40	85			
Storage Te	emperature	Tstg	-65	150	°C		

Note 1. All voltages with respect to ground.

Note 2. The 1.8V LDO is off (LDOE pin = "L") and an external power is supplied to the VDD18 pin.

WARNING: Operation at or beyond these limits may result in permanent damage to the device. Normal operation is not guaranteed at these extremes.

5. Recommended Operation Conditions									
(VSS=0V; N	Note 1)								
Parameter		Symbol	Min.	Тур.	Max.	Unit			
Power	Analog (AVDD pin)	AVDD	4.75	5.0	5.25	V			
Supplies	(LDOE pin= "L")(Note 3)IO Buffer (TVDD pin)(Note 4)Digital Core (VDD18 pin)(LDOE pin= "H")(Note 5)IO Buffer (TVDD pin)	TVDD VDD18 TVDD	1.7 1.7 3.0	1.8 1.8 3.3	1.98 1.98 3.6	V V V			
Voltage Reference (Note 7)	"H" voltage Reference (Note 6)	VREFH1-4	4.75	5.0	5.25	V			
	"L" voltage reference	VREFL1-4	-	AVSS	-	V			

Note 1. All voltages with respect to ground.

Note 3. TVDD must be powered up before VDD18 when the LDOE pin = "L". The power up sequence between AVDD and TVDD or AVDD and VDD18 is not critical.

Note 4. TVDD must not exceed VDD18±0.1V when LDOE pin= "L".

Note 5. When LDOE pin = "H", the internal LDO supplies 1.8V (typ). The power up sequences between AVDD and TVDD is not critical.

Note 6. VREFH1~4 pin must not exceed AVDD+0.1V.

Note 7. VREFL1-4 pins must be connected to AVSS. Analog Input Voltage is proportional to  $\{(VREFH) - (VREFL)\}$ . Vin (typ, @ 0dB) =  $\pm 2.8 \times \{(VREFH) - (VREFL)\} / 5$  [V].

\* AKM assumes no responsibility for the usage beyond the conditions in this data sheet.

#### 6. Analog Characteristics

(Ta=25°C; AVDD=5.0V; TVDD=3.3V, fs=48kHz, 96kHz; BICK=64fs; Signal Frequency=1kHz; 24bit Data; Measurement frequency=20Hz~20kHz at fs=48kHz, 40Hz~40kHz at fs=96kHz, 40Hz~40kHz at fs=192kHz, unless otherwise specified.)

Parameter			Min.	Тур.	Max.	Unit
Analog Input Char	acteristics:					
Resolution			-	-	32	Bits
Input Voltage (Note 8)			±2.7	±2.8	±2.9	Vpp
S/(N+D)	fs=48kHz BW=20kHz	-1dBFS -20dBFS	100	110 97		dB dB
		-60dBFS		57	-	dB
	fs=96kHz	-1dBFS		110	-	dB
	BW=40kHz	-20dBFS -60dBFS		90 50	-	dB dB
	fs=192kHz	-1dBFS	-	110	-	dB
	BW=40kHz	-20dBFS		90	-	dB
	DW = 40KHZ	-60dBFS		50	-	dB
Dynamic Range Stereo Mode		112	120 123	-	dB	
(-60dBFS with A-weighted) Mono Mode		-		-		
S/N		Stereo Mode	112	120	-	dB
(A-weighted) Mono Mode		3.0	123 3.5	- 4.1	kΩ	
Input Resistance			110	120	4.1	
Interchannel Isolation Interchannel Gain Mismatch			110	0	0.5	dB dB
Power Supply Rejection (Note 9)				60	-	dB
Power Supplies				00	_	uD
Power Supply Curre	ont					
Normal Operation (PDN pin = "H")						
AVDD				57.4	TBD	mA
TVDD (fs=48kHz)				8.45	TBD	mA
TVDD (fs=96kHz)				TBD	TBD	mA
TVDD $(fs=192kHz)$				TBD	TBD	mA
Power down mode (PDN $pin = "L")$ (Note 10)						
AVDD+TVDD				10	100	μΑ

Note 8. This value is (AINnP)–(AINnN) that the ADC output becomes full-scale (n=1~8). Input voltage is proportional to VREFH–VREFL.

 $Vin = 0.56 \times (VREFHm-VREFLm) [Vpp]. (m=1~4)$ 

Note 9. PSRR is applied to AVDD, TVDD with 1kHz, 20mVpp sine wave. The VREFH1~4 pins are held to the same voltage.

Note 10. All digital inputs are fixed to TVDD or TVSS.

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