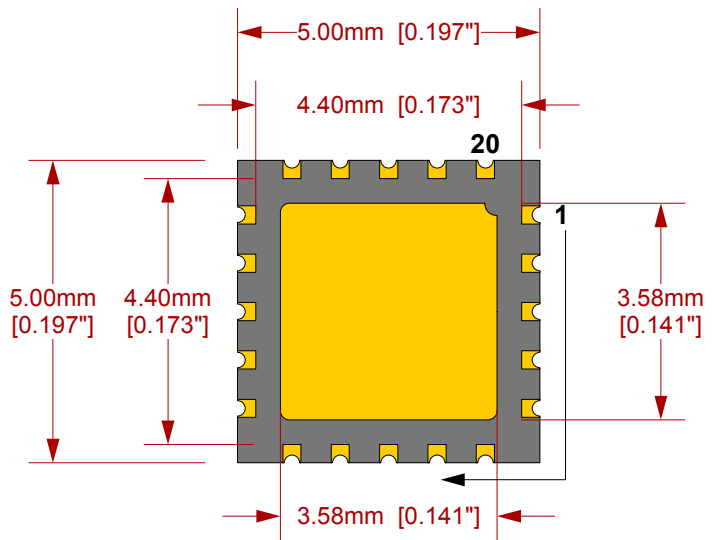
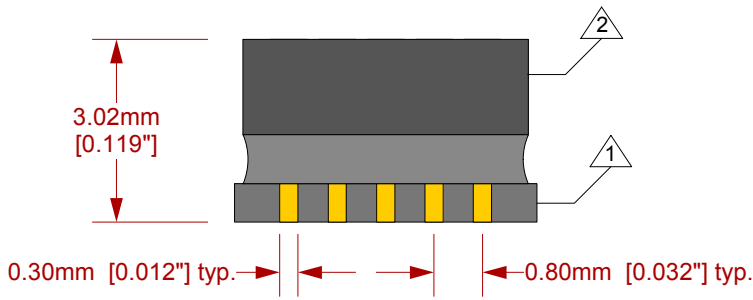


Top View



Bottom View



Front View

**RoHS COMPLIANT**


① Substrate: 0.635mm  $\pm$ 0.18mm [0.025"  $\pm$ 0.007"] per IPC 4101/21, Gold surface finish.

② Substrate: 1.59mm  $\pm$ 0.18mm [0.062"  $\pm$ 0.007"] FR4/G10 or equivalent high temp material. Non-clad.

③ Pins: shell material- Brass Alloy 360 1/2 hard; finish- 0.25 $\mu$ m [10 $\mu$ ] Au over 1.27 $\mu$ m [50 $\mu$ ] Ni (min.). Contact material- BeCu; finish 0.25 $\mu$ m [10 $\mu$ ] Au over 2.54 $\mu$ m [100 $\mu$ ] Ni (min. ).

**Description:** 20 position MLF surface mount foot  
20 position (+1 Gnd index pin). 1.0mm pitch UGA female receptacles (top) to 20 position, 0.8mm MLF pattern

**Tolerances:** diameters  $\pm$ 0.03mm [ $\pm$ 0.001"], PCB perimeters  $\pm$ 0.13mm [ $\pm$ 0.005"], PCB thicknesses  $\pm$ 0.18mm [ $\pm$ 0.007"], pitches (from true position)  $\pm$ 0.08mm [ $\pm$ 0.003"], all other tolerances  $\pm$ 0.13mm [ $\pm$ 0.005"] unless stated otherwise. Materials and specifications are subject to change without notice.

 <p>© 2005 IRONWOOD ELECTRONICS, INC. 11351 Rupp Drive, Suite 400, Burnsville, MN 55337 Tele: (952) 229-8200 www.ironwoodelectronics.com</p>	<p><b>SF-QFN20E-L-02 Drawing</b></p>	<p>Status: Released</p>	<p>Scale: 8:1</p>	<p>Rev: A</p>
	<p>Drawing: H. Hansen</p>	<p>Date: 11/21/05</p>		
	<p>File: SF-QFN20E-L-02 Dwg</p>	<p>Modified:</p>		