

N-Channel Logic Level Enhancement Mode Power MOSFET

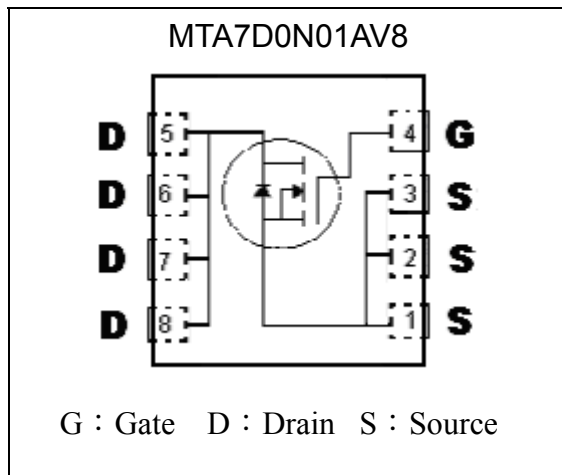
MTA7D0N01AV8

BV _{DSS}		16V
I _D @ T _C =25°C, V _{GS} =10V		28A
I _D @ T _A =25°C, V _{GS} =10V		12.5A
R _{DS(on)} (TYP)	V _{GS} =10V, I _D =11A	5.9mΩ
	V _{GS} =4.5V, I _D =8A	6.9mΩ
	V _{GS} =3V, I _D =5A	8.4mΩ

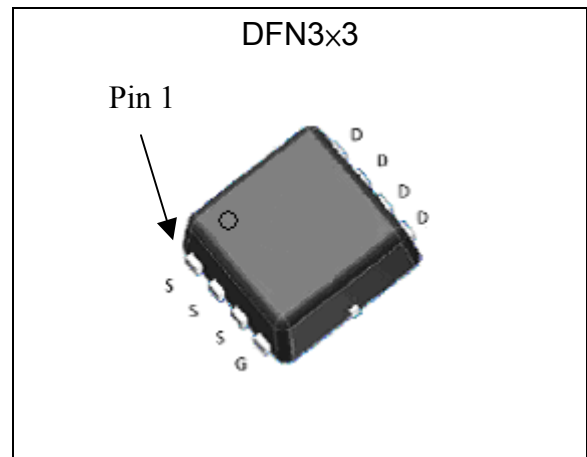
Features

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

Equivalent Circuit

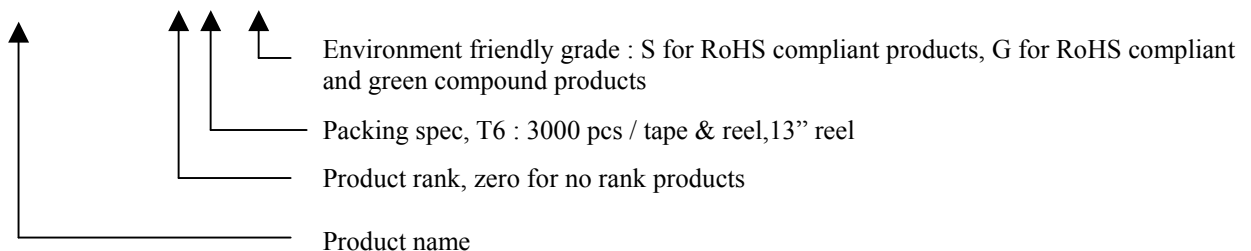


Outline



Ordering Information

Device	Package	Shipping
MTA7D0N01AV8-0-T6-G	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C, unless otherwise specified)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	16	V	
Gate-Source Voltage	V _{GS}	±10		
Continuous Drain Current @ V _{GS} =10V, T _C =25°C	I _D	28	A	
Continuous Drain Current @ V _{GS} =10V, T _C =100°C		17.7		
Continuous Drain Current @ V _{GS} =10V, T _A =25°C	I _{DSM}	12.5		
Continuous Drain Current @ V _{GS} =10V, T _A =70°C		10		
Pulsed Drain Current	I _{DM}	112 *1		
Avalanche Current	I _{AS}	21		
Avalanche Energy @ L=0.1mH, I _D =21A, R _G =25Ω	E _{AS}	22	mJ	
Total Power Dissipation	P _D	T _C =25°C	12.5	W
		T _C =100°C	5	
	P _D SM	T _A =25°C	2.5 *2	
		T _A =70°C	1.6 *2	
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	10	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	50 *2	

Note : 1. Pulse width limited by maximum junction temperature.
 2. Surface mounted on a 1 in² pad of 2oz copper, t≤10s. In practice R_{th,j-a} will be determined by customer's PCB characteristics. 125°C/W when mounted on a minimum pad of 2 oz. copper.

Characteristics (T_C=25°C, unless otherwise specified)

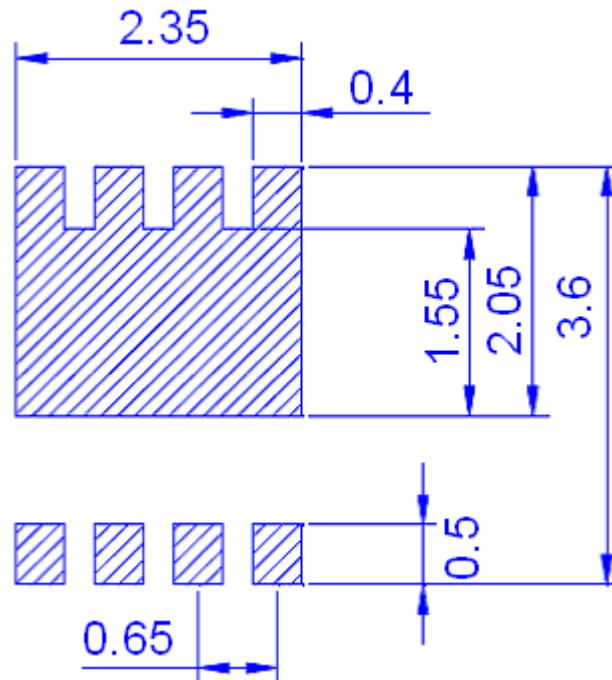
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	16	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	0.5	0.73	1.0		V _{DS} = V _{GS} , I _D =250μA
G _{FS} *1	-	18	-	S	V _{DS} =5V, I _D =11A
I _{GSS}	-	-	±100	nA	V _{GS} =±8V
I _{DSS}	-	-	1	μA	V _{DS} =16V, V _{GS} =0V
	-	-	5		V _{DS} =16V, V _{GS} =0V, T _j =55°C
R _{DS(ON)} *1	-	5.9	8.5	mΩ	V _{GS} =10V, I _D =11A
	-	6.9	9.8		V _{GS} =4.5V, I _D =8A
	-	8.4	12		V _{GS} =3V, I _D =5A
Dynamic					
C _{iss}	-	853	-	pF	V _{DS} =10V, V _{GS} =0V, f=1MHz
C _{oss}	-	209	-		
C _{rss}	-	171	-		
Q _g *1,2	8.5	11.5	15	nC	V _{DS} =15V, V _{GS} =4.5V, I _D =20A
Q _{gs} *1,2	-	1.9	-		
Q _{gd} *1,2	-	3.9	-		

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
t _{d(ON)} *1, 2	-	9.4	14.1	ns	V _{DS} =12.5V, I _D =20A, V _{GS} =4.5V, R _{GS} =2Ω
t _r *1, 2	-	22.4	33.6		
t _{d(OFF)} *1, 2	-	36.6	54.9		
t _f *1, 2	-	18.4	27.6		
t _{d(ON)} *1, 2	-	5.4	8.1	ns	V _{DS} =15V, I _D =20A, V _{GS} =10V, R _{GS} =3Ω
t _r *1, 2	-	14.4	21.6		
t _{d(OFF)} *1, 2	-	39.2	58.8		
t _f *1, 2	-	9.4	14.1		
R _g	2.5	4.0	5	Ω	f=1MHz
Source-Drain Diode					
I _S *1	-	-	28	A	
V _{SD} *1	-	0.9	1.2	V	I _S =16A, V _{GS} =0V
t _{rr}	-	12.8	-	ns	I _F =20A, dI _F /dt=100A/μs
Q _{rr}	-	5.8	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
 *2.Independent of operating temperature

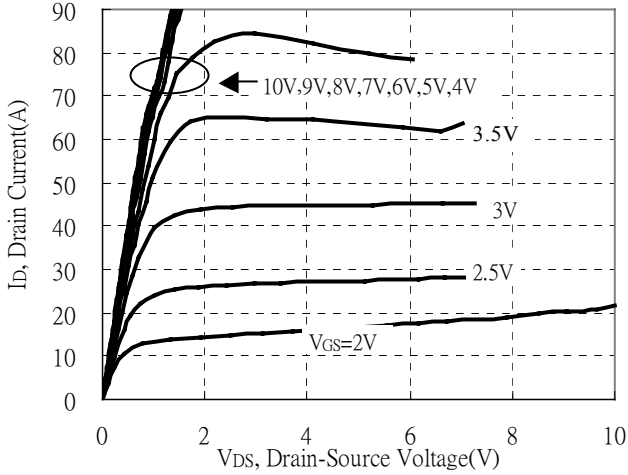
Recommended Soldering Footprint



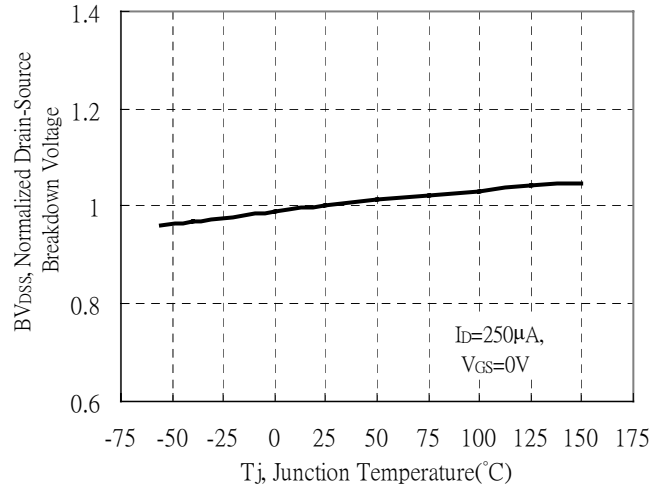
unit : mm

Typical Characteristics

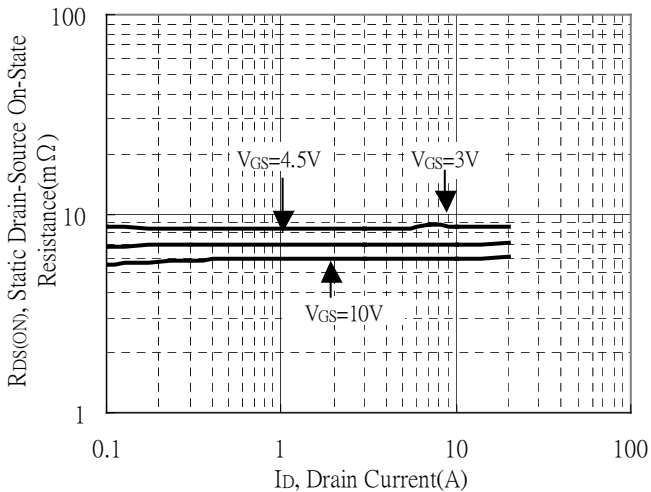
Typical Output Characteristics



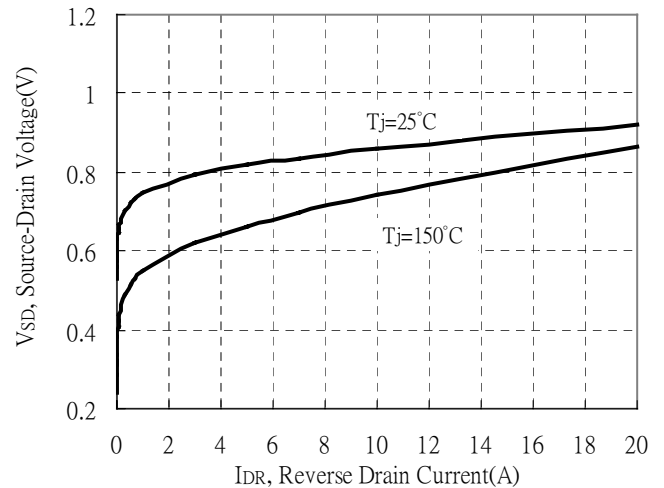
Brekdown Voltage vs Ambient Temperature



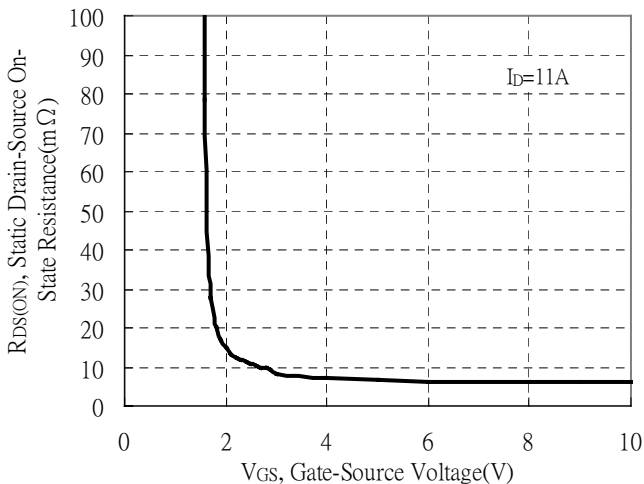
Static Drain-Source On-State resistance vs Drain Current



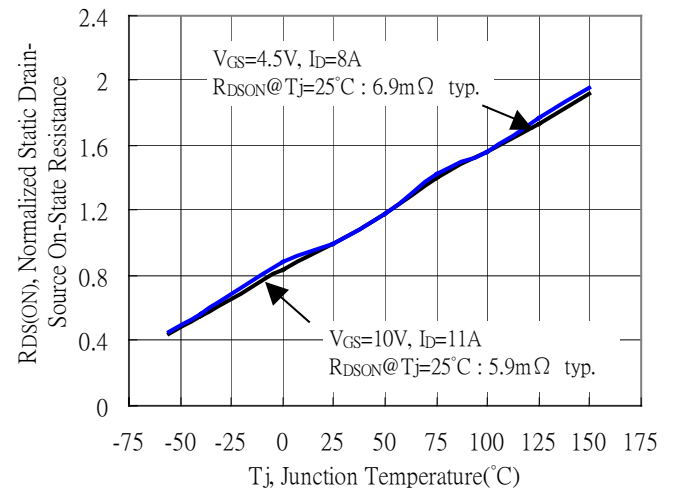
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

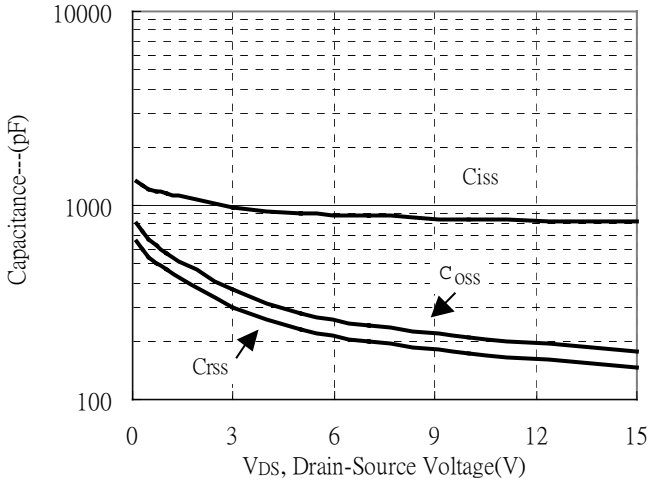


Drain-Source On-State Resistance vs Junction Temperature

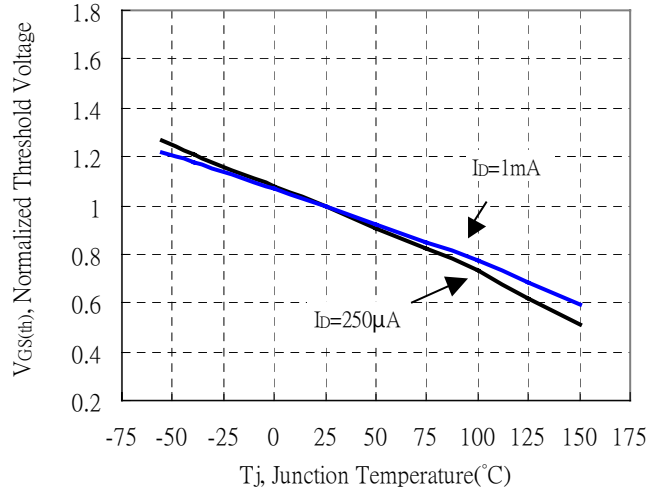


Typical Characteristics(Cont.)

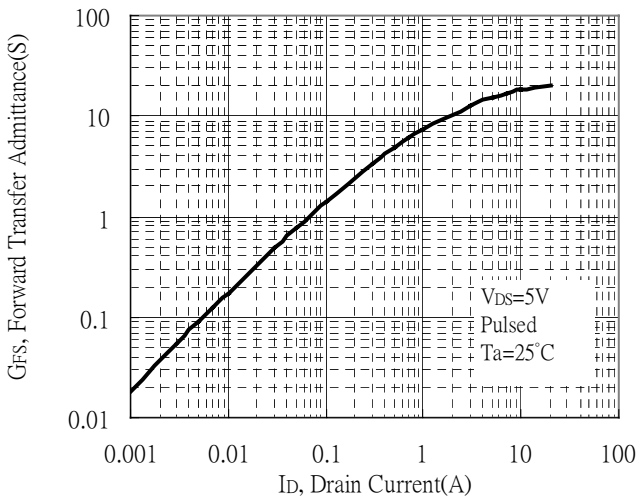
Capacitance vs Drain-to-Source Voltage



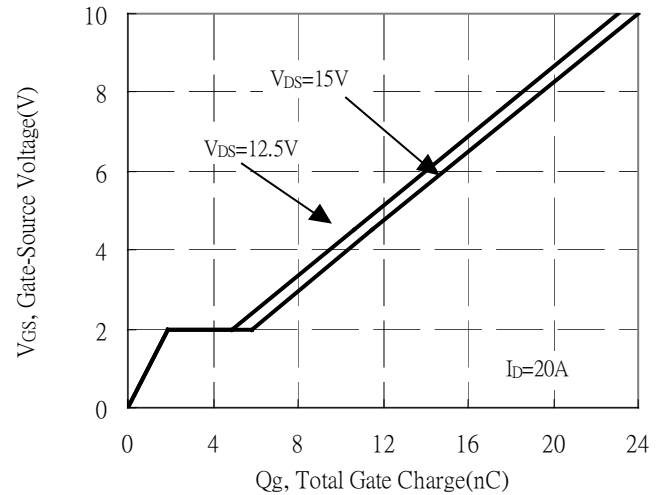
Threshold Voltage vs Junction Temperature



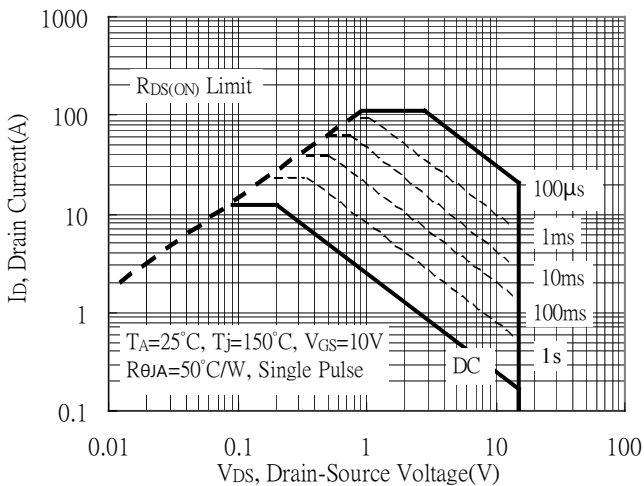
Forward Transfer Admittance vs Drain Current



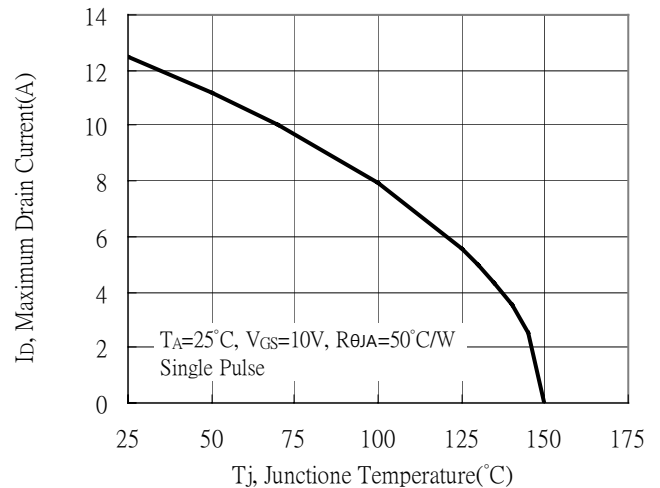
Gate Charge Characteristics



Maximum Safe Operating Area



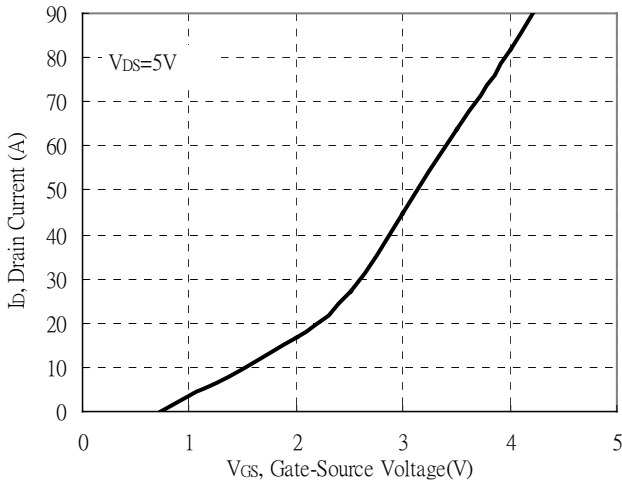
Maximum Drain Current vs Junction Temperature



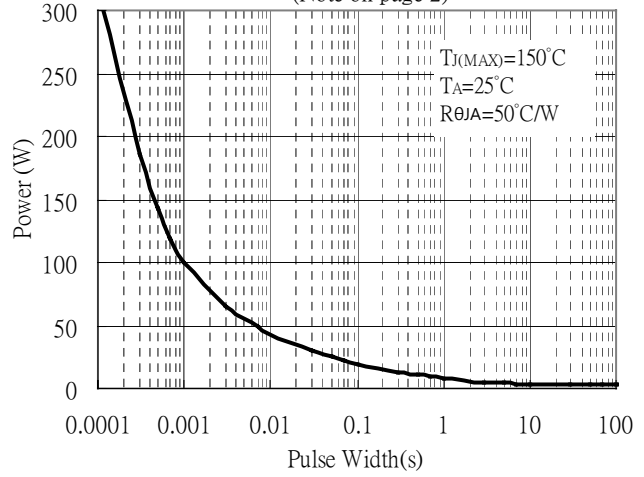


Typical Characteristics(Cont.)

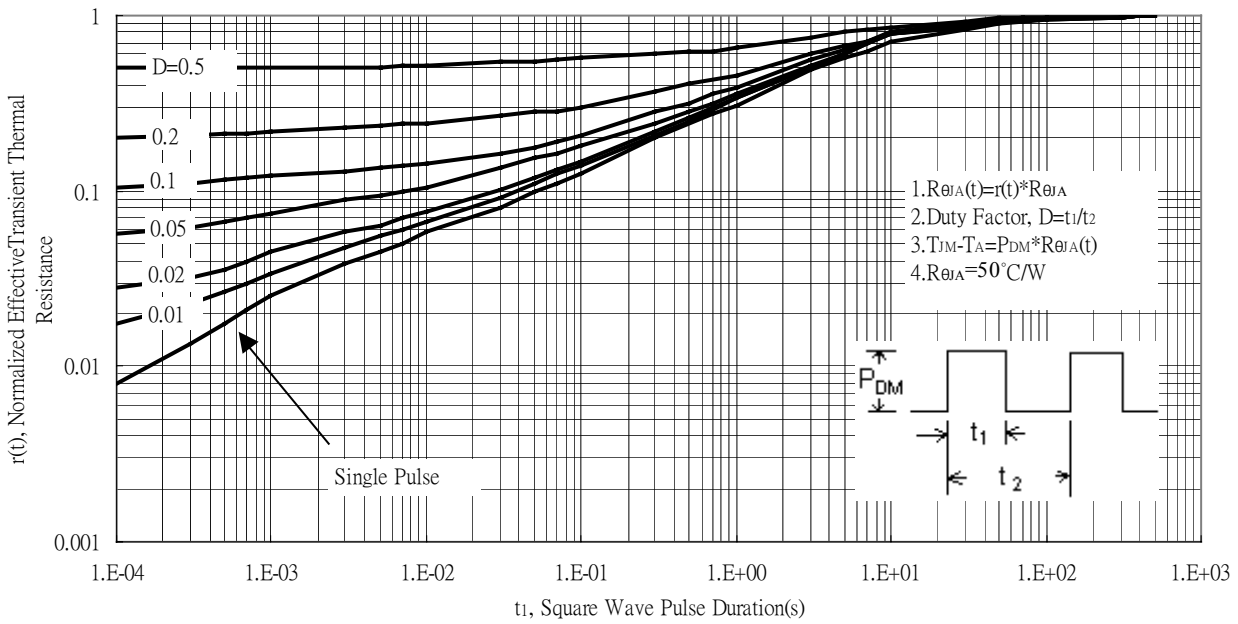
Typical Transfer Characteristics



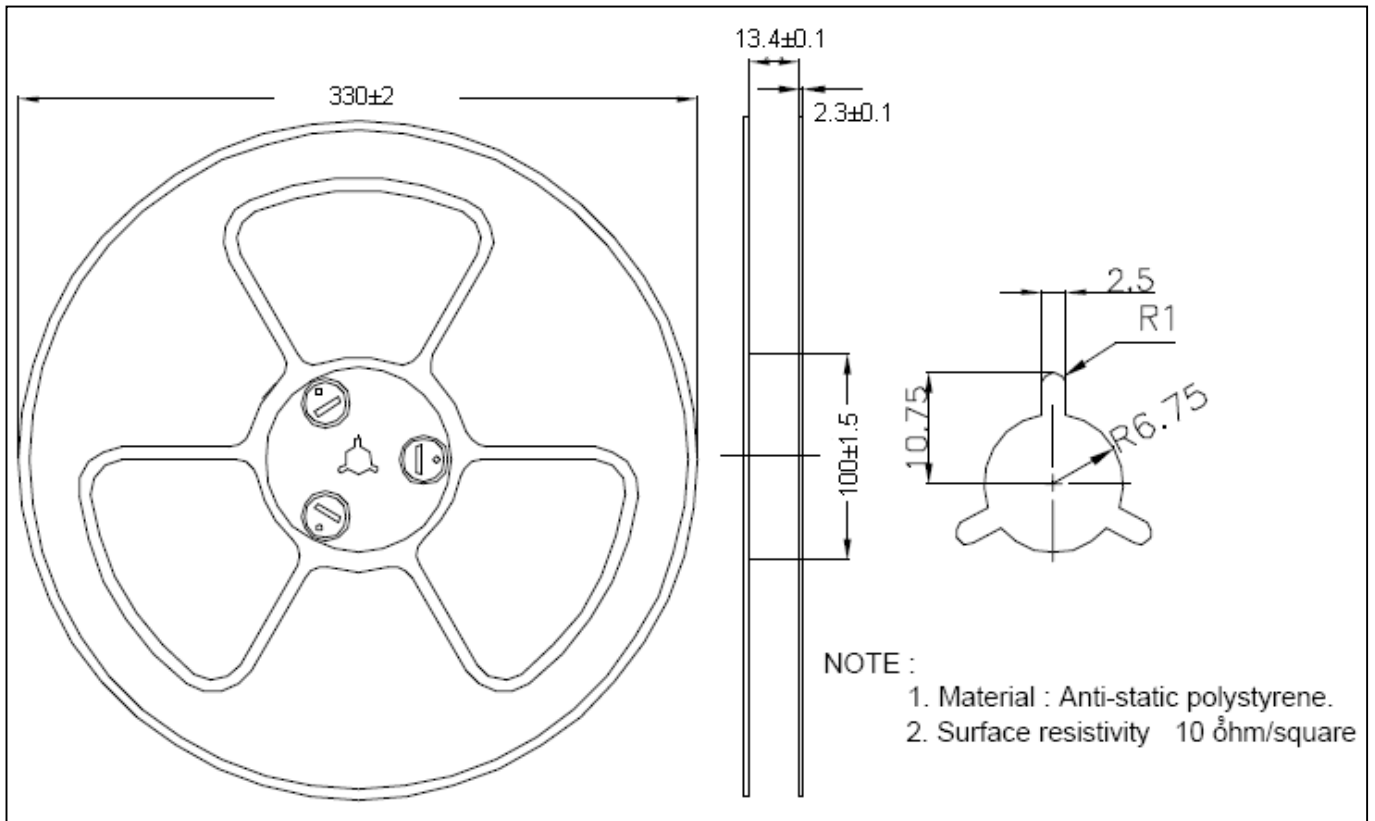
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



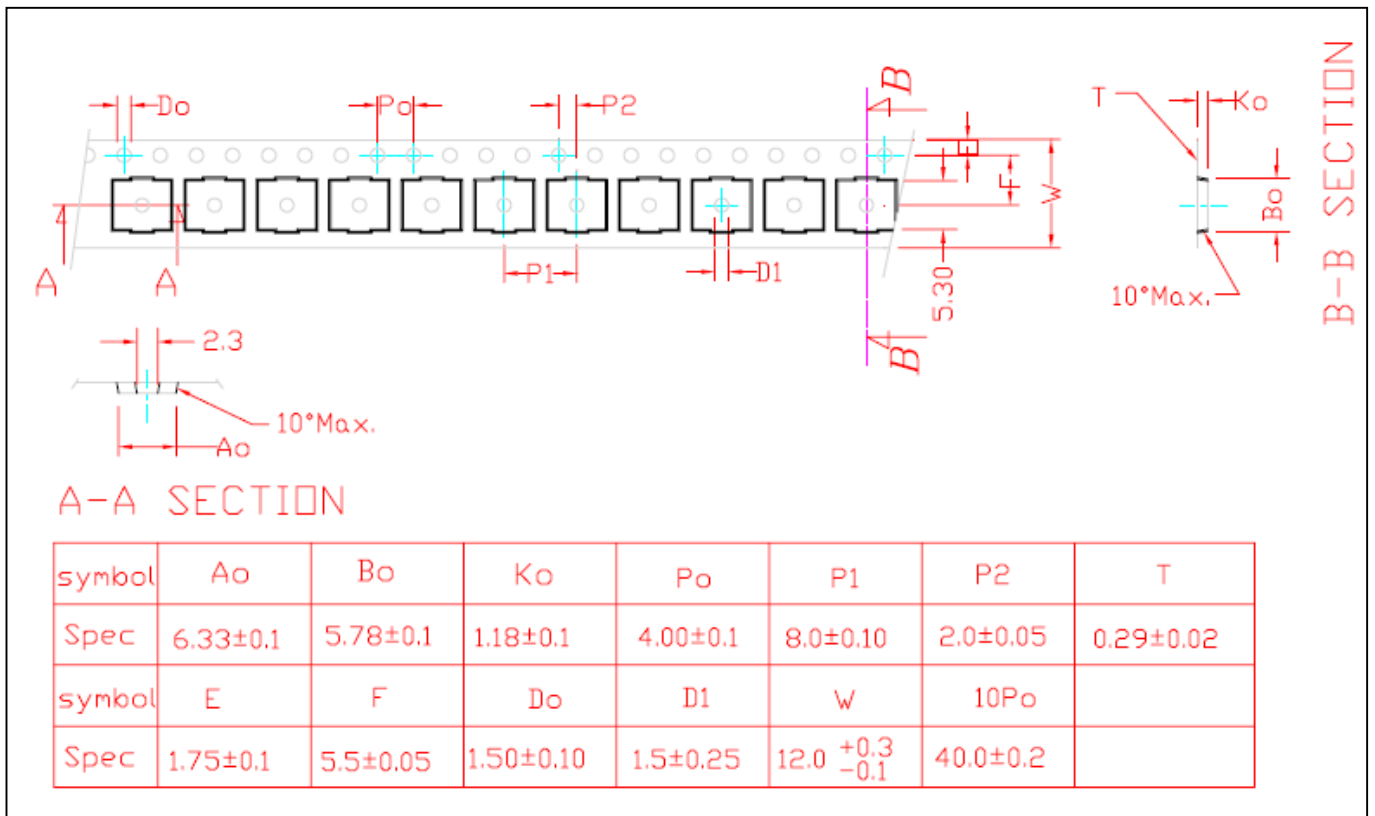
Transient Thermal Response Curves



Reel Dimension



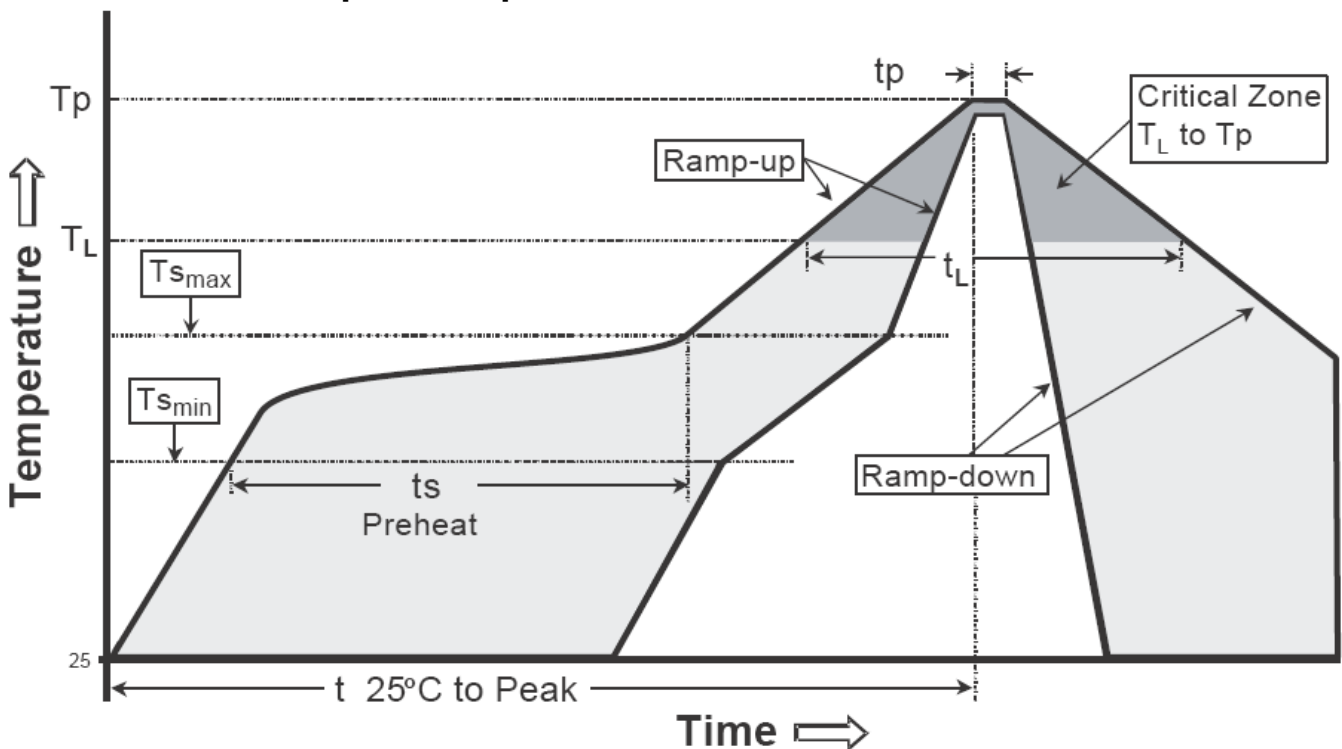
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

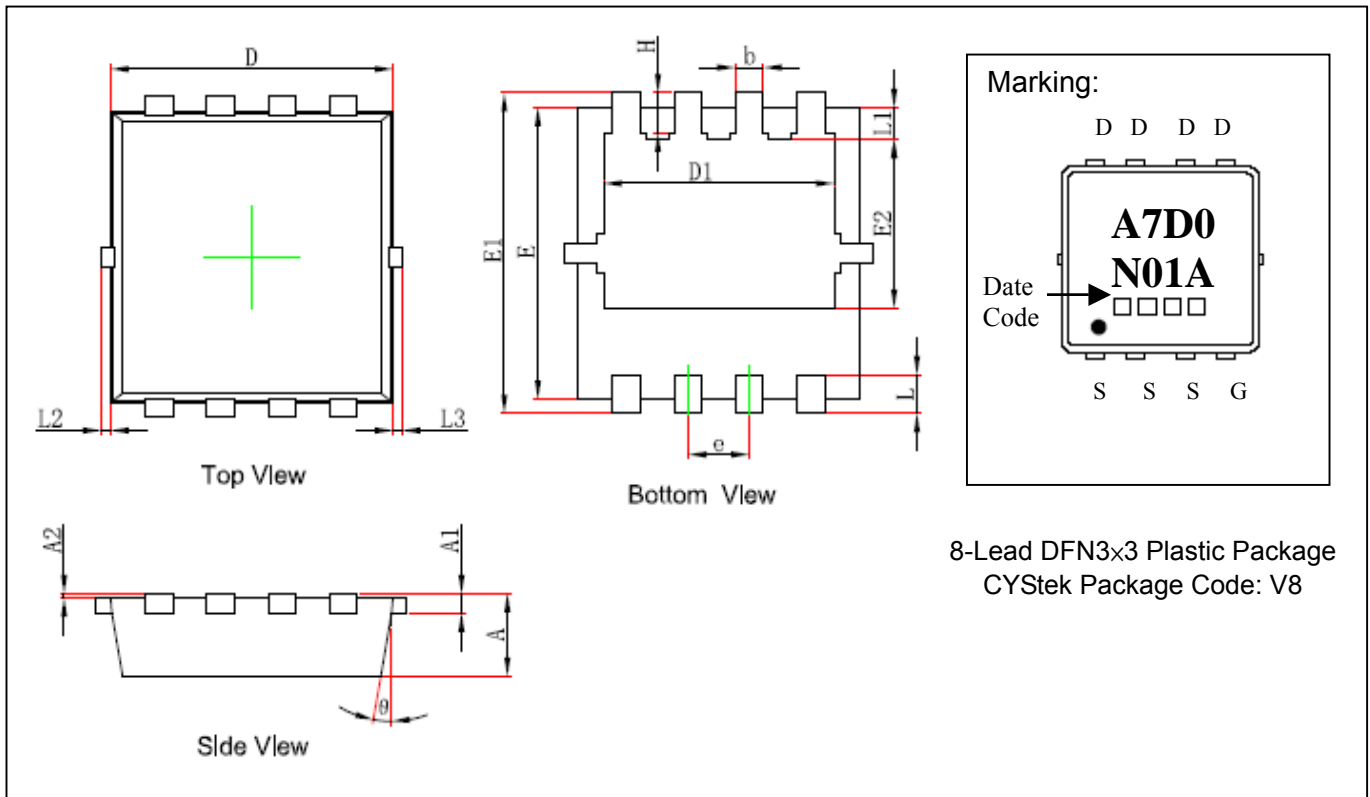
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Ts max to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN3x3 Dimension



*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.605	0.850	0.026	0.033	b	0.200	0.400	0.008	0.016
A1	0.152	REF	0.006	REF	e	0.550	0.750	0.022	0.030
A2	0.000	0.050	0.000	0.002	L	0.300	0.500	0.012	0.020
D	2.900	3.100	0.114	0.122	L1	0.180	0.480	0.007	0.019
D1	2.300	2.600	0.091	0.102	L2	0.000	0.100	0.000	0.004
E	2.900	3.100	0.114	0.122	L3	0.000	0.100	0.000	0.004
E1	3.150	3.450	0.124	0.136	H	0.315	0.515	0.012	0.020
E2	1.535	1.935	0.060	0.076	θ	9°	13°	9°	13°

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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