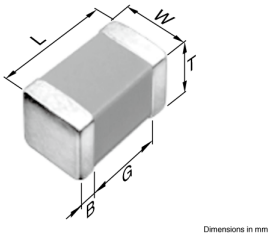
Creation Date : February 06, 2017 (GMT)

Multilayer Ceramic Chip Capacitors

C1608C0G2A101G080AA

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C1608C0G2A101GT****
Applications	Commercial Grade
Feature	Mid Voltage (100 to 630V)
Series	C1608 [EIA 0603]
Status	Production (Not Recommended for New Design)



Size	
Length(L)	1.60mm ±0.10mm
Width(W)	0.80mm ±0.10mm
Thickness(T)	0.80mm ±0.10mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering) 0.60mm to 0.80mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	100pF ±2%
Rated Voltage	100VDC
Temperature Characteristic	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

Other	
Soldering Method	Wave (Flow) Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

! Images are for reference only and show exemplary products.
! This PDF document was created based on the data listed on the TDK Corporation website.
! All specifications are subject to change without notice.

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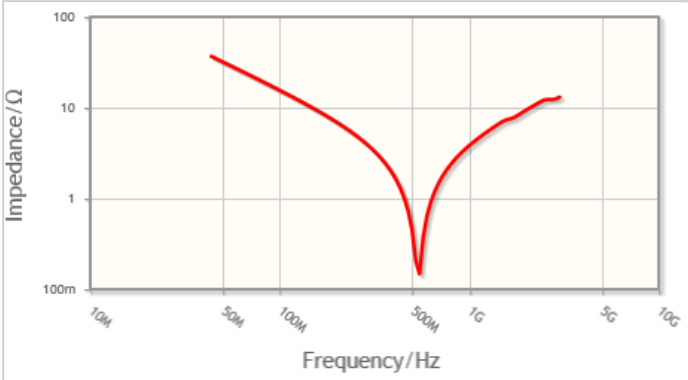
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



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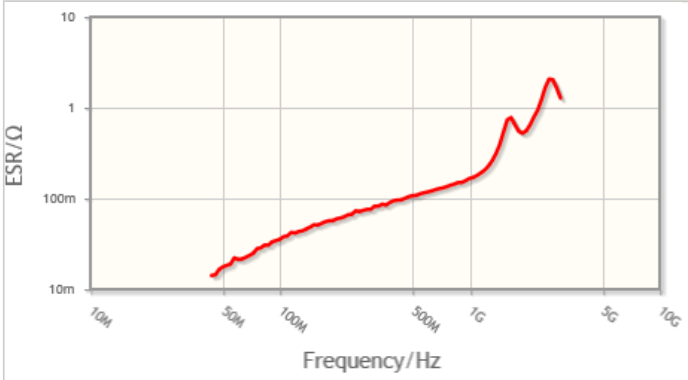
ESR



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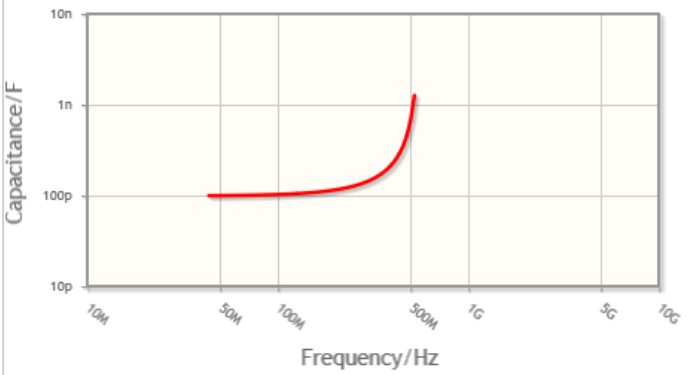
Capacitance



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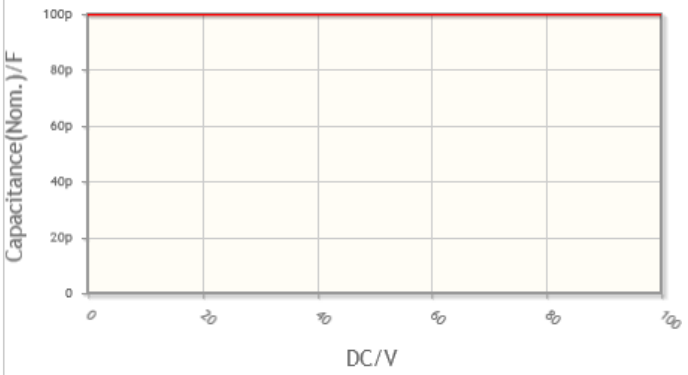
DC Bias Characteristic



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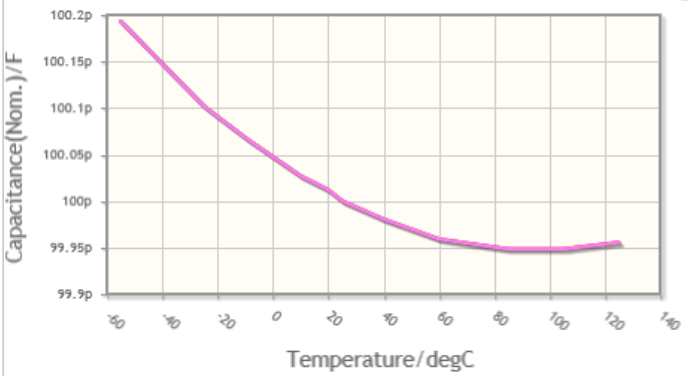
Temperature Characteristic



C1608C0G2A101G080AA(No Bias) C1608C0G2A101G080AA(DC Bias = 50V)

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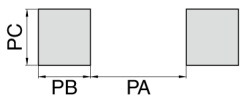
Associated Images

Land Pattern (Terminal Connection)



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